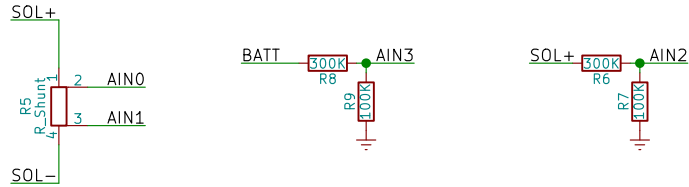
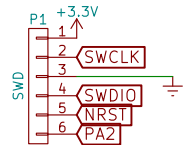


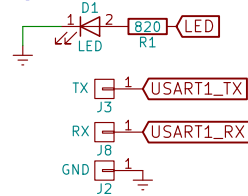
ADC Inputs



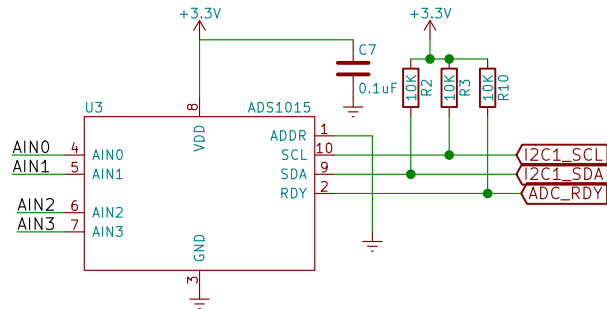
Programming Header



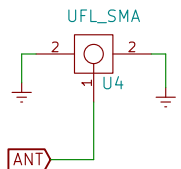
Debug



ADC

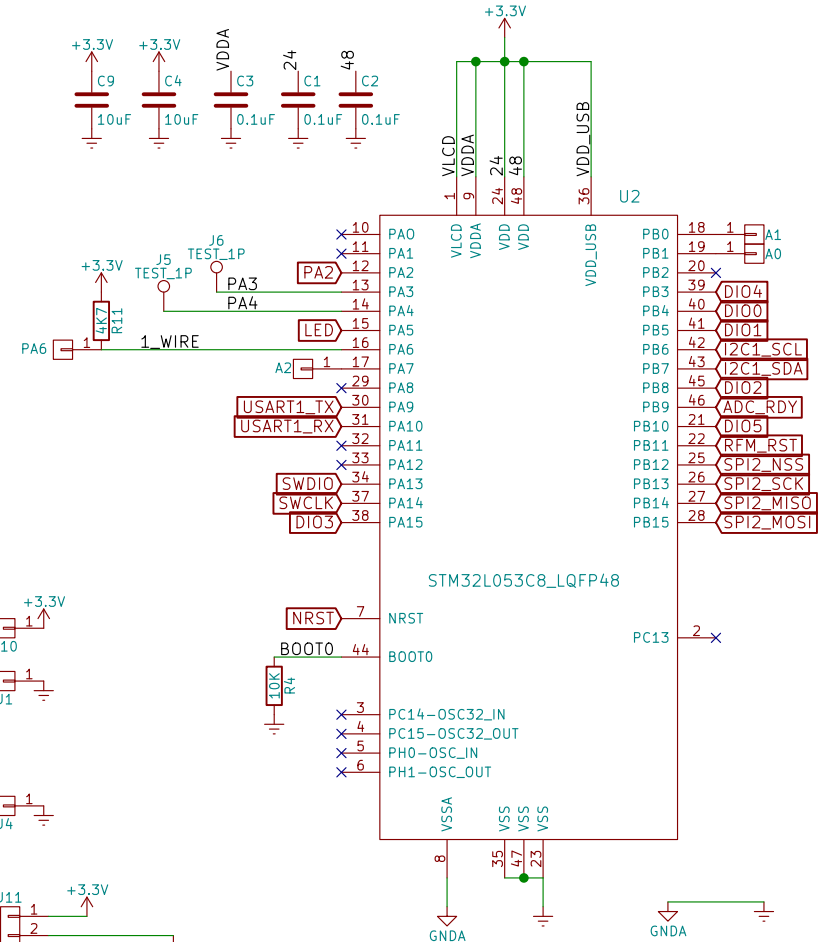
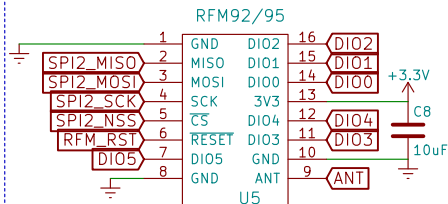


Antenna



Place on Top layer

RFM 95/96/92 Module



Sheet: /

File: stm32l053c.sch

Title: LoRa Garden Solar

Size: A4

Date:

KiCad E.D.A. kicad 4.0.7-e2-637658ubuntu16.04.1

Rev: v1.0.0

Id: 1/1