

## 1. Description

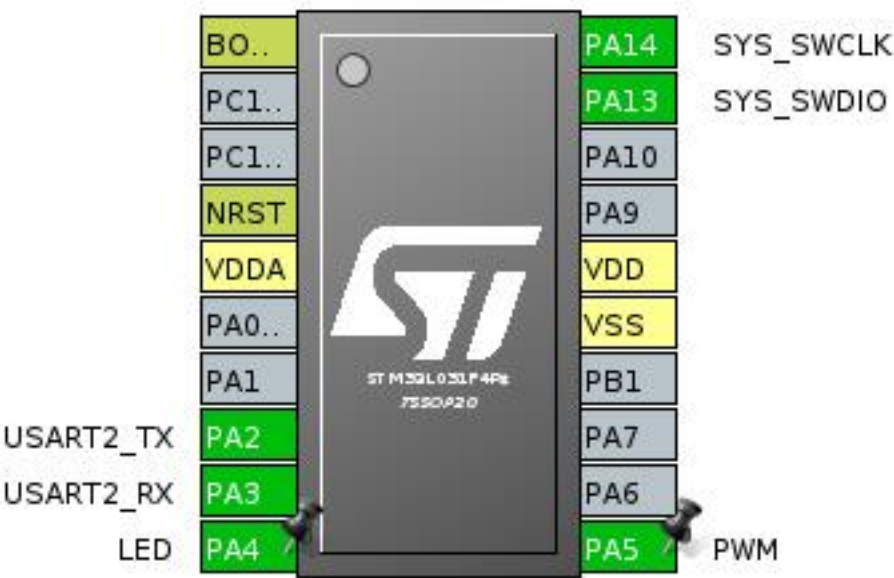
### 1.1. Project

Project Name	charge-controller
Board Name	custom
Generated with:	STM32CubeMX 5.0.0
Date	01/23/2019

### 1.2. MCU

MCU Series	STM32L0
MCU Line	STM32L0x1
MCU name	STM32L031F4Px
MCU Package	TSSOP20
MCU Pin number	20

## 2. Pinout Configuration

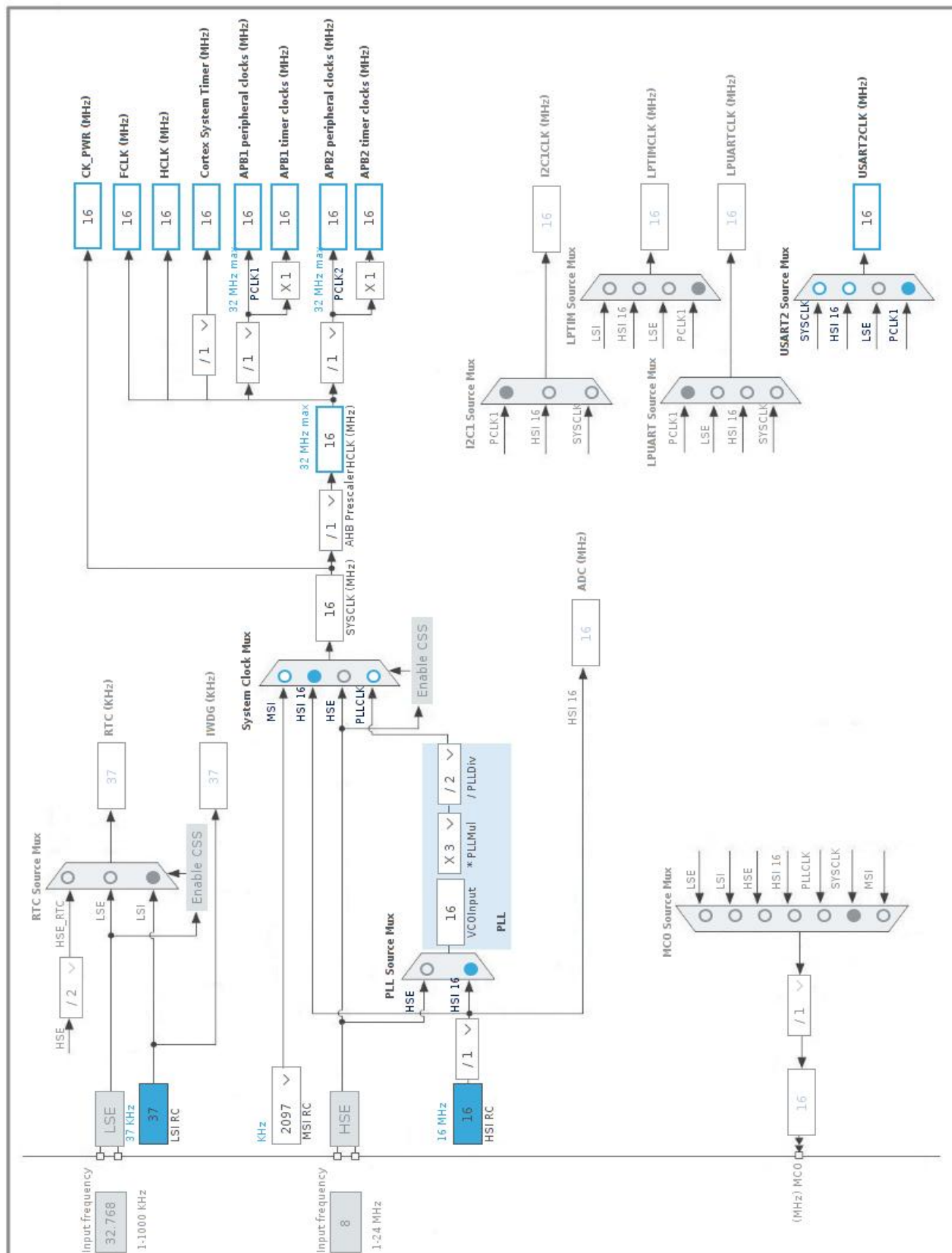


### 3. Pins Configuration

Pin Number TSSOP20	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	BOOT0	Boot		
4	NRST	Reset		
5	VDDA	Power		
8	PA2	I/O	USART2_TX	
9	PA3	I/O	USART2_RX	
10	PA4 *	I/O	GPIO_Output	LED
11	PA5	I/O	TIM2_CH1	PWM
15	VSS	Power		
16	VDD	Power		
19	PA13	I/O	SYS_SWDIO	
20	PA14	I/O	SYS_SWCLK	

\* The pin is affected with an I/O function

## 4. Clock Tree Configuration



## 5. Software Project

### 5.1. Project Settings

Name	Value
Project Name	charge-controller
Project Folder	/home/peter/repos/pwm-charge-controller/stm32/charge-controller
Toolchain / IDE	SW4STM32
Firmware Package Name and Version	STM32Cube FW_L0 V1.11.0

### 5.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	Yes

## 6. Power Consumption Calculator report

### 6.1. Microcontroller Selection

Series	STM32L0
Line	STM32L0x1
MCU	STM32L031F4Px
Datasheet	027063_Rev4

### 6.2. Parameter Selection

Temperature	25
Vdd	3.0

## 7. IPs and Middleware Configuration

### 7.1. SYS

**mode:** Debug Serial Wire

**Timebase Source:** SysTick

### 7.2. TIM2

**Clock Source :** Internal Clock

**Channel1:** PWM Generation CH1

#### 7.2.1. Parameter Settings:

##### Counter Settings:

Prescaler (PSC - 16 bits value)	<b>1000 *</b>
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value )	<b>4096 *</b>
Internal Clock Division (CKD)	No Division
auto-reload preload	Disable

##### Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection	Reset (UG bit from TIMx_EGR)

##### PWM Generation Channel 1:

Mode	PWM mode 1
Pulse (16 bits value)	<b>127 *</b>
Fast Mode	Disable
CH Polarity	High

### 7.3. USART2

**Mode:** Asynchronous

#### 7.3.1. Parameter Settings:

##### Basic Parameters:

Baud Rate	<b>9600 *</b>
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

##### Advanced Parameters:

Data Direction	Receive and Transmit
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Over Sampling	16 Samples
Single Sample	Disable

**Advanced Features:**

Auto Baudrate	Disable
TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX Pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

\* **User modified value**



## 8. System Configuration

### 8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
SYS	PA13	SYS_SWDIO	n/a	n/a	n/a	
	PA14	SYS_SWCLK	n/a	n/a	n/a	
TIM2	PA5	TIM2_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low	PWM
USART2	PA2	USART2_TX	Alternate Function Push Pull	No pull-up and no pull-down	<b>Very High</b> *	
	PA3	USART2_RX	Alternate Function Push Pull	No pull-up and no pull-down	<b>Very High</b> *	
GPIO	PA4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED

### 8.2. DMA configuration

nothing configured in DMA service

### 8.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable Interrupt	true	0	0
Hard fault interrupt	true	0	0
System service call via SWI instruction	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash and EEPROM global interrupt	unused		
RCC global interrupt	unused		
TIM2 global interrupt	unused		
USART2 global interrupt / USART2 wake-up interrupt through EXTI line 26	unused		

\* User modified value

## ***9. Software Pack Report***