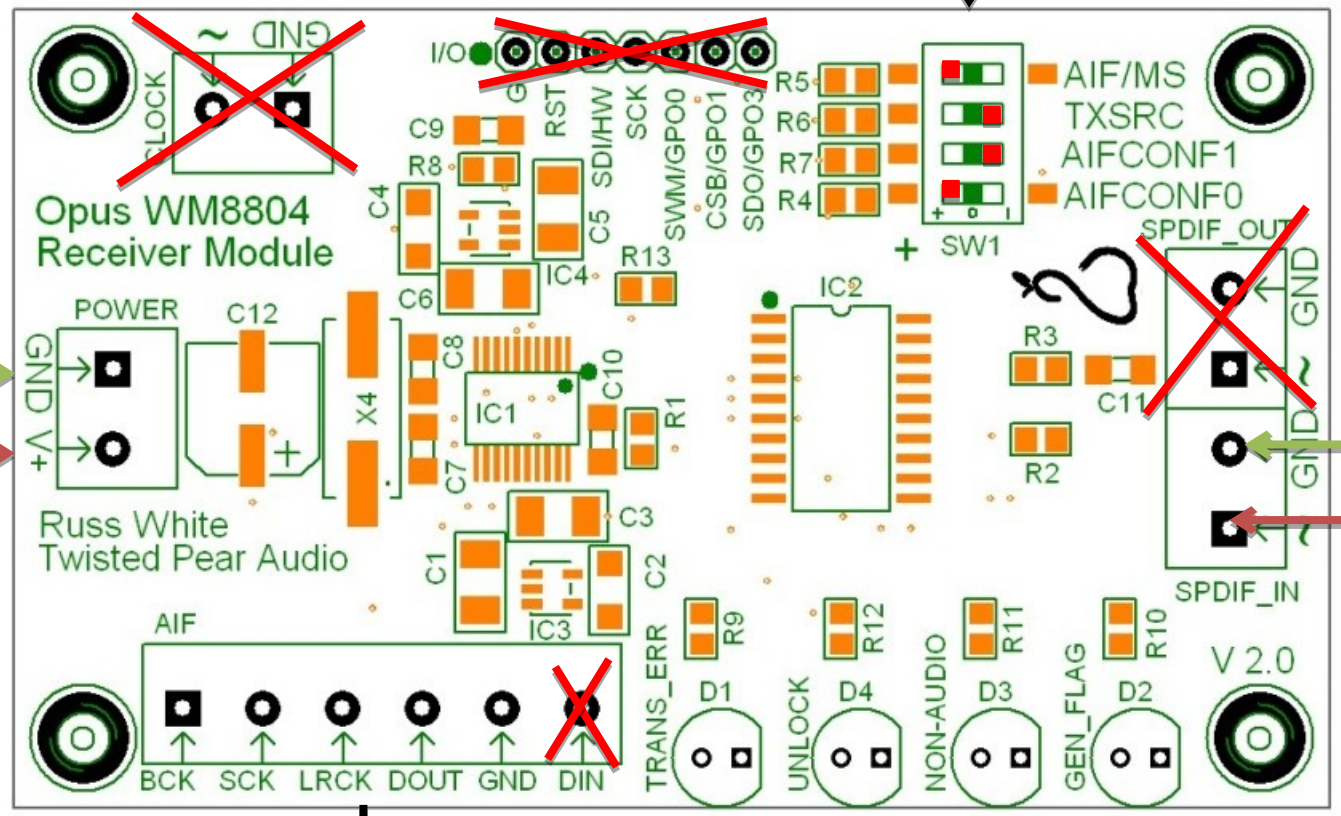
Recommended SPDIF IN/SPDIF+PCM OUT Configuration:

AIF/MS	1
TXSRC	0
AIFCONF1	0
AIFCONF0	1

+ = 1
- = 0
O = open



7.5VDC

SPDIF in

To Metronome

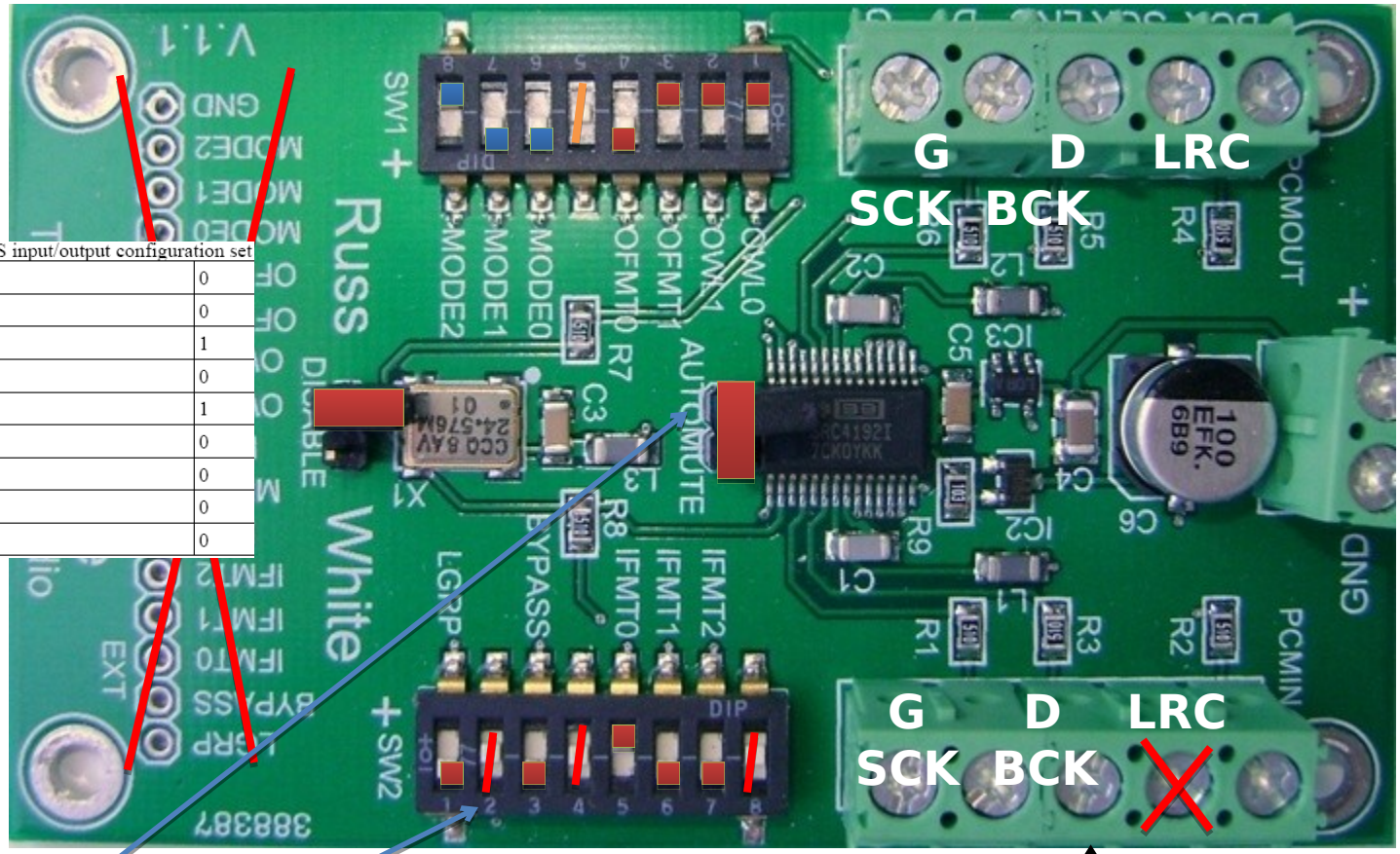
PCM Input/Output:
PCM is output and input on the AIF terminal block. BCK is the bit clock. SCK is the system or master clock. LRCK is the LEFT/RIGHT clock. DOUT is the PCM output signal. GND is digital GND. DIN is PCM input. PCM input/output as configured above will be 24-bit I2S, See the data sheet for detail.

Switch Settings:
For the recommended configuration of I2S PCM input and 24-bit I2S output in master mode set the tristate MODE switches as follows:

	128fs(192khz)	256fs(96khz)	512fs(48khz)
MODE2	0	0	0
MODE1	0	1	1
MODE0	1	1	0

To OPUS

+ = 1
- = 0
O = open



7.5VDC

To set the recommended I2S input/output configuration set

IFMT2	0
IFMT1	0
IFMT0	1
OFMT1	0
OFMT0	1
OWL1	0
OWL0	0
BYPASS	0
LGRP	0

The “AUTOMUTE” jumper connects the RDY pin to the MUTE pin to mute the output when the SRC4192 is not in a ready state. It is recommended you leave all unused switches in the open (center) position.

From OPUS Receiver

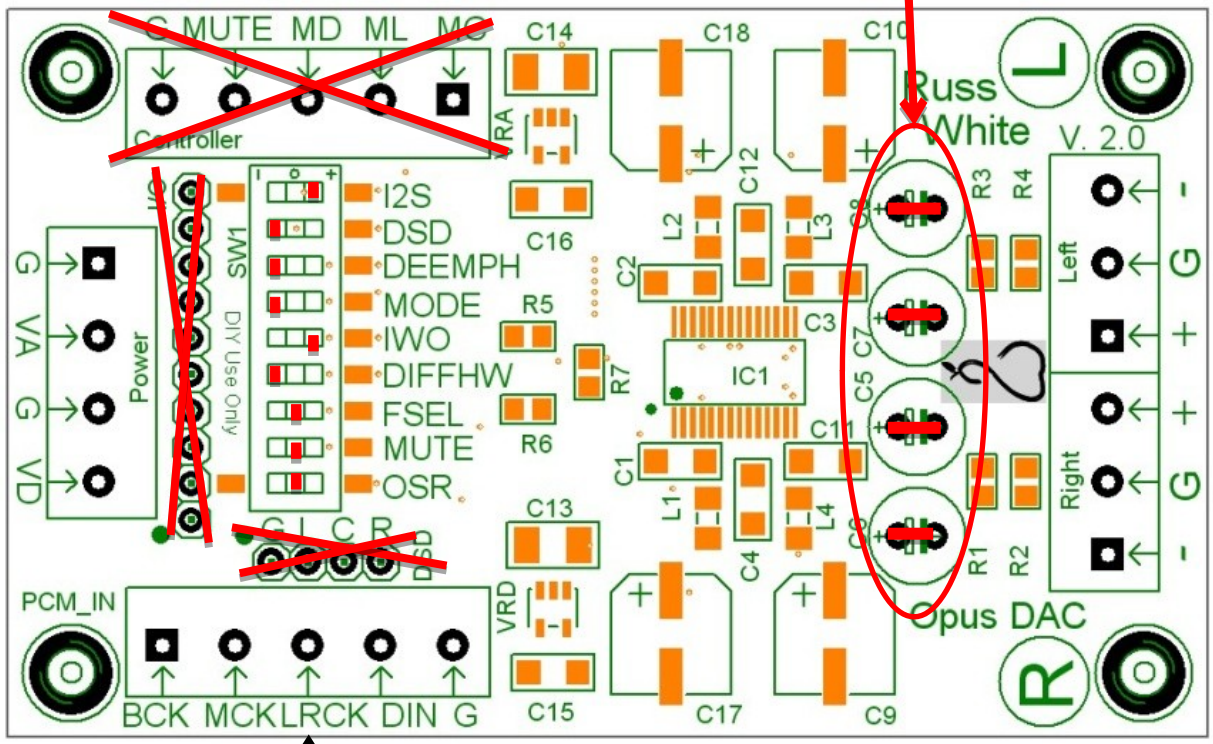
PCM Input/output:
PCM (I2S as shown above) is input via the PCMIN and PCMOUT terminal blocks. BCK is the bit clock. SCK is the system, or reference clock. LRC is the LEFT/RIGHT clock. D is the PCM data input. GND is digital GND. **IMPORTANT NOTE: SCK is not normally used on PCMIN.**

Pin Names:

PIN(number) WM8741	WM8740
DSD(27)	DM1
DEEMPH(28)	DM0
FSEL(4)	MODE8X
OSR(22)	RESETB

+ = 1
- = 0
O = open

2 x 7.5 VDC

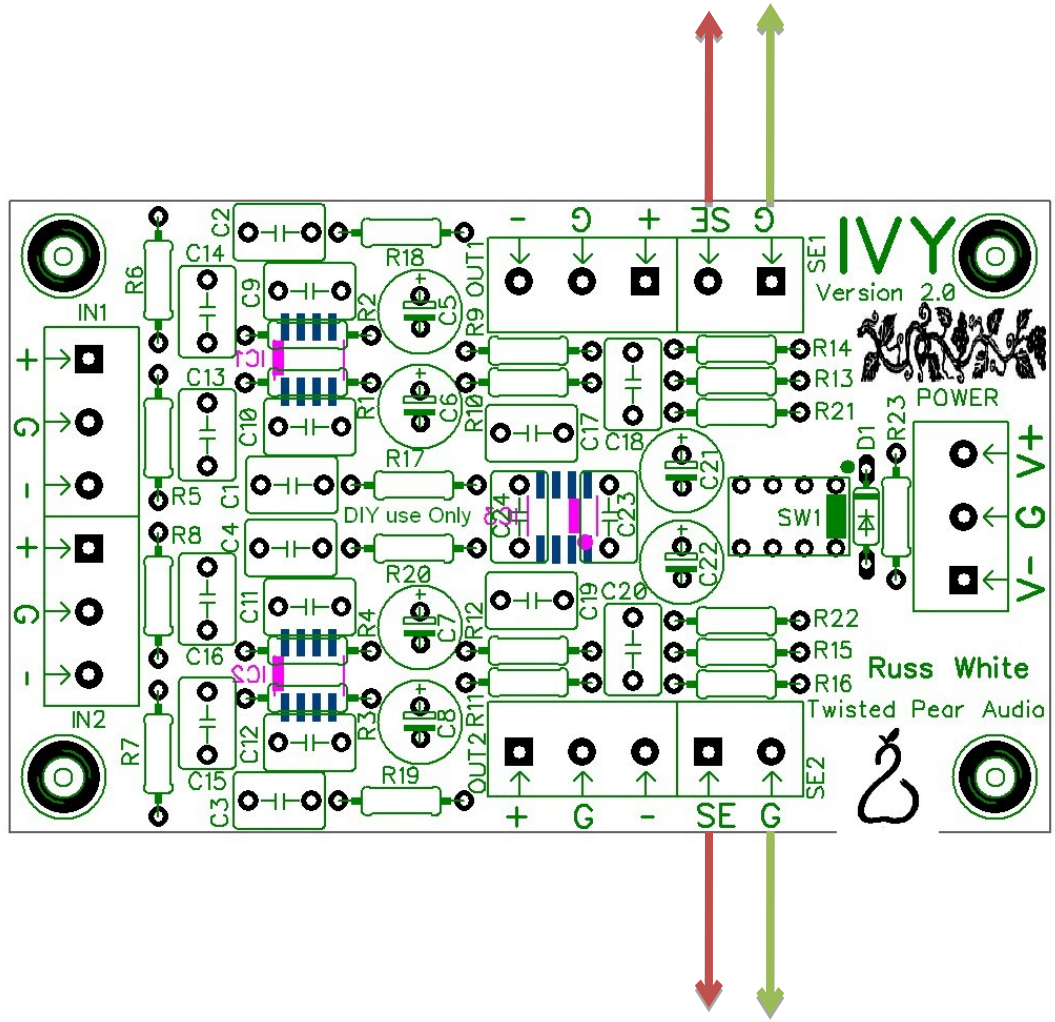


Recommended Stereo Configuration:

I2S	1
DSD(DM1)	0
DEEMPH(DM0)	0
MODE	0
IWO	1
DIFFHW	0
FSEL(MODE8X)	Open
MUTE	Open
OSR(RESETB)	Open

From

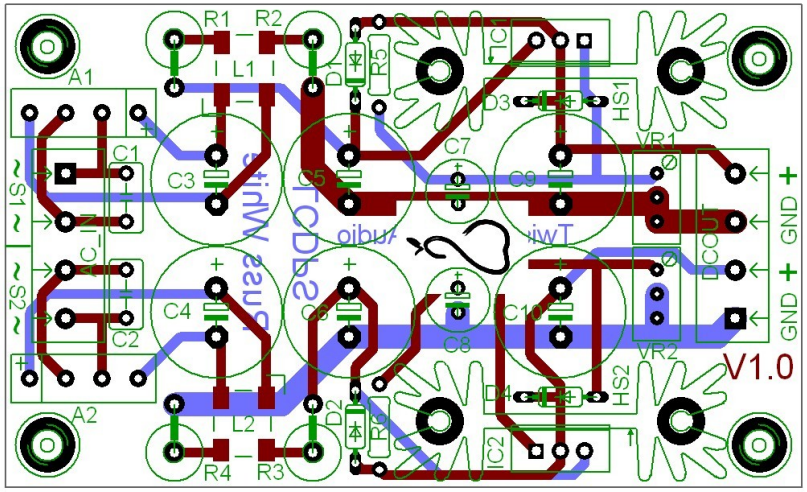
PCM Input:
PCM (I2S as shown above) is input via the PCM_IN terminal block. BCK is the bit clock. MCK is the system, or master clock. LRCK is the LEFT/RIGHT clock. DIN is the PCM data input. G is digital GND.

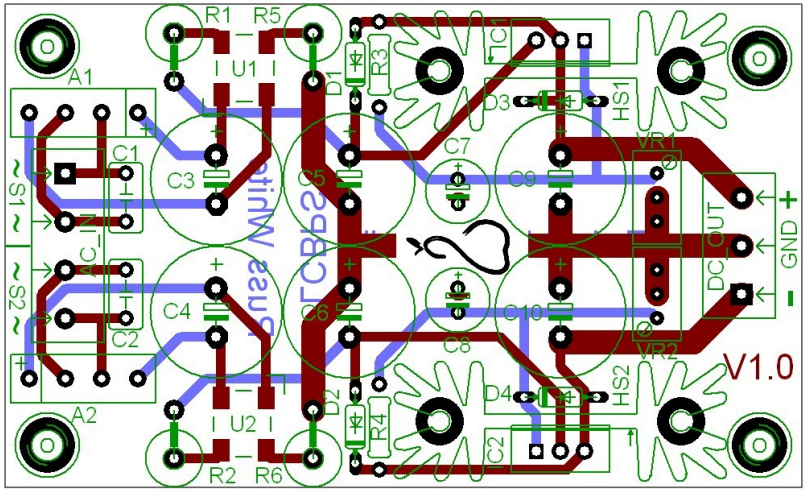


From Opus

+/- 15 VDC

For 2Vrms output
C1-4 1nf
R17-20 Jumper
R1-4 2K
R5-8 2K
C13-16 Omit


R1
R2
L1
A1
C1
C3
S1
S2
C4
C2
A2
R4
R3
L2
D1
D2
R5
R6
C5
C6
IC1
IC2
D3
D4
C7
C8
C9
C10
HS1
HS2
VR1
VR2
DC OUT
GND
GND
V1.0


R1
R5
U1
A1
C1
C3
S1
S2
C4
C2
A2
R2
R6
U2
D1
D2
R3
R4
C5
C6
IC1
IC2
D3
D4
C7
C8
C9
C10
HS1
HS2
VR1
VR2
DC OUT
GND
V1.0