# TOUSIF RAZA

B. Tech Electronics and Communication Engineering Student
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## Summary

Dedicated B. Tech Electronics and Communication Engineering student with a Diploma in Electrical Engineering. Skilled in VLSI Cadence Virtuoso, Xilinx Vivado, Ki-Cad, IoT, Linux, C and Java. Passionate about bridging technology with real-world applications and committed to continuous learning.

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## Experience

**AI and Robotics Trainer (Embedded System) | Future Corp Jamshedpur**          *March 2025 - Present*
•         Facilitating upskilling in AI, robotics, and embedded systems through structured training modules and project-based learning.
**VLSI Design Internship | NIT Jamshedpur**          *May 2024 - July 2024*
• Gained hands-on experience in VLSI design using Cadence Virtuoso and Xilinx Vivado.
**Quality Assurance Engineer Trainee | ZF Group (QAD)**          *May 2022 - Oct 2022*
• Conducted product manufacturing process inspection and problem-solving in Vehicle Dynamic Control.

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## Education

**B. Tech in ECE | RVSCET, Jamshedpur**          *8.16 GPA – Agg. | Nov 2022 - July 2025*
**Diploma in Electrical Engineering | Al-Kabir Polytechnic, Jamshedpur.**          *82.88% | Aug 2018 - Oct 2021* **X (CBSE) | Sardar Patel Public School, Bokaro**          *75% | 2018*

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## Projects

• Sahayika AI (Raspberry Pi 4) | Github
  Developed an AI-powered assistant using Raspberry Pi 4 for automation and intelligent interaction.
• Portable Eco Bot (ESP32) | Github
  Designed a portable eco-friendly robotic system using ESP32 for environmental monitoring and smart applications.
**Smart Bridge Using Arduino UNO | GitHub**
• Engineered a bridge with an ultrasonic sensor and Arduino Uno for flood detection and response.

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## Skills

VLSI Layout Design Basic Level, Linux, C, Java, Ki-Cad IoT Applications

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## Publications

1.  S. Mahanty, S. Kumar, A. Kumar, P. Kumari, **T. Raza** and S. Kumar, "*Design and Implementation of AllOptical Logic Gates Based On XPM Effect In A Phase-Shifted Fiber Bragg Grating*," 2024 5th ICRTCST, Jamshedpur, India, 2024, pp. 573-578, DOI: 10.1109/ICRTCST61793.2024.10578368

2.  S. Mahanty, S. Kumar, A. Kumar, P. Kumari, **T. Raza** and S. Kumar, "*Design and Implementation of AllOptical Decoder Using Phase-Shifted Fiber Bragg Grating*," 2024 5th ICRTCST, Jamshedpur, India, 2024, pp. 579- 584, DOI: 10.1109/ICRTCST61793.2024.10578374

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## Achievement

• Best Paper Award at IEEE conference.
• "Best Tutor" during 5-day IoT program at RVSCET.

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## Volunteering

**Co-Chair | Learning Vertical | Young Indians | XLRI Jamshedpur**

• Organized workshops and industrial visits.
**Tutor & Member | Circuitron Club | RVSCET Jamshedpur**

• Introduced an IoT training program for juniors, focusing on IoT fundamentals and applications.