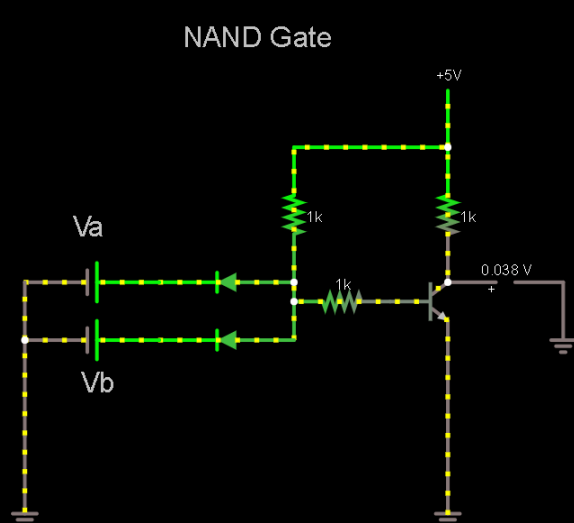
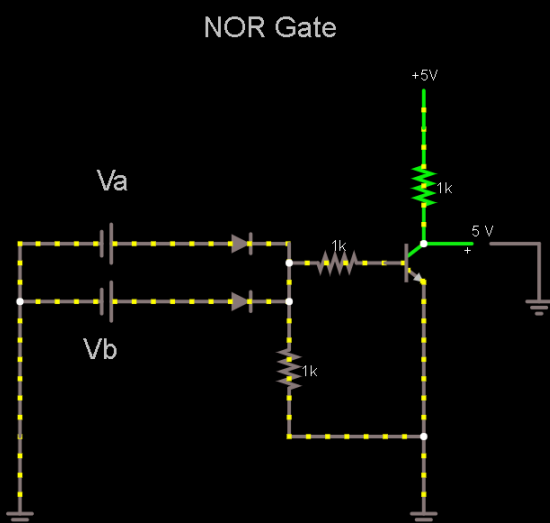
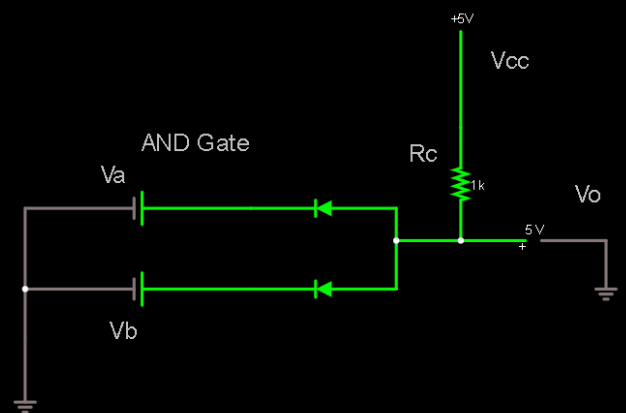
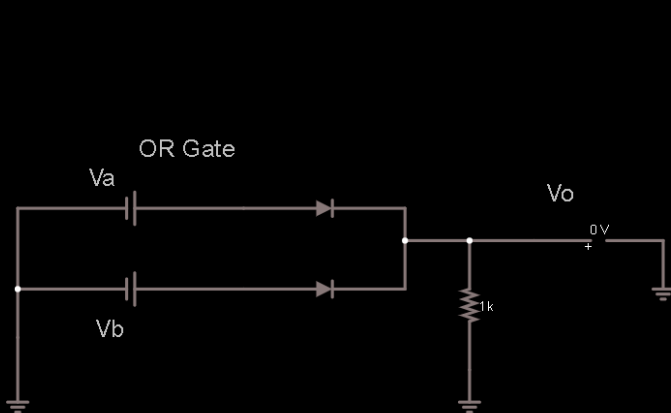
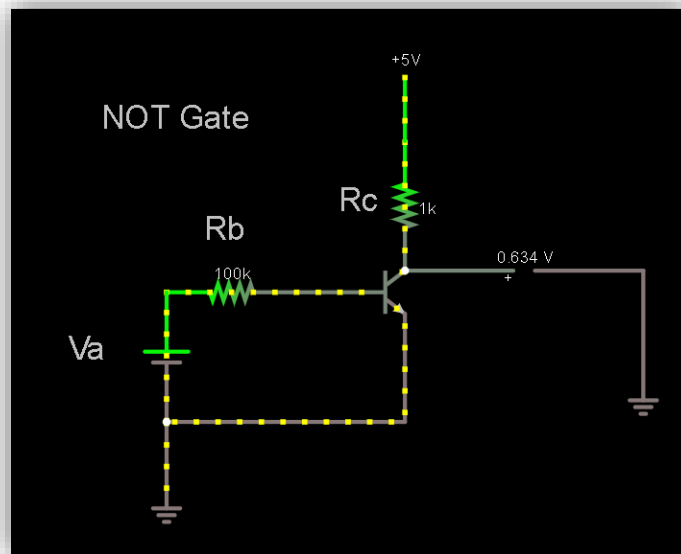


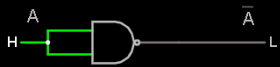
# Realization of Gates(Diodes and Transistors)



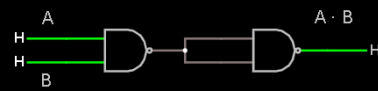
# Realization of Gates using IC7400

## Realization of Gates using NAND Gate

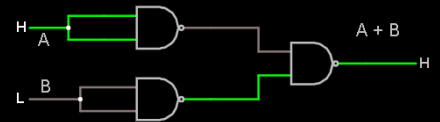
NOT Gate



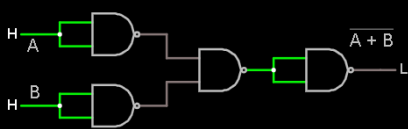
AND Gate



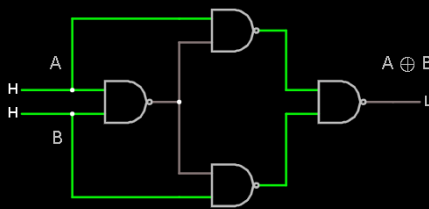
OR Gate



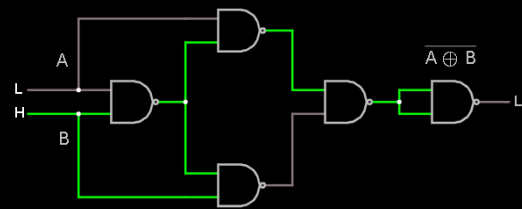
NOR Gate



XOR Gate



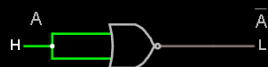
XNOR Gate



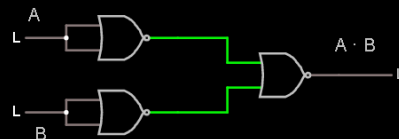
# Realization of Gates using IC7402

## Realization of Gates using NOR Gate

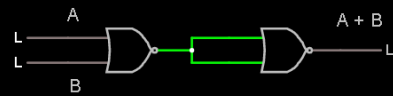
NOT Gate



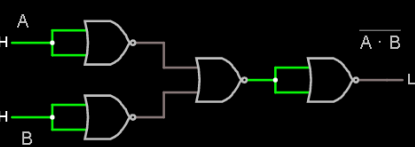
AND Gate



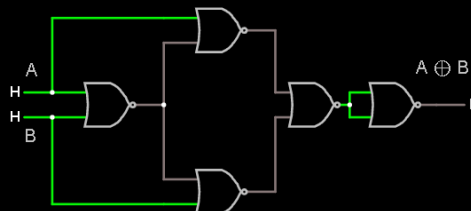
OR Gate



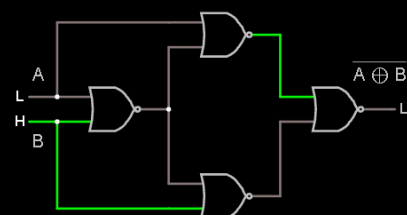
NAND Gate



XOR Gate

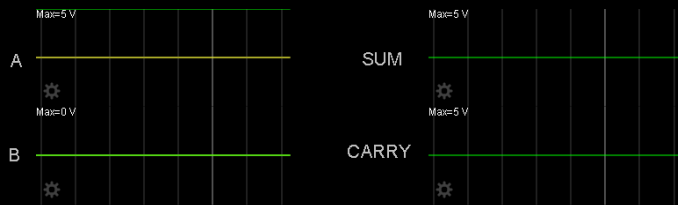
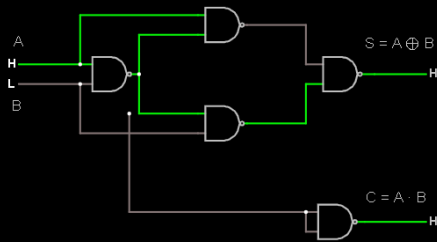


XNOR Gate

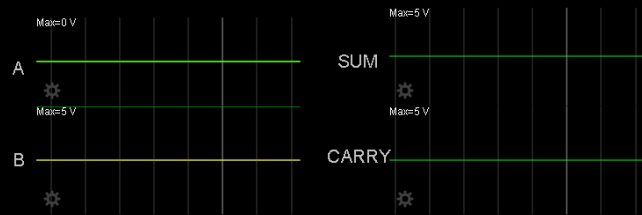
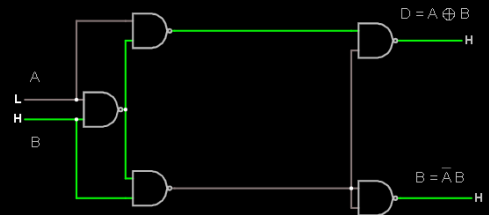


## Half Adder and Subtractor

Half Adder

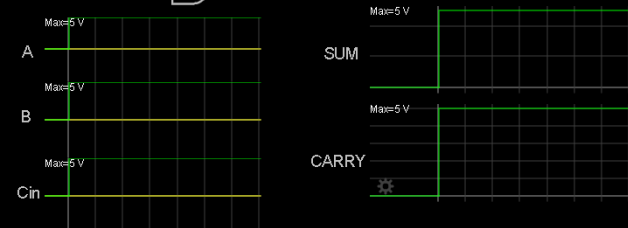
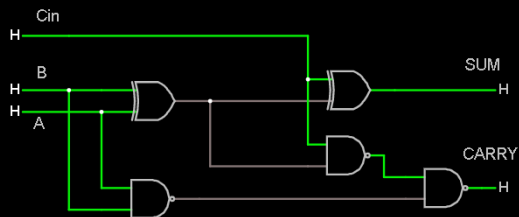


Half Subtractor

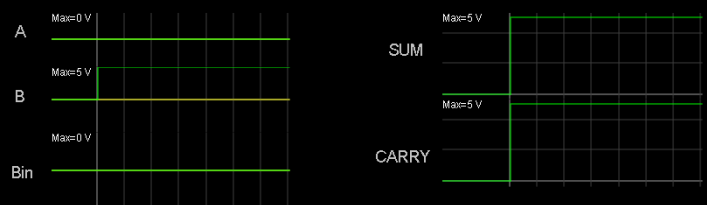
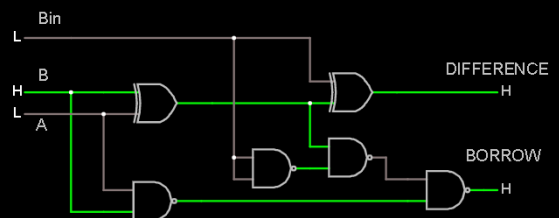


## Full Adder and Subtractor

Full Adder

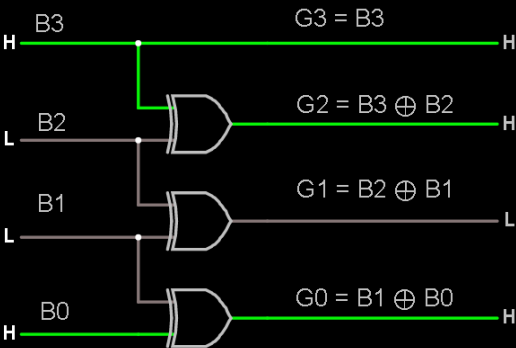


Full Subtractor

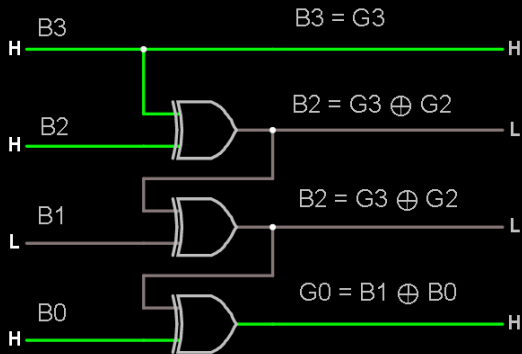


# Binary to Gray Code and Gray to Binary Code

Binary to Gray

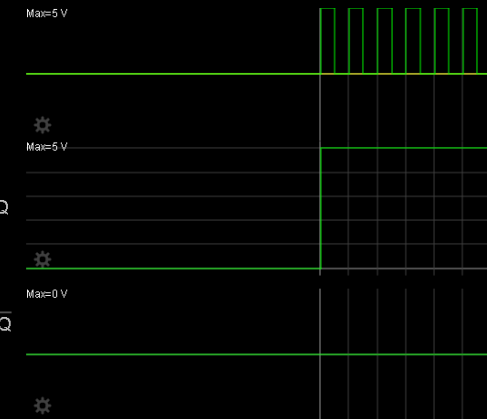
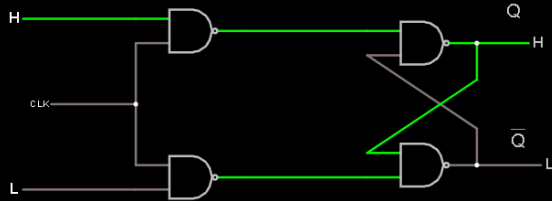


Gray to Binary

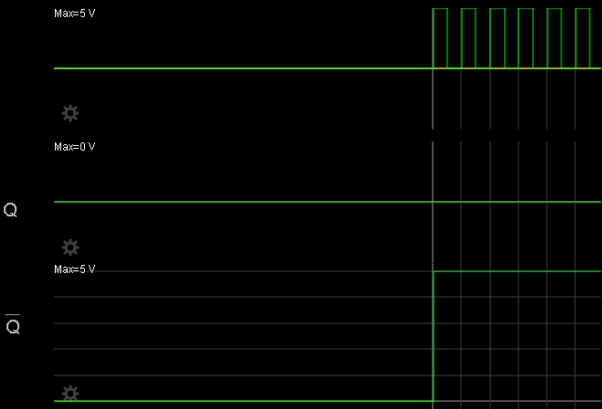
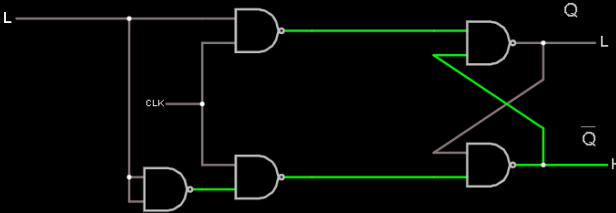


## RS and D Flip Flop

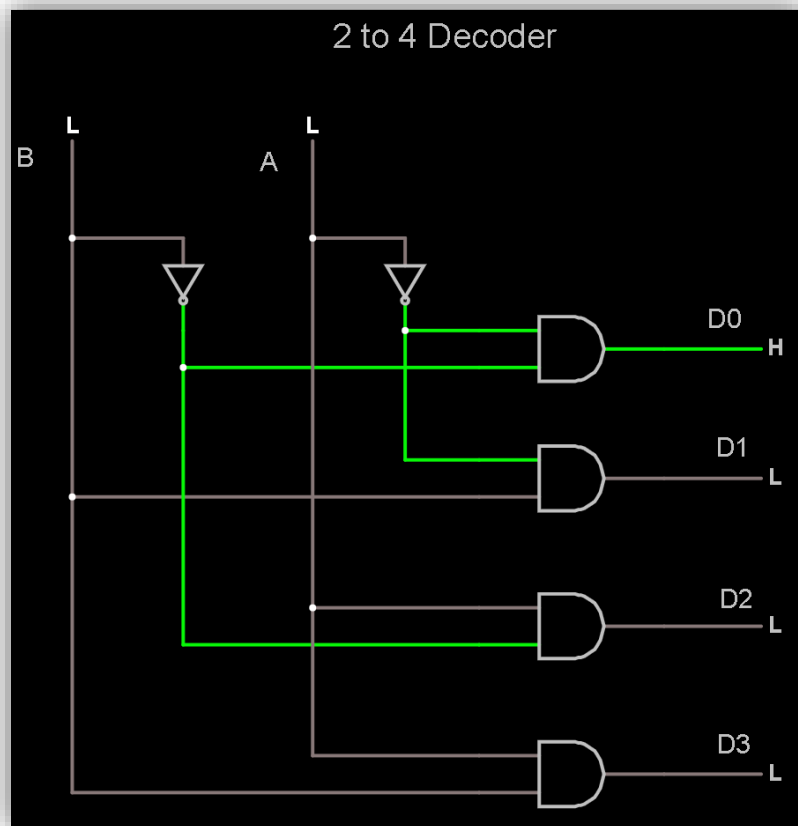
RS Flip Flop



D Flip Flop

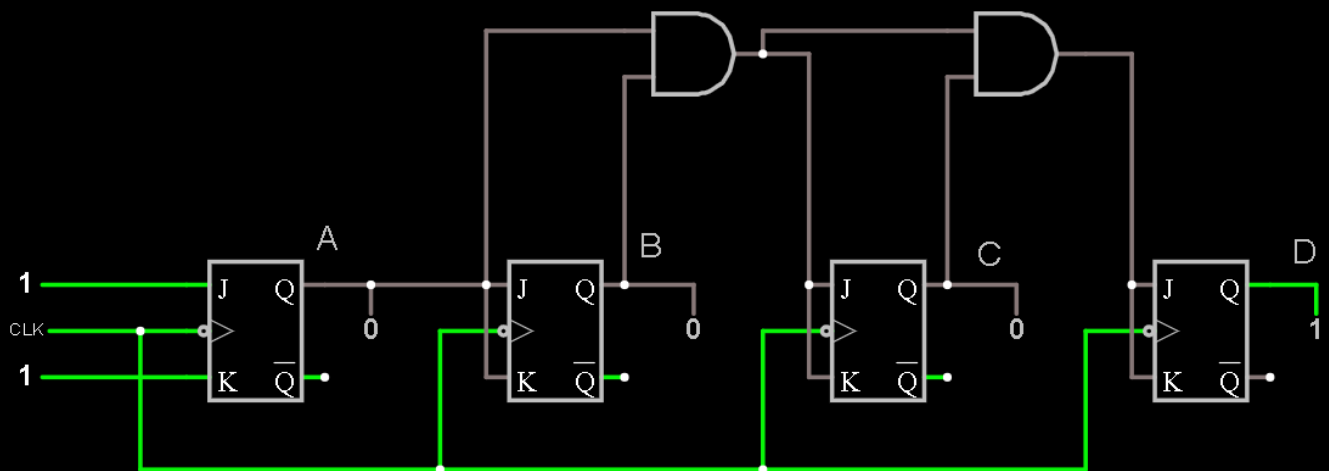


## 2 to 4 Decoder



## 4 bit Binary counter

4 bit Binary Counter using J-K Flip Flop

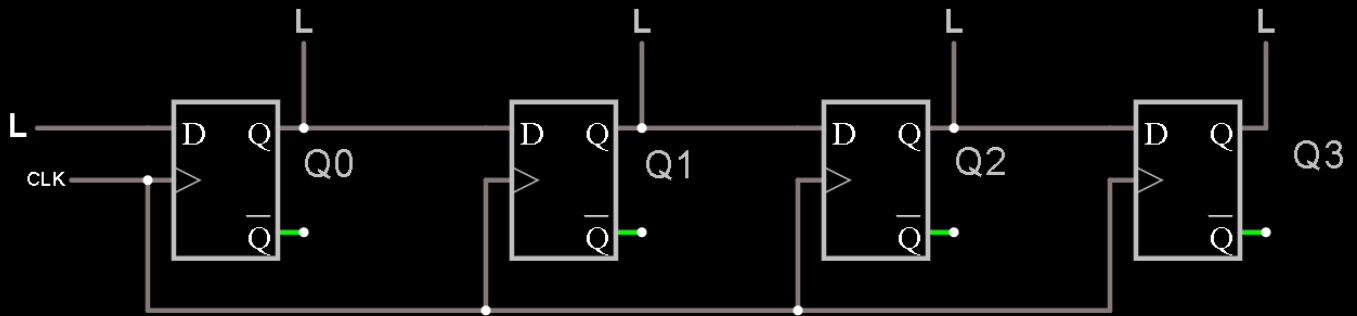


Max=5 V



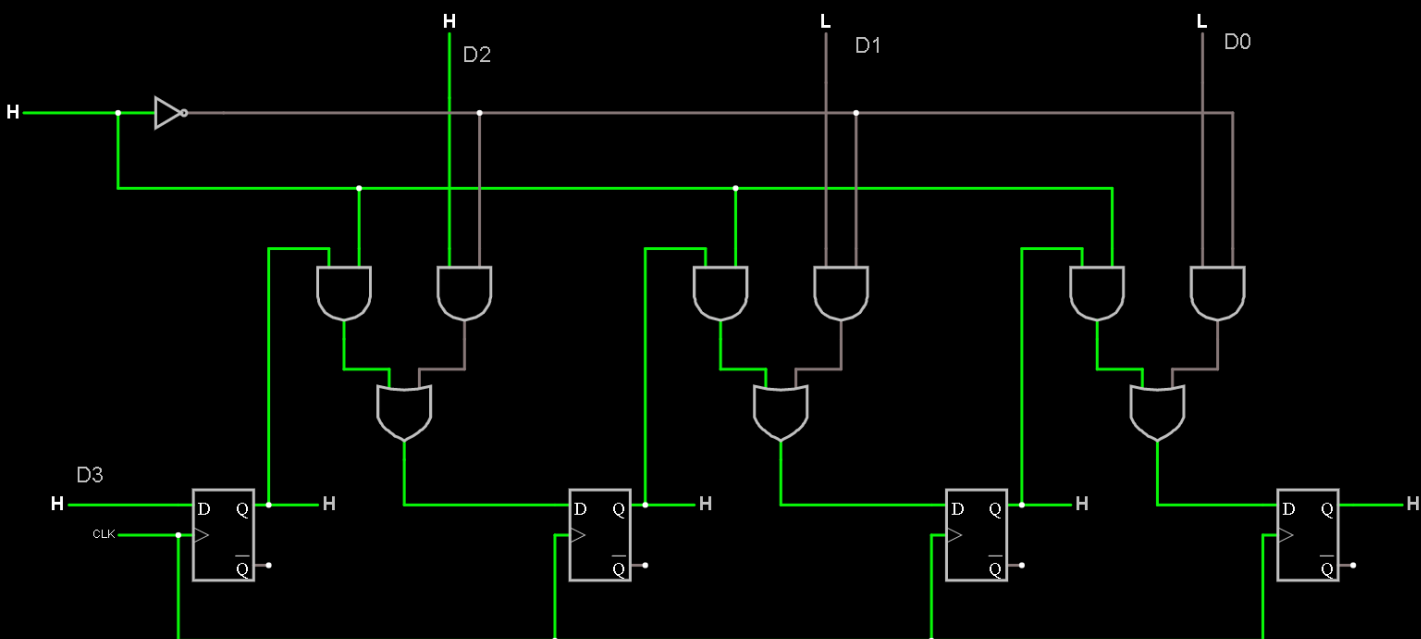
## SISO register

### Serial In Serial Out Shift Register



## PIPO register

### Parallel In Parallel Out Shift Register



## Day 1

Realization of Gates(Diodes and Transistors)

1. NOT Gate (<https://tinyurl.com/ydmzyxcg>)
2. OR And AND Gate (<https://tinyurl.com/yfc3rhtb>)
3. NOR And NAND Gate (<https://tinyurl.com/yjwkkn5r>)

Realization of Gates using IC7400

(<https://tinyurl.com/ye3ch9yq>)

Realization of Gates using IC7402

(<https://tinyurl.com/yj6tkcvt>)

## Day 2

Half Adder and Subtractor

<https://tinyurl.com/yzqhzhe4>

Full Adder and Subtractor

<https://tinyurl.com/yfkd5sxy>

## Day 3

Binary to Gray Code and Gray to Binary Code

<https://tinyurl.com/yzk3lrn2>

RS and D Flip Flop

<https://tinyurl.com/yenwfqv5>

2 to 4 Decoder

<https://tinyurl.com/yfehme6q>

Day 4

4 bit Binary counter

<https://tinyurl.com/ydoqd9ys>

SISO register

<https://tinyurl.com/yhpm6ftx>

PIPO register

<https://tinyurl.com/yf739q9d>