CIRCUIT SAT INPUT: A boolean circuit ( with n boolean & one output. An input assignment x 3.t Output of C = 1. Thm: Circuit SAT is NP-complete Mother of all NP- completens X, X,

Thm: Circuit SAT in NF complete

Proof: 1) Circuit SAT 
NP: (CONVINCE)

Proof: 1) For every problem A 
NP

=) A & CircuitsA7

FIL A = FACTORIZATION

FACTORIZATION

Input: An n bit number N INPUT: Circuit C

Solution: P, 9>1 xt p.g=N Solution: A satisfying assignment

number

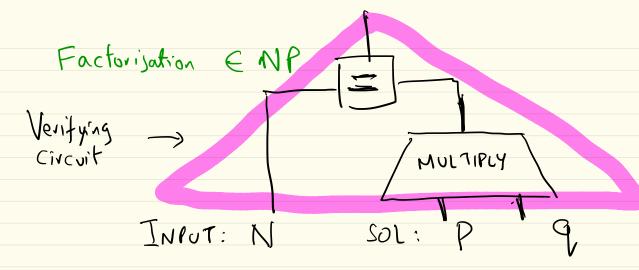
Nondotion

Verificatio

Circuit SAT

Nondotion

Nondotion



Thm: CIRCUIT SAT 3SAT
A 3SAT formula

TNPUT: on 26,...21 INPUT: A circuit C Sol: A satisfying assignment. Sol: A satisfying ansignment x 3SA1 Mariables. AND of WILL Wm Seinternal wiver. (onstraints: 1) encode each gate
2) (Wm Ywm Ywm)

35AT in NP complete

Prop:

1) 3 SAT ENP A 2) Every problem in NP

A & B 3SAT

· CircuitSA1 is NP-complete

A < Circuit SAT < 3SAT

A SpB and BSpC then A 50 C

10 prove

A is NP-complete

3SAT

INDSET

one of them

p

2)  $A \in NP$ 

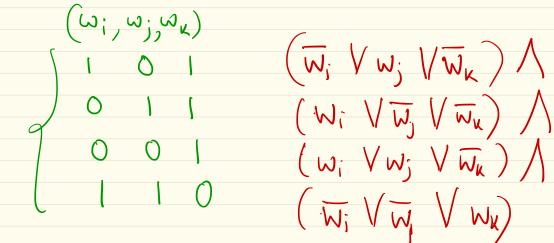
Proof: 3SAT is NP-complete.

1) If the circuit C has an assignment of ((x)=1)

=) 3 a satisfying amignment to 3SAT formula

2) (river a satisfying assignment to formula

—) find a satisfying assignment



INPUT: A 3-SAT formula on voriables 35AT: 21, ... 2/n E60,15 Formula with (X, V212 V213) / (X4 V25 V26)

m clauses
(C1. ... Cm) Chuse (21. 7 24, V24) Solution: An assignment (2, >0, 1, >1...)
that satisfies all claves. GOAL:

INDEPENDENT SET 35AT INPUT: A 3SAT formula INPUT: Graph G= (V, E), K P Sol: An independent set SOL: A satisfying ausignment 3 0 13 (X Vy Vz) / (y Vz Vw) 1 (WVxVt) 1 Satisfying anignment x=1 y=0 z=0 w= 1 += 0