

Oxford College of Engineering and Management

Assignment - II

Microprocessor

BCA Second Year, Third Semester

1. Define the instruction cycle and machine cycle. Explain op-code fetch cycle of the instruction MOVA,B.
2. Draw and explain the timing diagram for IN 02H.
3. Draw the timing diagram of instruction MVI A, 35H.
4. Draw the timing diagram of instruction STA 2030H
5. What is DMA? Explain the operation of DMA technology.
6. Draw the functional block diagram of 8255A and explain it.
7. Draw pin configuration of 8255A with the descriptions of each pin.
8. Explain the different operational modes of 8255A.
9. Write the control for setting D₃ bit and resetting D₅ bit of port C in BSR modes of 8255A.
10. Write the difference between serial and parallel communication.
11. Explain the different methods of parallel communications.
12. Explain the RS-232 with diagram along with its voltage levels.
13. Draw the block diagram of 8086 microprocessor and explain each block.
14. Differentiate Intel 8085 and 8086 microprocessor.
15. Explain the flags of 8086.
16. Write a program in 8086 to display the string "Oxford College"

Instructions:

- Solve all the questions compulsorily in A4 size paper and make proper binding with cover page.
- Printed and photocopied assignment is not accepted.
- Assignment should be submitted before Qualifying Test exam.