Theeshiikan A Shanmuganathan

https://www.theeshiikan.com — https://github.com/theeshiikan
Markham, ON L6B 0A3 — +1 (647) 769-0283 — theeshiikan.aravinth@torontomu.ca

Education

B.Eng. in Computer Engineering - 3.42 *2022 – Present*
Toronto Metropolitan University, Toronto, ON

- **Dean's List** — *2022/2023*
- **Relevant Coursework:** Algorithms and Data Structures (C), Digital Systems (FPGA/VHDL), Object-Oriented Engineering Analysis and Design (Java), Electronic Circuits (Analog Design), Microprocessors (Assembly)

Professional Experience

Lead Code Instructor, Code Ninjas *04/2023 – Present, Aurora, Canada*

- Helped develop over **500+** coding projects in **JavaScript, Python, Scratch and Makecode** guiding **100+ students** in creating games and applications.
- Led a LEGO robotics camp, teaching engineering concepts and coding robots in **JavaScript**.
- **Collaborated with large teams**, ensuring effective communication.
- Planned and hosted **3+** Hackathons, open houses, and events to engage the community.

MetEng Competition *2024 -Toronto Metropolitan University*

- **Collaborated in teams of 4** to create an **efficient software application** with a real world impact.
- Analyzed and processed **1000+ job postings** to tailor an enjoyable use experience
- Built a **Python** job-matching platform using data analytics for disadvantaged job-seekers.

Projects

MoneyFlow Tracker — JavaFX, Java and CSS

- Developed an **interactive GUI** for a banking system with multiple screens and logins.
- Implemented admin functionality for adding/removing customers.
- Facilitated **50+ customer accounts** with transaction history, balance updates, and withdrawal/deposit functions.
- Used **UML diagrams** to carefully plan and document the project structure, resulting in a well-organized code and streamlined development process.
- Personal Takeaway: Improved problem-solving skills and adaptability by learning JavaFX.

General-Purpose Processor Design — Quartus, FPGA and VHDL

- Designed and implemented a general-purpose processor on a **FPGA**, Created a processor using **latches, decoders, FSMs, and an ALU** for simple tasks.
- Achieved stable functionality with **20+ successfully executed test cases** for simple arithmetic and logic tasks.
- Personal Takeaway: Gained technical growth and collaboration skills.

Personal Website — HTML, CSS and Javascript

- **theeshiikan.com** and **kassghayouri.com**
- Developed and organized a website with subcategories for content organization.
- Added **JavaScript-based features** for enhanced engagement, increasing page interactions by **30%**.
- Personal Takeaway: Sparked an interest in web development.

Multistage Amplifier Design — Multisim

- Designed a **2-stage amplifier** using **BJTs** for specific gain requirements.
- Simulated performance in Multisim, refining the circuit through **10+ test iterations** for optimal efficiency.
- Personal Takeaway: Developed focus on precision and optimization through multiple testing stages.

StatsCan Diabetes Analyzer — C

- Built a software that fully analyzed a file with data of **200+** cities in Canada.
- Created functions with **nested loops** to print out calculated statistics.
- Personal Takeaway: Strengthened my understanding of fundamental coding practices that can be implemented in any programming language.

Micro-bot Path Tracing — Assembly

- Built a software that utilizes a **Microprocessor** on a Micro-bot
- Improved accuracy and responsiveness through algorithm optimizations after **5+ testing rounds**.
- Personal Takeaway: Strengthened low-level programming skills applicable across various systems.

Technical Skills

- **Programming Languages:** VHDL, Python, C/C++, HTML, Java, JavaScript, Assembly
- **Soft Skills:** Problem-solving, team collaboration, strong communication
- **Tools & Technologies:** Quartus, MATLAB, Git, Oscilloscopes, Logic Analyzers, and MOSFETs
- **Concepts:** Digital Logic Design, Circuit Design/Analysis, Verification Methodologies, FPGA Design