







NOTE 1

The Non-Functional Constraints are: Technology; Schedules; Production Capability; Contracts; Economics; and Experience and Precedence.

NOTE 2

The Non-Functional Requirements

Constraints and non-functional requirements; Integration Requirements; and Interface Requirements.

NOTE 3

Requirements allocated to hardware,

Requirements allocated to software, Development Assurance Levels that are allocated to items, Description of associated failure conditions,

Allocated failure rates and exposure interval(s) for

hardware failures, System(s) description,

Design constraints,

System verification activities,

NOTE 7b

levels.

NOTE 11

NOTE 17

objectives

Original Safety

The output here is

the assignment of

specifically considering

development assurance

Assessment pertinent to

the level of architecture

(i.e. system and/or item)

When multiple-version

a system, the software

planning process should

choose the methods and tools to achieve the

dissimilarity necessary to

satisfy the system safety

dissimilar software is used in

Evidence of the acceptability by the system process of any data provided by the hardware and/or software processes to the system process on which any activity/assessment has been conducted by the system process (e.g. the system process evaluation of derived requirements provided by the software process to determine if there is any impact on the SSA).

NOTE 4

Derived requirements for evaluation/validation, Description of the implemented hardware or software architecture - sufficient to show the achieved independence and fault containment capabilities (e.g. hardware segregation, software partitioning),

Evidence of any system/item verification activities performed at the software or hardware development level, Hardware failure rates, fault detection coverage, common cause analyses and latency intervals (for incorporation in the

Problem or change documentation that may impact system or item requirements or hardware/software derived requirements, to be evaluated against the system or item requirements or the safety assessment, Any limitations of use, configuration identification/status constraints, performance/timing/accuracy characteristics, Data to facilitate integration of the hardware / software into the system (e.g. installation drawings, schematics, parts lists), Details of proposed hardware/software verification activities to be performed during system level verification.

NOTE 5

Derived requirements needed for hardware/software integration, such as definition of protocols, timing constraints, and addressing schemes for the interface between hardware and software.

NOTE 6

List of previously agreed upon external event probabilities,

System description including functions and interfaces, List of Failure Conditions,

Qualitative and quantitative analyses for Failure Conditions (e.g. FTA, FMES, Markov Analysis, Dependence Diagrams),

The results obtained from Common Cause Analyses, Safety related tasks and intervals (FTA, FMES, Markov Analysis, Dependence Diagrams),

Development Assurance Levels for aircraft functions and systems (FDAL) (PASA, PSSA),

Development Assurance Levels for electronic hardware and software items (IDAL),

The results of the non-analytic verification processes (i.e. test, demonstration and inspection), A summary of the aircraft safety activities from the beginning of the concept development to the completion of the detailed design development.

Those functions required for continued safe flight and landing

NOTE 14

NOTE 19

NOTE 23

Trace data to:

NOTE 15

CMA verifies that ANDed gates are truly independent

CMA verifies that The represents the usual route. Alternative routes are possible - see Fig 3-1 of DO 178C

NOTE 7

Members,

NOTE 10

DAL

NOTE 16

Refer to ARP 4754A

recommended methods

in accordance with the

Table 6 for the

Independence.

Functional Failure Set,

The need for the 1. Enable verification that no Source Code implements an once they have been undocumented function. implemented in the 2. Enable verification of the complete implementation of the low-level requirements.

NOTE 20

requirements to be verifiable software may itself impose additional requirements or constraints on the software development processes.

NOTE 21

Robustness test cases are requirements-based. The robustness testing criteria cannot be fully satisfied if the software requirements do not specify the correct software response to abnormal conditions and inputs. The test cases may reveal inadequacies in the software requirements, in which case the software requirements should be modified. Conversely, if a complete set of requirements exists that covers all abnormal conditions and inputs, the robustness test cases will follow from those software requirements.

NOTE 8

Only if validation of requirements is not possible before design implementation is available.

NOTE 12 How service history will be used (e.g. the amount of service experience available and a description of how the service data will be analysed)

NOTE 18

Trace data to:

1. Enable verification of the

complete implementation of

the system requirements allocated to software.

2. Give visibility to those

requirements that are not

directly traceable to system

derived high-level

requirements.

recommended methods in accordance with the DAL

Refer to ARP 4754A

Table 6 for the

NOTE 13

NOTE 9

Prior (existing) platform certification

Typical errors revealed by this testing method include: Incorrect interrupt handling; Failure to satisfy execution time requirements;

Incorrect software response to hardware transients or hardware failures, e.g. start-up sequencing, transient input loads, and input power transients; Data bus and other resource contention problems, e.g. memory mapping;

Incorrect behaviour of control loops; Incorrect control of memory management hardware or other devices under software control;

Inability of built-in test to detect failures;

Stack overflow;

Incorrect operation of mechanism(s) used to confirm the correctness and compatibility of field-loadable software; Violations of software partitioning.

Typical errors revealed by this testing method include: Incorrect initialisation of variables and constants; Parameter passing errors; Data corruption, especially global data; Inadequate end-to-end numerical resolution; Incorrect sequencing of events and operations.

NOTE 24

Typical errors revealed by this testing method include: Failure of an algorithm to satisfy a software requirement; Incorrect loop operations; Incorrect logic decisions; Failure to process correctly legitimate combinations of input conditions; Incorrect responses to missing or corrupted input data; Incorrect handling of exceptions, such as arithmetic faults or violations of array limits; Incorrect computation sequence; Inadequate algorithm precision, accuracy or performance.