

Cache Controller

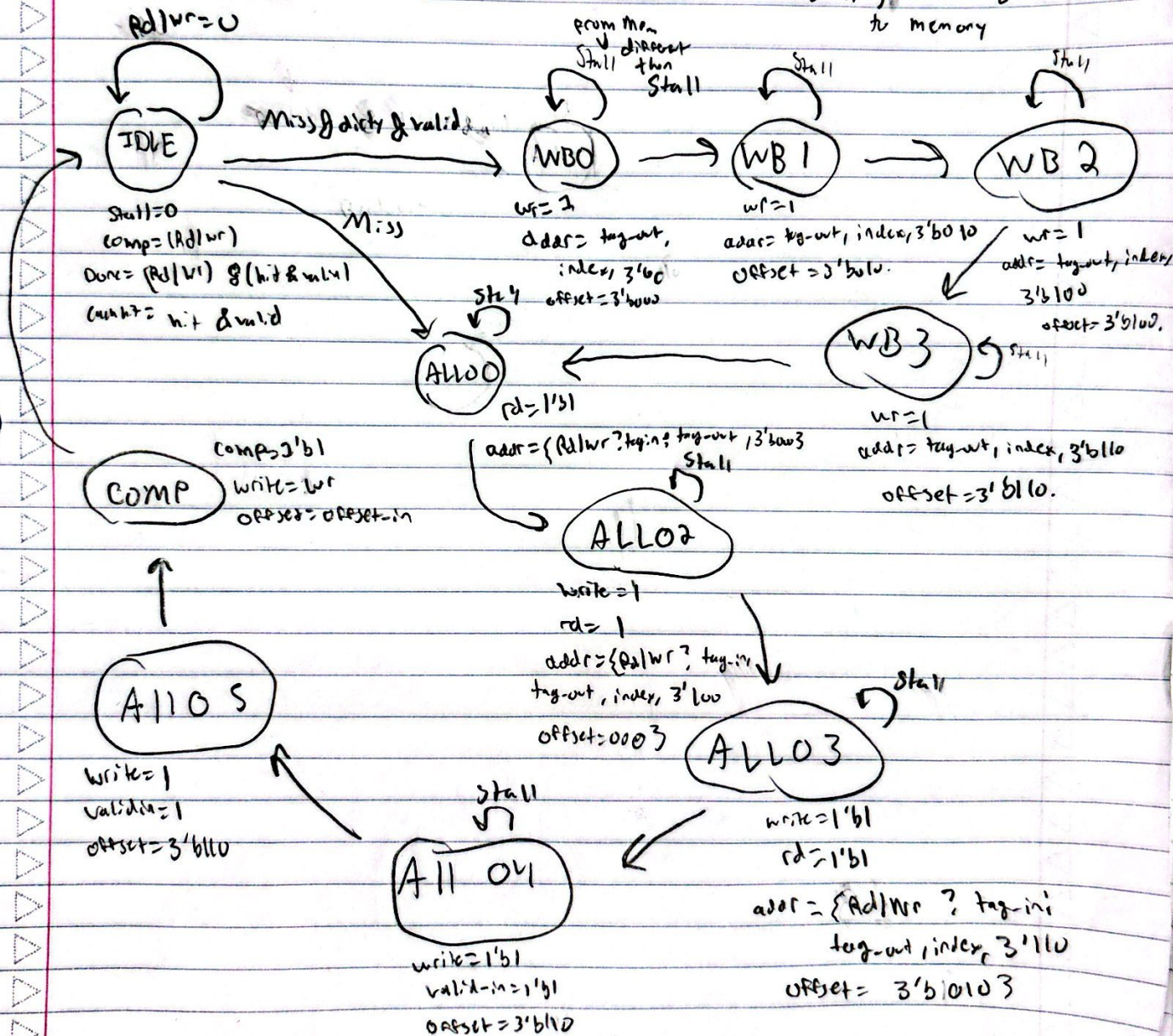
FSM

Note: We made a separate module for the cache controller so our control is the same for direct and Associative

Defaults:

Done = 0 err = 0 Comp = 0 addr = 0000
 stall = 1 enable = 1 write = 0 wr = 0
 Cache hit = 0 offset = 000 valid_in = 0 rd = 0

WBX state: write dirty victim to memory



ALLOX states = fetch memory and write to cache line.

4 words, 1 word per state.