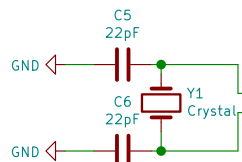


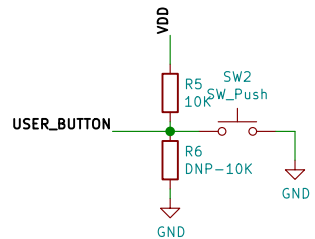
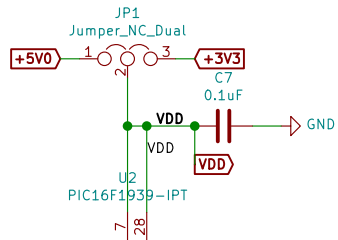
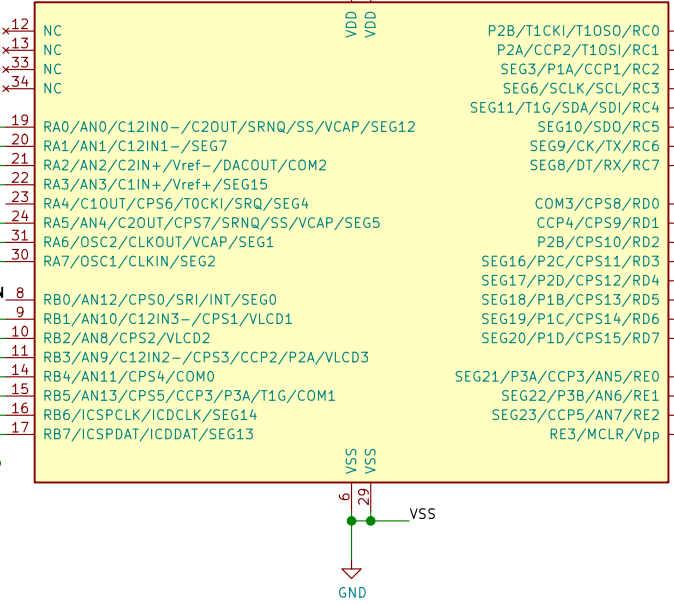
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1 MCLR\_VPP  
2 VDD  
3 VSS  
4 PGD(ICSPDAT)  
5 PGC(ICSPCLK)  
6 PGM(LVP)

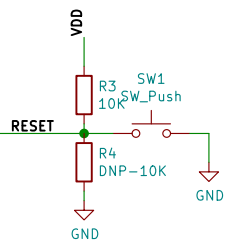
Conn\_01x06\_Male  
J6



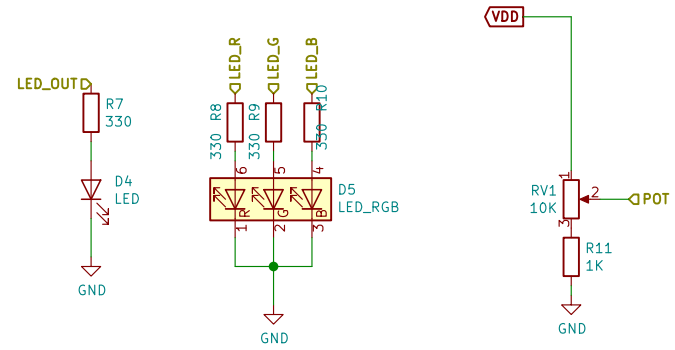
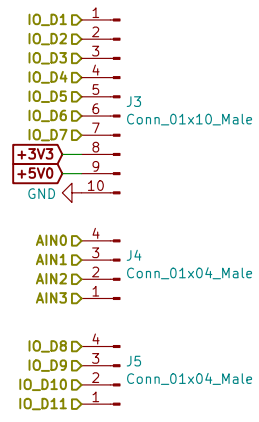
RA0\_AN0  
RA1\_AN1  
RA2\_AN2  
RA3\_AN3  
POT\_AN5  
USER\_BUTTON  
RB1  
RB2  
RB3  
RB4  
RB5  
RB6  
RB7  
PGC(ICSPCLK)  
PGD(ICSPDAT)



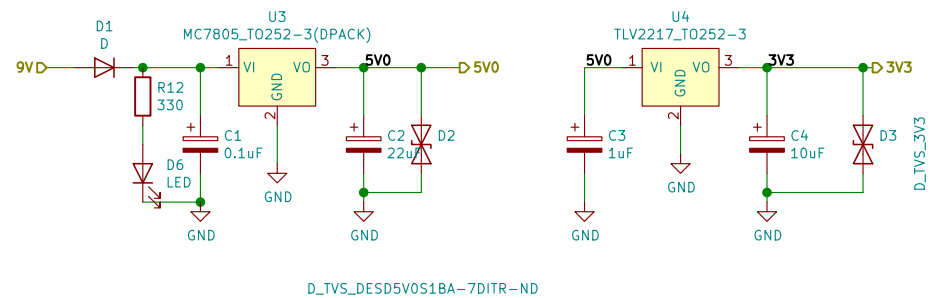
RE0  
RE1  
RE2



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Size: A4	Date: 2019-05-06	Rev: 1.1
KiCad E.D.A. kicad (5.1.2)-1		Id: 2/5



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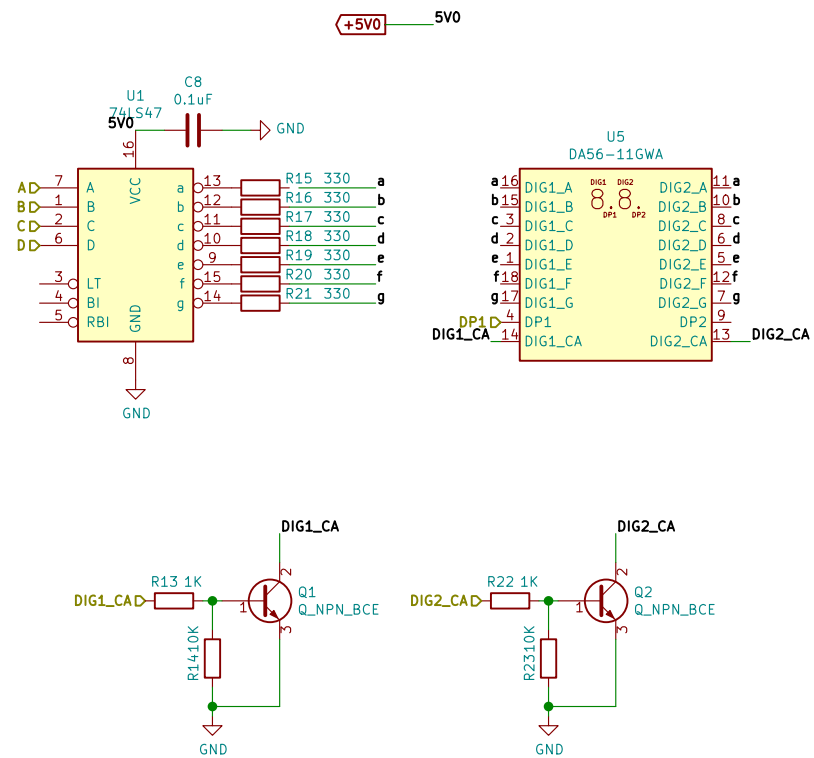
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**Rev: 1.1**

Id: 4/5



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Sheet: /Display/

File: Display.sch

**Title: Kit Development for MCU PIC**

Size: A4

Date: 2019-05-06

Rev: 1.1

KiCad E.D.A. kicad (5.1.2)-1

Id: 5/5