

	Delay	E.g	Pipeline																											
1. Read After Write																														
1. RAW-1																														
Non-lw																														
+ without forwarding (sw also cause RAW)	still +2 +2	sll \$t3, x, x sw \$t3, 0(x) add x, \$t3, \$t3	<table><tr><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td><td></td><td></td></tr><tr><td></td><td>F</td><td>/</td><td>/</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td></tr></table>	F	D	E	M	W						F	/	/	D	E	M	W										
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+ with forwarding	+0	sll \$t3, x, x slt x, \$t3, x	<table><tr><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td><td></td><td></td></tr><tr><td></td><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td><td></td></tr></table>	F	D	E	M	W						F	D	E	M	W												
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lw																														
+ without forwarding	+2	lw \$t3, x(x) srl x, \$t3, x	<table><tr><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td><td></td><td></td></tr><tr><td></td><td>F</td><td>/</td><td>/</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td></tr><tr><td></td><td></td><td></td><td></td><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td></tr></table>	F	D	E	M	W						F	/	/	D	E	M	W						F	D	E	M	W
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				F	D	E	M	W																						
+ with forwarding	+1	lw \$t3, x(x) srl x, \$t3, x sw x, x(\$t3)	<table><tr><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td><td></td><td></td></tr><tr><td></td><td>F</td><td>D</td><td>/</td><td>E</td><td>M</td><td>W</td><td></td><td></td></tr><tr><td></td><td></td><td>F</td><td>/</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td></tr></table>	F	D	E	M	W						F	D	/	E	M	W					F	/	D	E	M	W	
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		F	/	D	E	M	W																							
2. RAW-2																														
Non-lw																														
+ without forwarding (sw also cause RAW)	+1	sll \$t3, x, x xxx x, x, x slt x, \$t3, x	<table><tr><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td><td></td><td></td></tr><tr><td></td><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td><td></td></tr><tr><td></td><td></td><td>F</td><td>/</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td></tr></table>	F	D	E	M	W						F	D	E	M	W						F	/	D	E	M	W	
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+ load Word with forwarding (3rd instruction RAW on the 1st)	+0	lw \$t3, x(x) xxx x, x, x sw x, x(\$t3)	<table><tr><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td><td></td><td></td></tr><tr><td></td><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td><td></td></tr><tr><td></td><td></td><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td></tr></table>	F	D	E	M	W						F	D	E	M	W						F	D	E	M	W		
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3. Branch**																														
i) at MEM/Decode, ii) no forwarding iii) imm after write (non-lw/lw)*	MEM OR no forwarding follow RAW	sll/lw \$t3, x beq \$t3, x, L	<table><tr><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td><td></td><td></td></tr><tr><td></td><td>/</td><td>/</td><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td></tr></table>	F	D	E	M	W						/	/	F	D	E	M	W										
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i) at Decode, ii) forwarding iii) imm after write (non-lw)*	RAW1 +1	sll \$t3, x, x beq \$t3, x, L	<table><tr><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td><td></td><td></td></tr><tr><td></td><td>F</td><td>/</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td></tr></table>	F	D	E	M	W						F	/	D	E	M	W											
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i) at Decode, ii) forwarding iii) imm after write (lw)*	RAW2 +1	lw \$t3, x (x) beq \$t3, x, L	<table><tr><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td><td></td><td></td></tr><tr><td></td><td>F</td><td>/</td><td>/</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td></tr></table>	F	D	E	M	W						F	/	/	D	E	M	W										
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4. After-branch Control Hazard																														
No prediction / wrong prediction (at MEM)	+3		<table><tr><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td><td></td><td></td></tr><tr><td></td><td>/</td><td>/</td><td>/</td><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td></tr></table>	F	D	E	M	W						/	/	/	F	D	E	M	W									
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No prediction / wrong prediction (at Decode)	+1																													
Correct prediction	+0		<table><tr><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td><td></td><td></td></tr><tr><td></td><td>F</td><td>D</td><td>E</td><td>M</td><td>W</td><td></td><td></td><td></td></tr></table>	F	D	E	M	W						F	D	E	M	W												
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Delayed branching	Fail: +1/0 Success: +0																													

### Special Cases

- ① If SAME We have RAW-1 and RAW-2, then only add delay for RAW-1
- ② If ever RAW-1 followed by RAW-2, then only add delay for RAW-1

\*\*be careful, doesn't consider being 2-after write, but rule of thumb is just -1 the "imm after write" case