

PHYS 301 - Digital Electronics Lecture / Laboratory

Demonstration Program B			
P Step	PGM MEM	DATA MEM	BCD OUTPUT
0	NOP		-
1	CLR		0
2	LDA	0xC2	0
3	LDB	0x19	0
4	NOP		0
5	ADDU		0
6	LDC		219
7	LAC		219
8	LDB	0x24	219
9	SUBU		219
10	LDC		183
11	NOP		183
12	LDA	0xE7	183
13	ADDU		183
14	LDC		11 ** (267)
15	LDB	0x09	11 ** (267)
16	LAC		11 ** (267)
17	SUBU		11 ** (267)
18	LDC		2
19	LDA	0x03	2
20	ADDU		2
21	LDC		12
22	SUBU		12
23	LDC		6 *

Demonstration Program B			
P Step	PGM MEM	DATA MEM	BCD OUTPUT
24	LDC		6 *
25	HLT		6 *
26	LAC		6 *
27	LDC		6 *
28	HLT		6 *

* indicates a negative result

** indicates an overflow condition