

## PHYS 301 - Digital Electronics Lecture / Laboratory

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Demonstration Program A			
P Step	PGM MEM	DATA MEM	BCD OUTPUT
0	NOP		-
1	CLR		0
2	LDA	0x6A	0
3	LDB	0x07	0
4	SUB		0
5	LDC		99
6	LAC		99
7	ADD		99
8	LDB	0xA3	99
9	LDC		80
10	NOP		80
11	LDA	0x33	80
12	LDB	0x93	80
13	SUB		80
14	LDC		70
15	LDA	0xCD	70
16	LDB	0x1B	70
17	ADD		70
18	LDC		50 *
19	LAC		50 *
20	LDB	0x3D	50 *
21	SUB		50 *
22	LDC		11 *
23	LAC		11 *

[illegible]

\* indicates negative result

**\*\* indicates an overflow condition**