PHYS 301 - Digital Electronics Lecture / Laboratory

Demonstration Program C			
P Step	PGM MEM	DATA MEM	BCD OUTPUT
0	NOP		-
1	CLR		0
2	LDA	0x02	0
3	LDB	0x10	0
4	NOP		0
5	SUBU		0
6	LDC		14 *
7	LAC		14 *
8	LDB	0x20	14 *
9	ADDU		14 *
10	LDC		18 (46)
11	NOP		18 (46)
12	LAC		18 (46)
13	LDA	0xC5	18 (46)
14	LDB	0x92	18 (46)
15	SUBU		18 (46)
16	LDC		51
17	LDA	0x1B	51
18	LDB	0x8A	51
19	NOP		51
20	ADD		51
21	LDC		17
22	NOP		17
23	SUB		17

                                           Spring 2017

Demonstration Program C			
P Step	PGM MEM	DATA MEM	BCD OUTPUT
24	LDC		37
25	LDA	0xFC	37
26	ADDU		37
27	LDC		22 **
28	HLT		22 **
29	SUBU		51
30	LDC		51
31	HLT		51

* indicates a negative result

** indicates an overflow condition