

# Paul Kim

Undergraduate at Electrical & Computer Engineering, Seoul National University

in @thekpaul 🏠 <https://thekpaul.dev> 🇰🇷 Republic of Korea

📧 thekpaul 🏢 jsvn7777@snu.ac.kr ✉️ thekpaul000@gmail.com



## Education

Mar. 2018 BA in Electrical & Computer Engineering, Seoul National University  
Expected to graduate in Summer 2024  
**Areas of Interest:** System Programming, Digital Systems Design, Deep Learning  
**Graduation Project:** Optimising the CNN Architecture with Hardware Design

## Experience

Mar. 2023 FPGA Engineer Intern for **NeuroRealityVision**  
Dongtan, Republic of Korea

- Firmware, software development for MIPI-to-Ethernet integration
- Bare-metal, standalone FPGA application with interrupt-based workflow
- Custom IP development with Verilog-based RTL on Vivado

Jan. 2023 to Jun. 2023 Student Intern for Dept. of System Semiconductor Engineering  
**Seoul National University**, Seoul, Republic of Korea

- Worked on CNN Architecture and Hardware Design Optimisation

Dec. 2019 to Jul. 2021 KATUSA, Human Resources Specialist (42A)  
**94<sup>th</sup> Military Police Battalion**, Pyeongtaek, Republic of Korea

- Mandatory military service as a KATUSA Agent  
(Korean Augmentation To the United States Army)

## Skills

- Engineering**
- Experienced in embedded systems design on multi-purpose SoC FPGAs
  - Capable of basic RTL hardware design using Verilog
  - Capable of versatile development with C, C++ and Python
  - Capable of maintaining HTML/CSS front-end projects
  - Proficient in typesetting with  $\text{\LaTeX}$
- Languages**
- Korean: Native proficiency
  - English: Fluent, professional working proficiency

## Education

### Bachelor's Degree: ECE, SNU

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## Experience

### FPGA Engineer Intern: NeuroRealityVision

- Firmware, software development for MIPI-to-Ethernet integration
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### Student Intern: SSAI, SNU

- Worked on CNN Architecture and Hardware Design Optimisation

### KATUSA Human Resources Specialist

- Mandatory military service as a KATUSA Agent (Korean Augmentation To the United States Army)