CS143: Runtime

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Runtime

- Semantic Analysis (wrap up)
 - Error Recovery
- Runtime Conventions
 - Memory Regions
 - Stack Allocation
 - Expression Evaluation on a Stack

Error Recovery

Goal: Report errors after the first one undeclared. let y! Int $\leftarrow x + 2$ in y+3 Type for undeclaned x ? If x: Object, x+2 will cause another error, Reports Too many errors.

Another Solution Compiler stores "No_type" for erroneous expressions No-type & C for all types C 50 No_type Ll C'is always C All aperations/assignments are ok. let y: Int < x + Z in y + 3 < only one error No-+ype UInt > Int No - +ype

Types no longer atree

Not a problem unless you have code that assumes it's a tree

Not required in project

Object
/
Ti
Tz
/
No-type

Runtime Conventions

Memory Regions

Machine archite ouve does not (usually) dietate how:

Memory is used Function calls work etc.

But these need to be handled consistently

E.g., Function calls: Caller I called

need to agree on where arguments

will be.

Memory Organization

Process has many different memory regions

Ser up by OS, initialization code.

I will ignore dynamic loading, shared

memory, etc.

Memory Regions Code- the instructions Data - Several kinds Global variables & constants Stack - for function call data tteap-for other dynamically created data (e.g. from malloc! in ()) Executable code needs to be able to find these.

Finding Data Global Variables and constants Occupy fixed addresses in memory Compiler decides where they go. Heap-allocated objects
Pointers are stored/passed in globel,
local variables, parameters, etc. Formals & Locals! Next

Stack Allocation

Use of Steek Designed for sequential code Breaks down if In multithreaded code. If functions can access variables of containing functions AND functions are treated as first-class values (requires "closures")

Assume these are not issues for this become,

Knogram Stack During function call, need temporary sturage for: Actual parameter values Local variables Saved machine state (e.g. régisters) Return address Remon value Pointer to previous activation record (Sometimes other Stuff) ("Control u Link Function Calls call f call fz call fz return from f3 return from for return from f,

Function Calls

Call f₁

Call f₂

Call f₃

Leturn from f₃

Leturn from f₂

return from f,

Nesting structure.
Stack allocation
Will work.

Def: Data to execute a function is stored in an activation record (aka "stack frame").

Under above assumptions, these can be stored in the stack.

Actual parameter values
Local variables
Saved machine state (e.g. registers)
Return address
Return value
Control Link
(sometimes other stuff)

Stacks Grow Down (for now)

top

AR organization

result
avgnments
Control link
return address

Compiler writer must decide and generate code to set up / tear down access data

Design Considerations:

Simplicity of code gen

Efficiency (minimize

deta mores)

Responsibilities must be duided between caller and caller.

Compatibility with other.

Class Main { $g(): In+ \{1\};$ $f(x: In+): Int \{if x=0 \text{ then } g() \text{ else } f(x-1) \text{ fix} \}$ main (): Int $\{\{f(3); 3\}; \lambda(*)\}$

result
avghment
control link
return address

Main
(result)
3

rame
(*)

Larvent

Class Main { g(): Int {13; $f(x:In+):Int {if } x=0$ then g() else f(x-1) fix main (): Int ${\{f(3);33;}$ (*)result

return address

Class Main { $g(): In+ \{13\};$ $f(x: In+): Int \{if x=0 \text{ then } g() \text{ else } f(x-1) \text{ fix} \}$ main (): Int $\{\{13\}; \{3\}; \{3\}\}; \{4\}\}$

result avgnment Control link return address Main
f
(result)

(**)

(vesult)

frame

(***)

Class Main {
g(): In+ {1};
f(x: In+): Int {if x=0 then g() else f(x-1) fix}
main (): Int {{3};
}

result
avgnment
control link
return address

Current
frame

f

(**)

returned

value

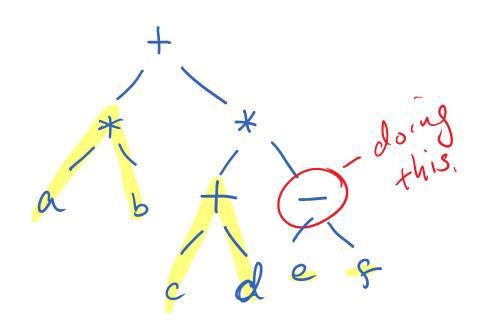
Heap Allocation Problem: Some data lives longer than the function call that creates it. Solution: Heap allocation. Heap: One or more reserved memory regions C: malloc - marks free memory as used. free - marks used memory as free Many languages: new A allocates a new instance Storage may be reclaimed automatically (e.g. garbage collection) Memory Alignment

Almost all modern architectures are
byte addessable (8-bit bytes)

But many instructions require (or strongly
reward) alignment of data to 32-bit or
64 bit boundaries.

E.g., A 32-bit integer at addess 10007 would not work well with an add instruction "Padding" - unused bytes so next object is aligned.

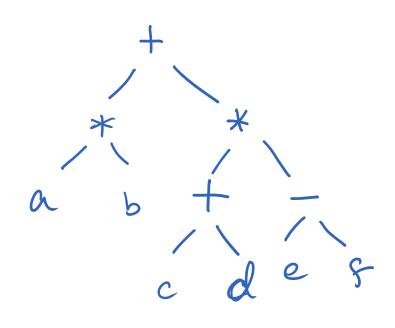
Expression Evaluation with a Stack



Evaluate left-to-right
bottom-up.

Need to save a*b,

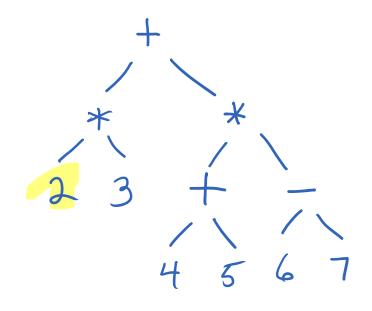
C+d, while computing
e-f.



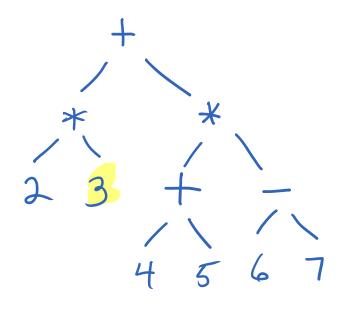
Evaluate left-to-right bottom-up.

Need to save a*b, C+d, while computing e-f.

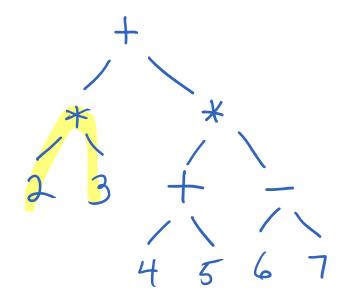
This requires temporary storage,



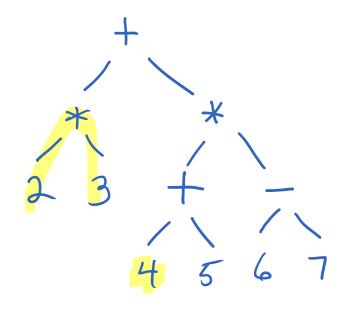
Stack (grows up)



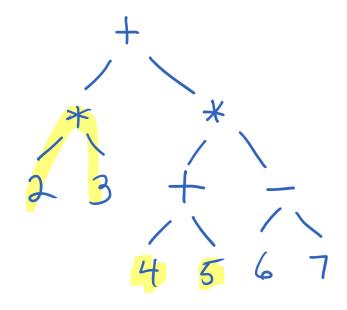
Stack (grows up)



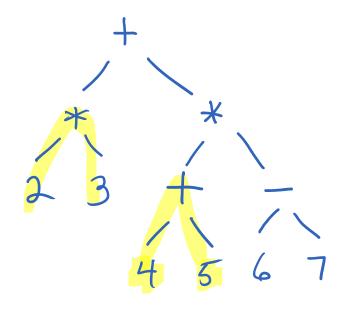
Stack (grows up)



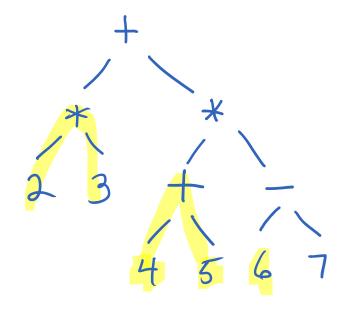
Stack (grows up)



Stack (grows up)

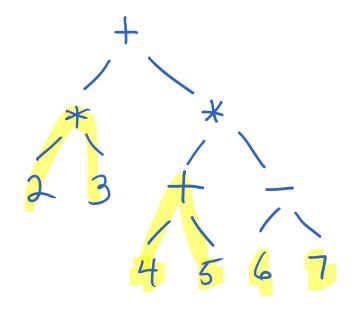


Stack (grows up)

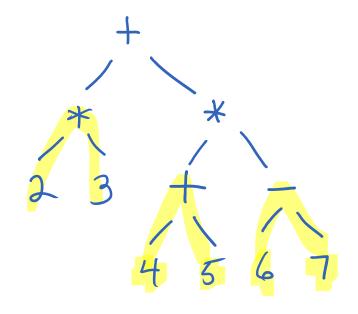


Stack (grows up)

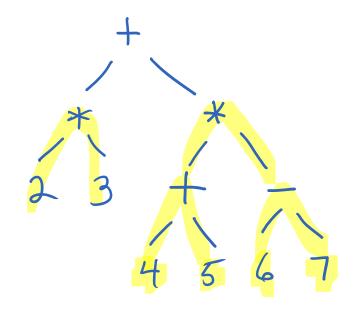
6
9



Stack (grows up)
7
6
9

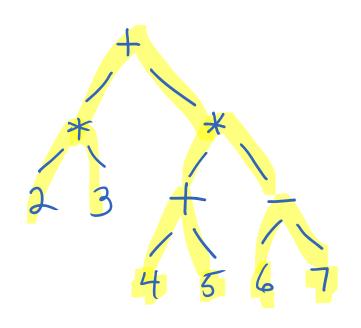


Stack (grows up)



Stack (grows up)

-9 6



Stack (grows up)

-3

Code Generation for Expressions

Instructions:

push i - push the number i on the stack add - pop top two numbers, add them, push the sum.

 $2+3 \Rightarrow Push 2$ Push 3 add

Code Generation for Stack Machines 13 Easy No need to optimize scarce registers Don't need to specify operands always the top n values on the stack. No need to speerty destinations

Short instructions & compact programs,

Stack + Accumulator

Idea: Top of stack is heavily used keep; + in a fast register

(called the accumulator - acc)

add: acc & acc + top of stack (does not pop top of stack)