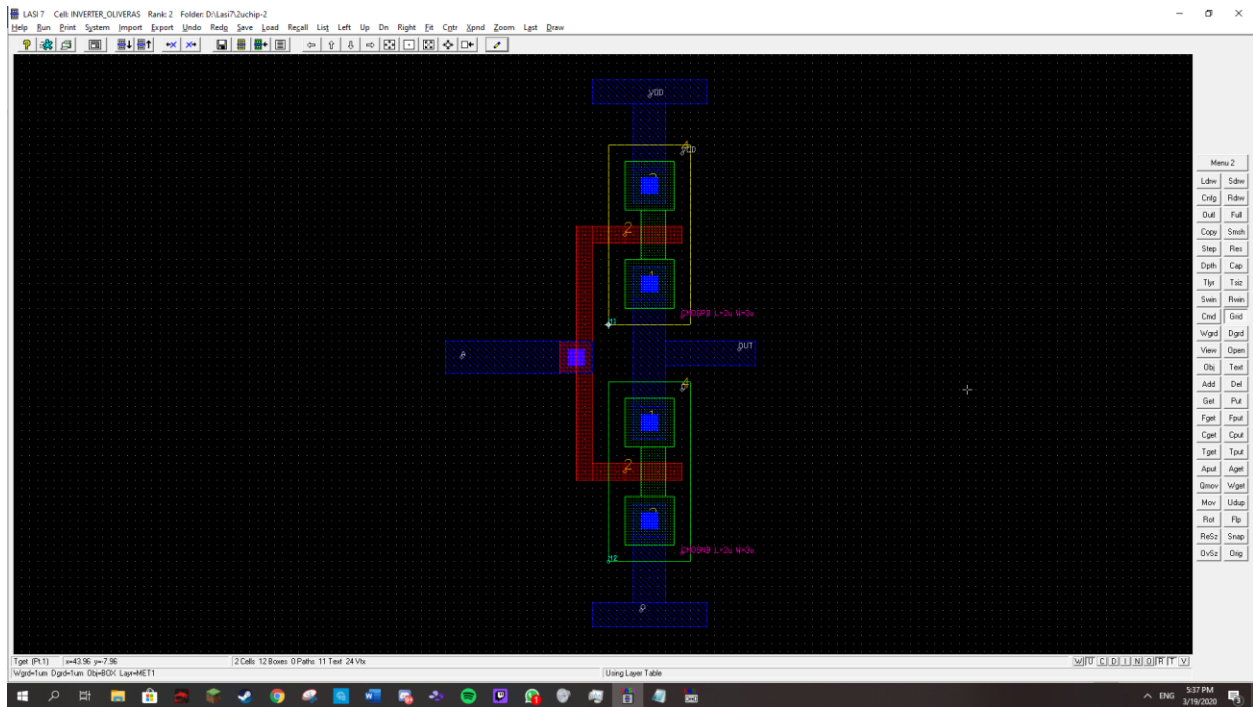
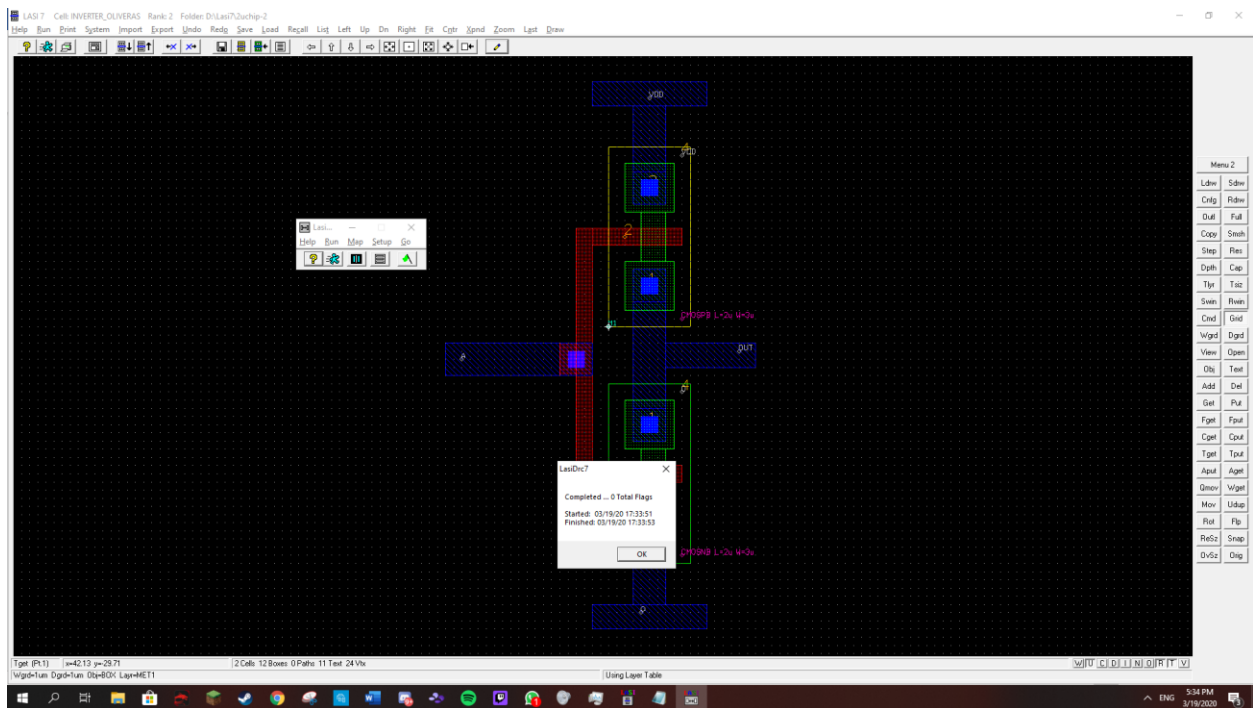


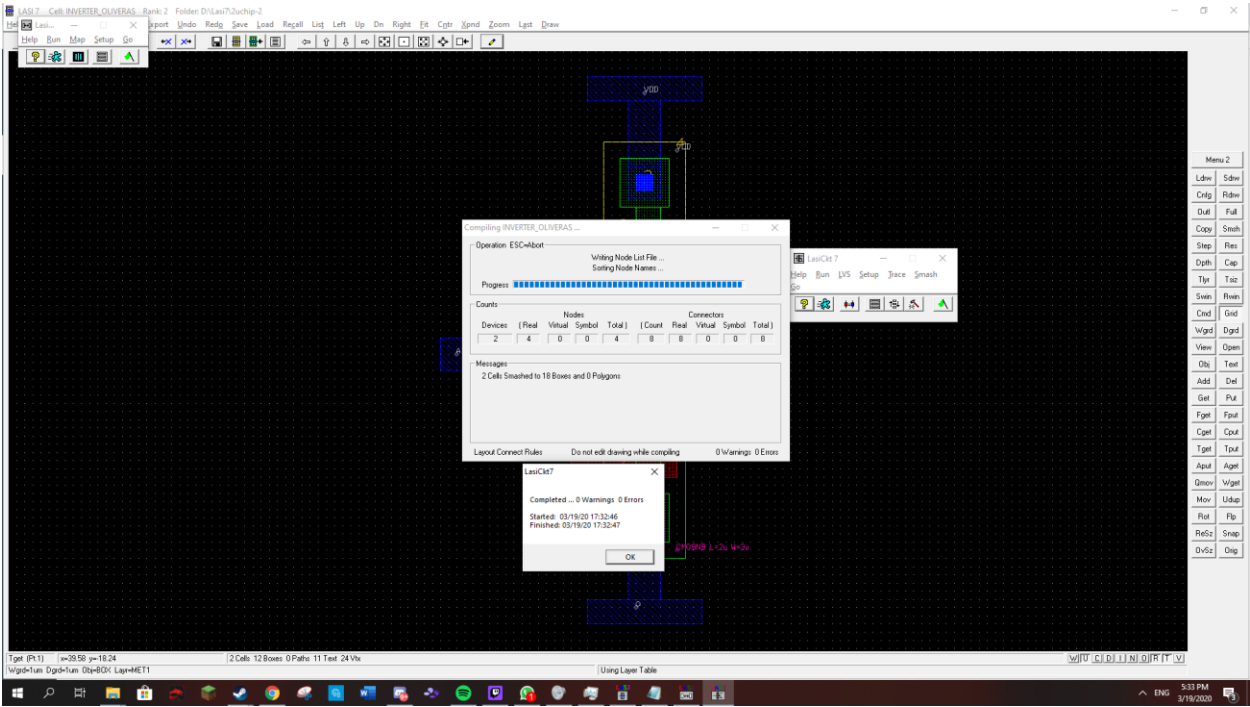
Assignment 1 (Lab 1) – INVERTER Design (Note: M2 text is over M1 because I mistakenly labelled it M1, and when I chose Tget it wasn't removing M1)



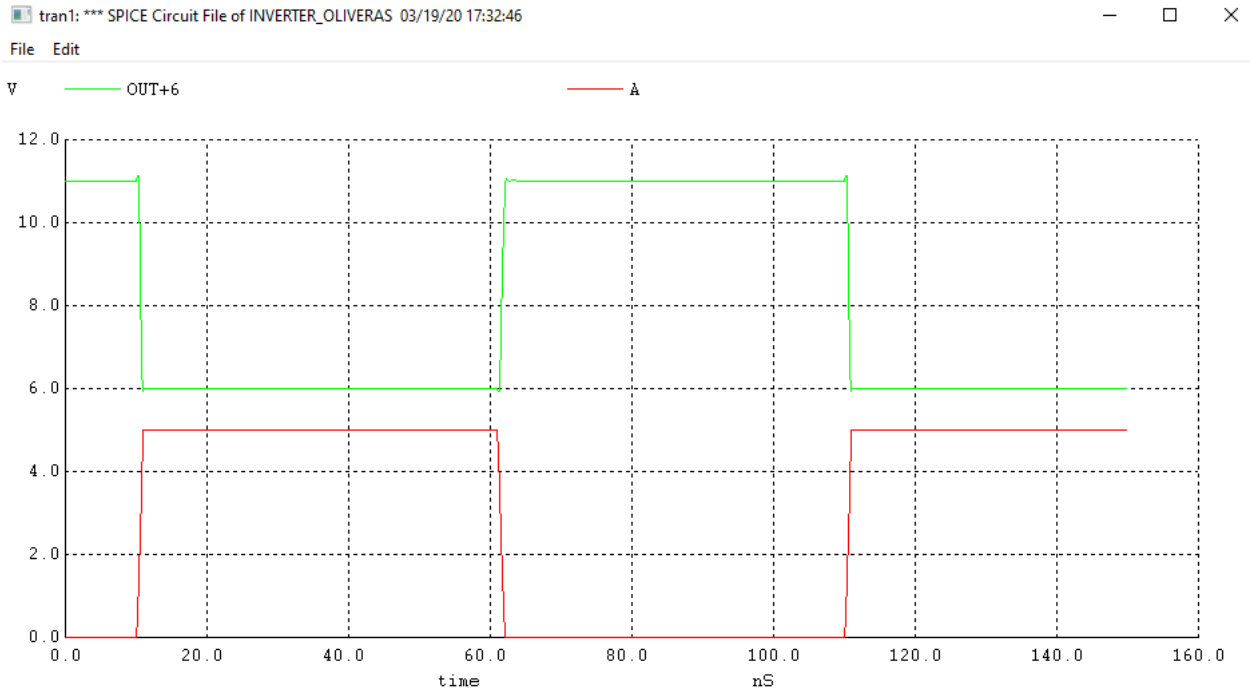
## LASIDRC



LASICKT



Waveforms



HDR File

V1 VDD 0 DC 5V AC 0 0

V2 A 0 DC 0 AC 0 0 PULSE (0 5V 10n 1ns 1ns 50ns 100ns)

.options reltol=0.1 abstol=1u vntol=50mv

.probe

.tran 1ns 150ns

.plot tran all