

ch 6 continuation: verification

Alg 1. Reachability

Alg 2 coreachability (\leq, δ, Q_m, Q_x)

let $k := 0, Q_0 = Q_m \setminus Q_x$

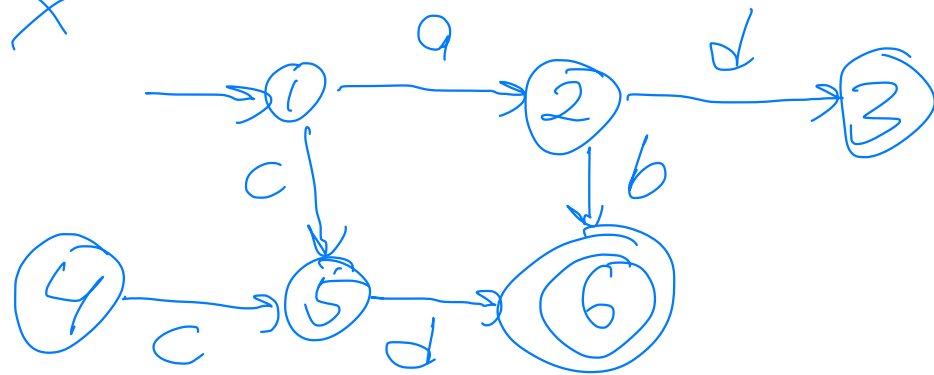
repeat

$k := k + 1$

$Q_k := Q_{k-1} \cup \{q \mid \delta(q, \sigma) \in Q_{k-1}, \sigma \in \Sigma\} \setminus Q_x$

until $Q_{k-1} = Q_k$, return Q_k

Ex



Alg 1 Reachability

$Q_0 = \{1\}$

$Q_1 = \{1\} \cup \{2, 5\} = \{1, 2, 5\} \stackrel{\text{def}}{=} Q_r$

$Q_2 = \{1, 2, 3, 5, 6\} \quad Q_3 = Q_2 = Q_r$

Alg 2 coreachability

$Q_0 = \{6\}$

$Q_1 = \{2, 5, 6\}$

$Q_2 = \{1, 2, 4, 5, 6\}$

$Q_3 = Q_2 \stackrel{\text{def}}{=} Q_c$

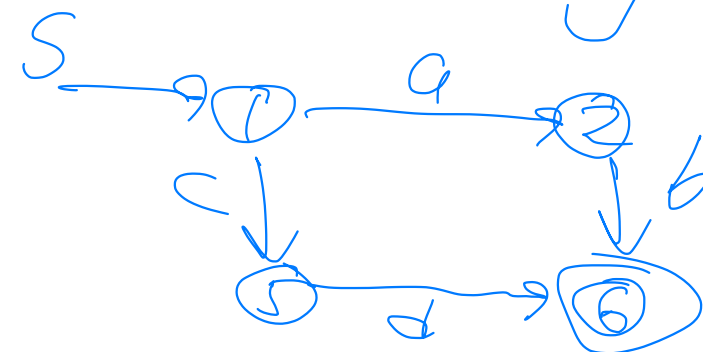
Trim automaton includes

only the states that
are both reachable
and coreachable.

Trim states are

$Q_r \cap Q_c = \{1, 2, 5, 6\}$

Nonblocking supervisor



verification based on reachability analysis

1) Is a specific state q reachable? $q \in Q_r$?

2) Forbidden states Q_f are given. $Q_f \cap Q_r = \emptyset$?

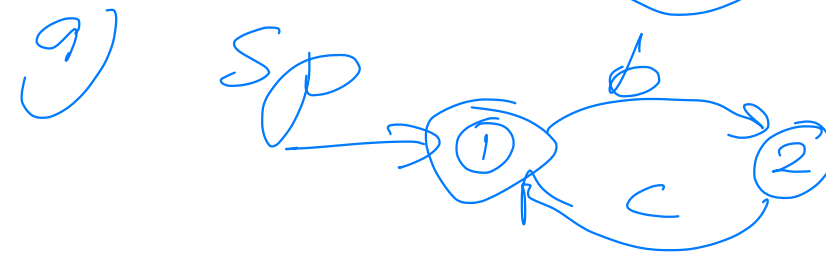
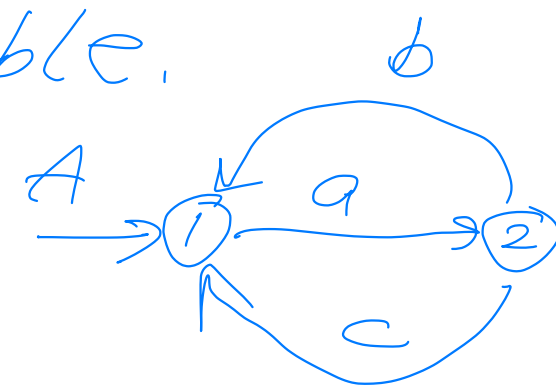
3) Are event sequences given by a specification Sp possible in an automaton A ?

Generate $AllSp$. 1) If $AllSp$ is nonblocking $\Rightarrow Sp$ is a part of the behaviour of A

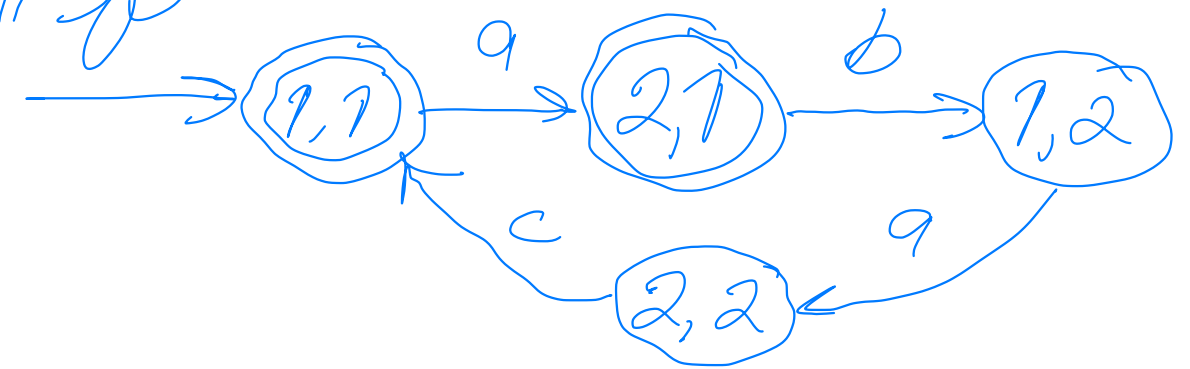
2) If $AllSp$ is blocking, the Sp event sequences are not

possible.

Ex

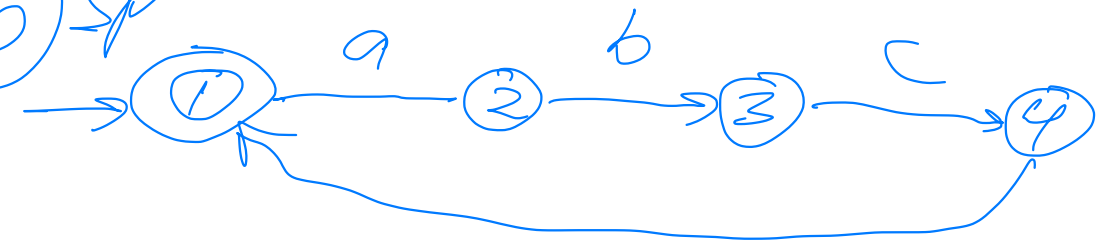


$AllSp$

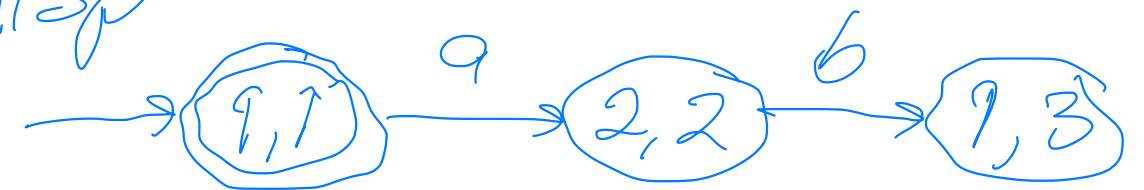


Sp is possible in A

b) Sp



$AllSp$



$\Rightarrow Sp$ is not possible in A (blocking state)

controllability

Plant $P = \langle Q^P, \Sigma^P, \delta^P, q_i^P \rangle$

supervisor $S =$

$\langle Q^S, \Sigma^S, \delta^S, q_i^S, Q_m^S, Q_x^S \rangle$

$$\Sigma^S \subseteq \Sigma^P$$

closed loop system: $P \parallel S$

$$\Sigma^P = \Sigma_c \dot{\cup} \Sigma_u, \Sigma_c \cap \Sigma_u = \emptyset$$

Σ_c = set of controllable events which the supervisor can accept or prevent.

Σ_u = set of uncontrollable events which S cannot prevent.

controllable supervisor S

$$P \parallel S \quad Q_r^{P \parallel S} \subseteq Q^P \times Q^S$$

$$Q_r^{P \parallel S} = \text{Reachability}(\Sigma^{P \parallel S}, \delta^{P \parallel S}, q_i^{P \parallel S})$$

controllability is related to the uncontrollable events in P that are also a part of the supervisor alphabet Σ^S

$$\Gamma_u \in \Sigma_u \cap \Sigma^S$$

Definition of controllability

A supervisor S is controllable with respect to (wrt) a plant P and a set of uncontrollable events $\Sigma_u \subseteq \Sigma^P$ if,

for all reachable states

$\langle p, q \rangle \in Q_r^{P||S}$ in the closed

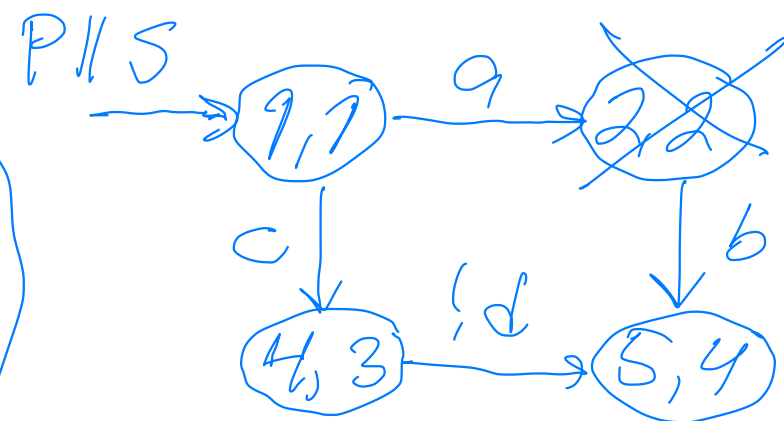
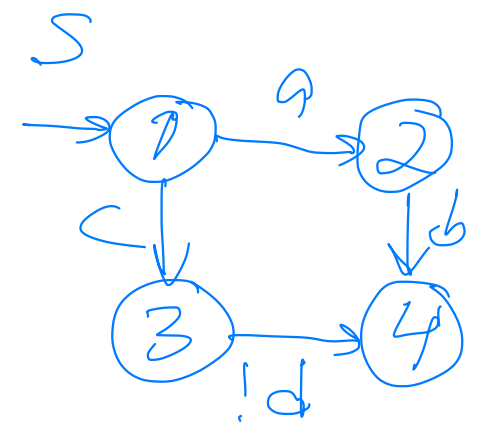
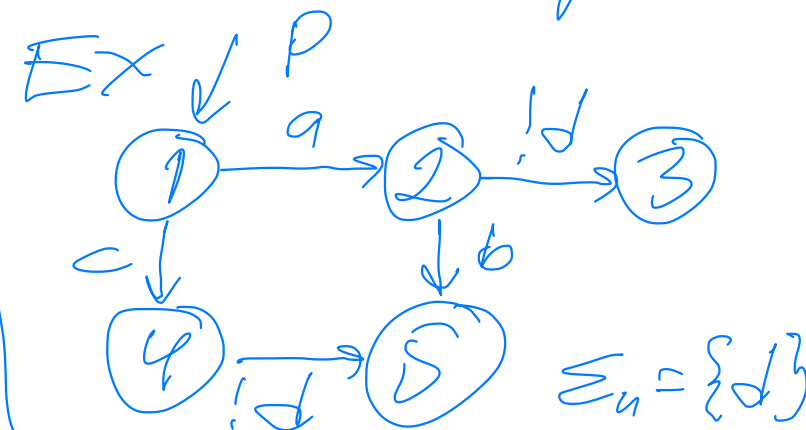
loop system $P||S$, and for

all uncontrollable events

$$\sigma_u \in \Sigma_u \cap \Sigma^S:$$

$$\delta^P(p, \sigma_u) \in Q^P \Rightarrow \delta^S(q, \sigma_u) \in Q^S$$

This means that S must be able to follow P when P executes an uncontrollable event $\sigma_u \in \Sigma_u \cap \Sigma^S$ in the closed loop system.



Note that d is uncontrollable

$(2, 2)$ is an uncontrollable state since S cannot execute d , which P is able to do.

Uncontrollable states

$$Q_{uc} = \{ \langle p, q \rangle \in Q_r^{P \parallel S} \mid \\ \exists \tau_u \in \Sigma_u \cap \Sigma^s \wedge \delta^P(p, \tau_u) \text{ exists } \wedge \delta^S(q, \tau_u) \text{ does not exist} \}$$

7. Supervisor synthesis

Plant $P = P_1 \parallel P_2 \parallel \dots \parallel P_n$

specification $SP =$

$$= SP_1 \parallel SP_2 \parallel \dots \parallel SP_m$$

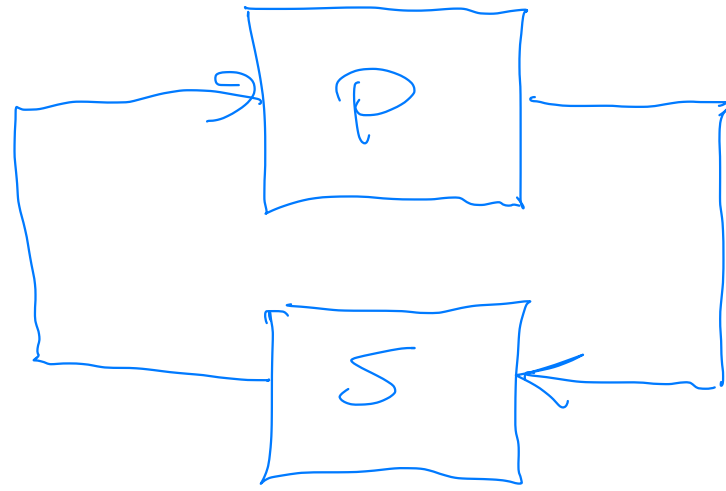
Total specification $S_0 = P \parallel SP$
is also a first candidate
for a possible supervisor S .

If S_0 has any blocking or uncontrollable states, such states are removed from S_0 to get a controllable and nonblocking supervisor $S \leq S_0$ (S is a subautomaton of S_0).

This supervisor S is also a model of the closed loop system, i.e. $S = P \parallel S$ when $\Sigma^s \subseteq \Sigma^P$

\therefore we need to define what we mean by subautomaton (\leq) and equality ($=$)

closed loop system



closed loop model

P/S

Sub-automaton

$$A = \langle Q^A, \Sigma^A, \delta^A, q_i^A, Q_m^A \rangle$$

$$B = \langle Q^B, \Sigma^B, \delta^B, q_i^B, Q_m^B \rangle$$

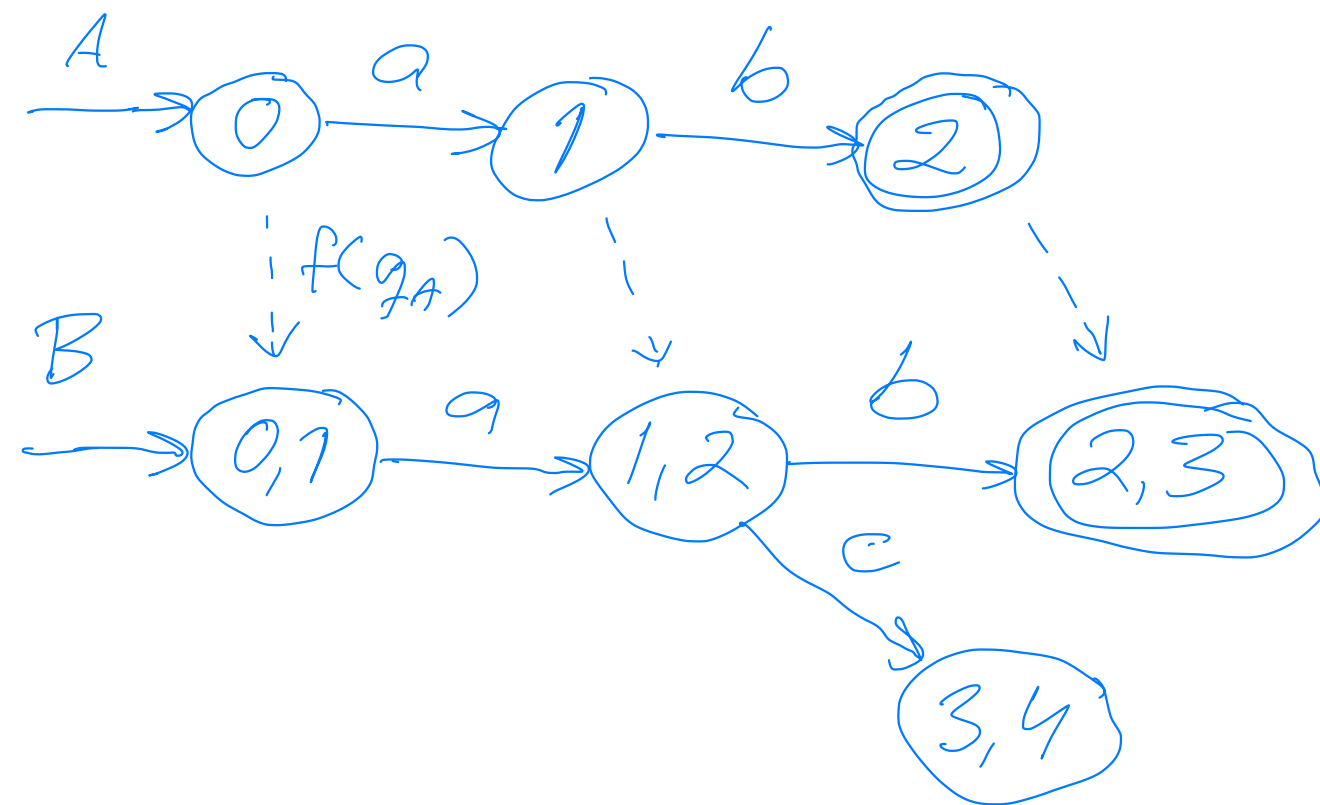
A is a subautomaton of B, written $A \leq B$ if

$$Q^A \subseteq Q^B, \quad \Sigma^A = \Sigma^B, \quad \delta^A \subseteq \delta^B$$

$$q_i^A = q_i^B, \quad Q_m^A \subseteq Q_m^B$$

The state names in Q^A can be different than corresponding state names in Q^B , if for all states $q_A \in Q^A$ there is a one-to-one mapping

(bijective function such that $q_B = f(q_A)$)



$A \leq B$ since

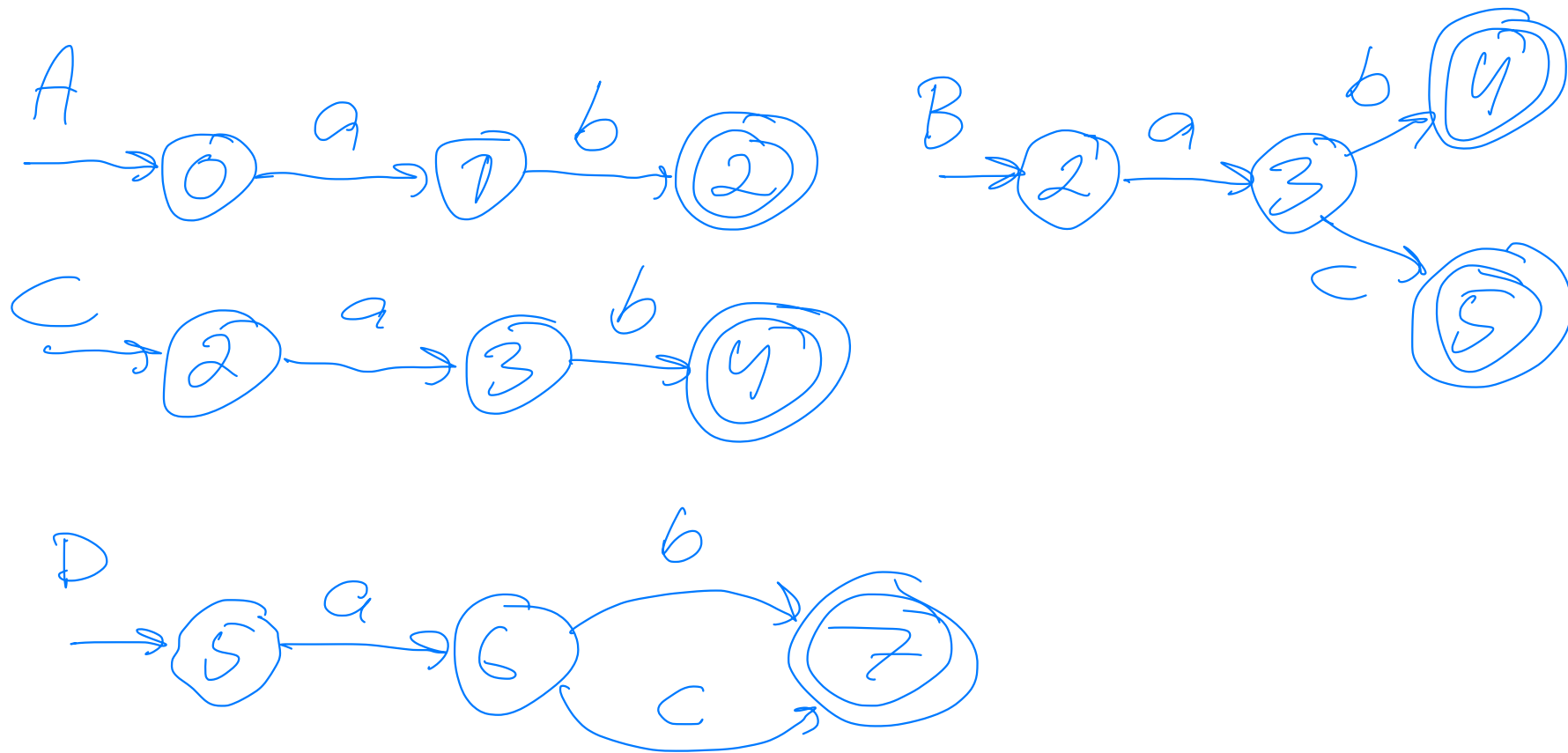
$$\begin{aligned} f(0) &= (0,1) \\ f(1) &= (1,2) \\ f(2) &= (2,3) \end{aligned}$$

Equivalence between automata

1) Language equivalence
 $\Sigma^A = \Sigma^B$

$$L(A) = L(B) \quad L_m(A) = L_m(B)$$

2) Structural equivalence
if $A \leq B$ and $B \leq A$
 $\Rightarrow A = B$



$$A \leq B \quad A \neq B$$

$$A \leq B \quad \text{when } \Sigma^A = \{a, b, c\}$$

$$L(B) = \overline{a(b+c)} = L(D)$$

$$L_m(B) = a(b+c) = L_m(D)$$

\therefore B and D are language equivalent but not structurally equivalent
 $B \neq D$.

Refinement

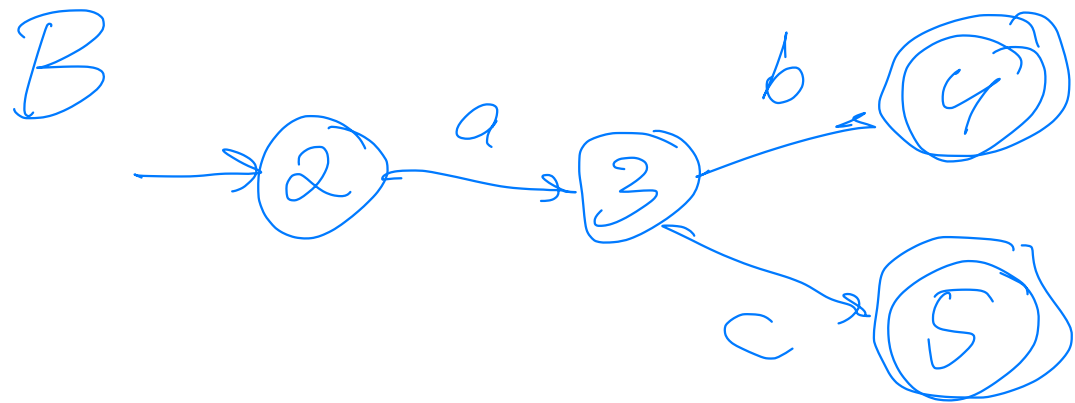
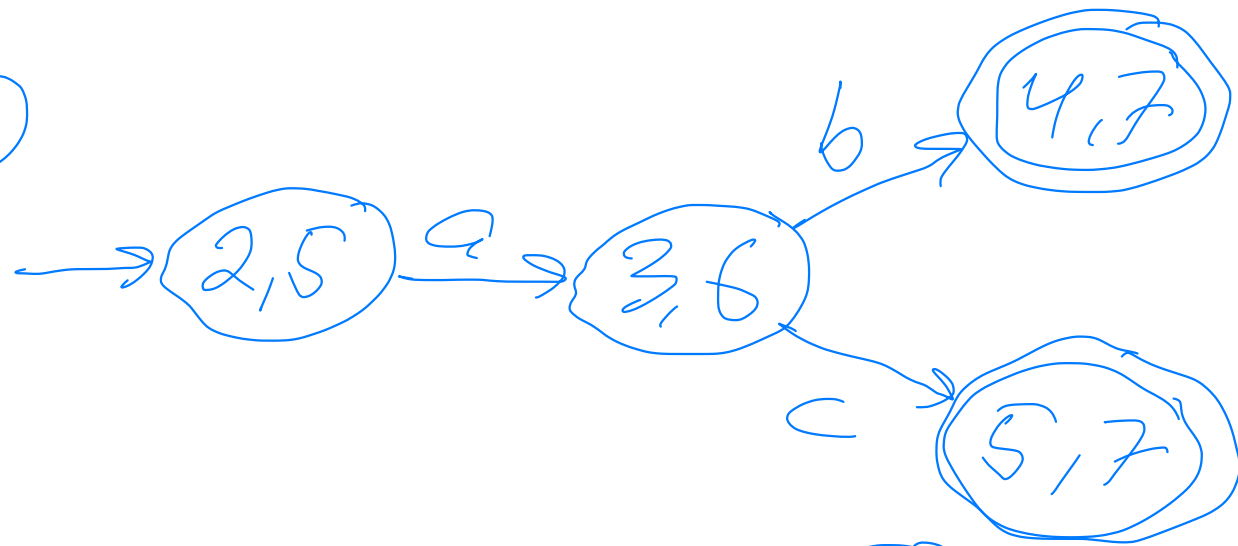
A refines B if $A \parallel B = A$

$$A \leq B \Rightarrow A \parallel B = A$$

But refinement does not guarantee subautomaton.

B refines D since

$B \parallel D$



$\therefore B \parallel D = B$

Lemma 7.1 If a supervisor S is constructed such that S is a subautomaton of $S_0 = P \parallel S_P$, i.e. $S \leq S_0$, then S refines P ,

i.e. $P \parallel S = S$

Proof: $S \leq S_0 \Rightarrow S = S_0 \parallel S$
i.e. S refines S_0 .

Now $S_0 = P \parallel S_P$

$S_0 \parallel P = (P \parallel S_P) \parallel P = P \parallel (P \parallel S_P)$
 $= (P \parallel P) \parallel S_P = P \parallel S_P = S_0 \Rightarrow$
 S_0 refines P ,

since also S refines S_0
 S refines S_0 refines $P \Rightarrow$
 S refines P $S = P \parallel S$