

EMCU

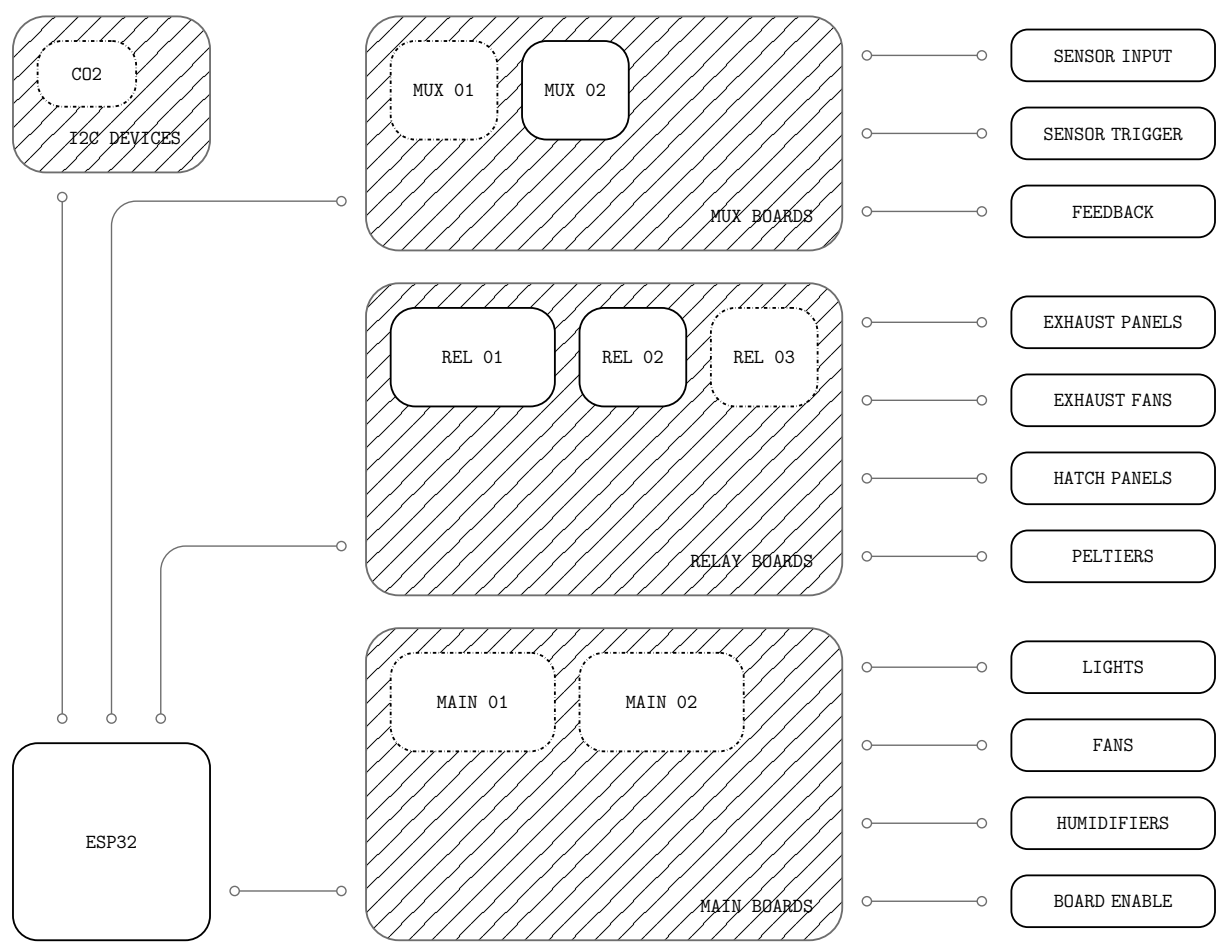


Figure 1.1