

Dept. of ECE
2017

Laboratory Report

Design with Analog Integrated Circuits (EET 3131)



Submitted by: -
Regd. No. –
Branch: -
Section:

INSTITUTE OF TECHNICAL EDUCATION AND RESEARCH
SOA UNIVERSITY
BHUBANESWAR, ORISSA

CONTENTS

| SL. NO. | OBJECTIVE OF THE EXPERIMENT | DATE OF EXPT. | DATE OF SUBMISSION | REMARKS |
|------------|-----------------------------|---------------------|-----------------------|---------|
| | | | | |
| | | | | |
| | | | | |
| | | | | |
| | | | | |