



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

KAKINADA - 533 003 , ANDHRA PRADESH, INDIA

## CONSOLIDATED MARKS MEMO / CREDIT SHEET



CMM. No.: **K 00124741**

Serial No.: **201007028879**

Name: **VYSYARAJU HARI KRISHNA**

Hall Ticket No.: **09NU1A0249**

Bachelor of Technology **Electrical and Electronics Engineering**

Name of the College: **VISAKHA INST OF TECH**

Name & Year of Final Exam: **B.Tech 2013**

Year of Admission: **2009 - 2010**

Class Awarded: **First Class**

S.No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S.No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL
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### I YEAR

1	APPLIED PHYSICS	17	42	59	4	2	BASIC ELECTRONIC DEV.&CKTS.	16	33	49
3	C PRG.& DATA STRUCTURES	16	31	47	4	4	ELECTRICAL CIRCUIT ANALYSIS	14	28	42
5	ENGINEERING DRAWING	17	72	89	4	6	ENGLISH	15	61	76
7	MATHEMATICAL METHODS	17	67	84	6	8	MATHEMATICS - I	20	45	65
9	COMPUTER PROGRAMMING LAB	25	41	66	4	10	ELECTRONIC DEV.&CKTS.LAB	25	49	74
11	ENGG.WORKSHOP&IT WORKSHOP	20	36	56	4	12	ENGLISH LANG.COMM.SKILLS LAB	20	38	58

### II YEAR

1	ELECTRICAL MACHINES - I	15	28	43	4*	1	CONTROL SYSTEMS	16	64	80
2	ELECTROMAGNETIC FIELDS	15	55	71	4	2	ELECTRICAL MACHINES - II	12	33	45
3	FLUID MECHANICS & HYDRAULIC MACHINE	13	49	62	4	3	ENVIRONMENTAL STUDIES	15	34	49
4	MATHEMATICS - III	16	49	65	4	4	LINEAR & DIGITAL IC APPLICATIONS	16	34	50
5	PULSE AND DIGITAL CIRCUITS	14	72	86	4	5	MANAGERIAL ECO. & FINANCIAL ANALYSIS	16	37	53
6	SWITCHING THEORY & LOGIC DESIGN	17	42	59	4	6	POWER SYSTEMS - I	18	48	66
7	ELECTRICAL CIRCUITS & SIMULATION (LAB)	23	44	67	2	7	ELECTRICAL MACHINES - I (LAB)	21	42	63
8	F M & H M (LAB)	16	38	54	2	8	IC & PULSE AND DIGITAL CIRCUITS (LAB)	25	49	74

### III YEAR

1	COMPUTER SYSTEM ORGANIZATION	18	34	52	4	1	DIGITAL SIGNAL PROCESSING	12	33	45
2	ELECTRICAL MACHINES-III	15	37	52	4	2	INSTRUMENTATION	16	47	63
3	ELECTRICAL MEASUREMENTS	15	34	49	4	3	MANAGEMENT SCIENCE	15	31	46
4	LINEAR SYSTEM ANALYSIS(NEW)	17	58	75	4	4	MICRO PROCESSORS AND MICRO CONTROL	15	28	43
5	POWER ELECTRONICS	18	28	46	4	5	SWITCH GEAR & PROTECTION	14	45	59
6	POWER SYSTEMS-II	17	60	77	4	6	VLSI DESIGN	17	31	48
7	CONTROL SYSTEMS AND SIMULATION LAB	21	39	60	2	7	ADVANCED ENGLISH COMMUNICATIONS	16	38	54
8	ELECTRICAL MACHINES LAB - II	21	49	70	2	8	POWER ELECTRONICS AND SIMULATION	21	46	67

### IV YEAR

1	ELECTRICAL DISTRIBUTION SYSTEMS	19	51	70	4	1	COMPREHENSIVE VIVA	0	80	80
2	HVDC TRANSMISSION	17	41	58	4	2	DIGITAL CONTROL SYSTEMS	11	66	77
3	NEURAL NETWORKS AND FUZZY LOGIC	18	48	66	4	3	PROGRAMMABLE LOGIC CONTROLLERS	9	47	56
4	POWER SEMICONDUCTOR DRIVES	15	55	70	4	4	UTILIZATION OF ELECTRICAL ENERGY	17	53	70
5	POWER SYSTEM ANALYSIS	15	48	63	4	5	SEMINAR	42	—	42
6	POWER SYSTEM OPERATION AND CONTROL	17	40	57	4	6	INDUSTRY ORIENTED MINI PROJECT	—	42	42
7	ELECTRICAL MEASUREMENTS LAB	20	45	65	2	7	PROJECT WORK	34	153	187
8	MICROPROCESSORS AND MICROCONTROLLERS	25	48	73	2					

Number of Credits registered for: **224**

Aggregate Marks Secured for best: **216 Credits 3648 out of 5350 ( 68.19 %)**

Date of Declaration of Result: **May 2013**

(See overleaf for Instructions)

(\*Courses registered but not countered for calculation of aggregate)

28/8/2013 **CONTROLLER OF EXAMINATION**

*Cham*