

# RESUME

**Mr Chinthakindi Kiran Kumar**

B.Tech, M.Tech, (Ph.D)

Assistant Professor

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**Career Objective:** Enthusiastic and driven professional seeking a challenging role in the education technology industry to leverage my skills in technology and passion for education.

## **Educational Qualification:**

- **Ph.D Work** Submitted to Lovely Professional University, Punjab
- **M.Tech (VLSID)** degree from Nishitha Institute of Science & Tech, Hyderabad with 72.38%
- **B.Tech (ECE)** degree from Syed Hashim College Science & Tech, Hyderabad with 63.41%
- **Intermediate** from Aditya Junior College, Hyderabad with 84.9 %
- **SSC** from IMSS School, Karimnagar with 81.1 %

**Experience:** Total of 18 years' Experience of Academic and Administrative Positions.

- Working as Assistant Professor in Malla Reddy College of Engineering & Technology from 1<sup>st</sup> July 2014 till date.
- Worked as Assistant Professor in Walchand Institute of Technology, Solapur from June 2013 to May 2014.
- Worked as Assistant Professor & Placement Coordinator in Geetanjali Institute of Science & Technology, Hyderabad from August 2011 to June 2013.
- Worked as Assistant Professor & Examination Branch In charge in Global Institute of Engineering & Technology, Hyderabad from June 2006 to May 2011.

## **Nptel Courses Completed:**

- Computer Networks and Internet Protocol, 12 weeks course, Elite Silver, Jan-Apr 2024
- Deep Learning, 12 weeks course, Elite Silver, Jan-Apr 2024
- Introduction to Internet of Things, 12 weeks course, Elite Silver, Jan-Apr 2024

## **Honors & Awards:**

- Received best Organizer Award in 2020.
- Received Best Teaching award in 2018.
- Received Rastrapathi Puraskar in 1999.
- Delivered guest lecture on Synchronous & Asynchronous Sequential Circuits at SVIT ON 3<sup>rd</sup> November 2017
- Achieved Amateur Station Operator Certificate.
- Member in the Syllabus Setting Workshop Sholapur University 2014.
- Appointed as an Examiner/Moderator in subject Circuits & Networks at Examination SE-I old & New (E&TC), 2013.

## **Contribution to Colleges & Department:**

- Worked as Coordinator thrice for the International Conferences ICSCSP, MRCET
- Worked as Examination Branch in-charge.
- Worked as Social Media Coordinator for Branding of the college.
- Worked as Placement coordinator and organized various drives.
- Worked as Projects coordinator & Year Coordinator.
- Worked for all Accreditations NBA, NAAC and UGC Autonomous
- Worked as JNTUH Faculty Registration coordinator and taking care faculty ratio.
- Organized Code infinity 24 Hackathon program in 2022.
- Preparation of laboratory manuals and course files.
- Organized all events like annual day, technical fest etc. .
- Making use of self-made ideology to gain the positive results and obtaining the goals of the organization.

## **Papers Published:**

- Adapting to the Dark: A Novel Adaptive Low Light Illumination Correction Algorithm for Video Sequences in Wireless Communications, International Journal of Electrical and Electronics Research 11.Special issue (2023): 01-09.
- Transform Domain-Based Perceptual Detection and Reduction of Blocking Artifacts in International Journal on Recent and Innovation Trends in Computing and Communication , Volume: 11 Issue: 9 ,2023.
- Advancements in Object Detection and Tracking Techniques: A Comprehensive Review in Variable Illumination Conditions, Intelligent Circuits and Systems for SDG 3–Good Health and well-being: 628-635.
- An Effective Approach for Object Detection Using Deep Convolutional Networks,"(BHARAT-2022), IEEE Xplore , pp. 37-42, 2022
- A Brief Study on Object Detection and Tracking, Journal of Physics, ICICS, 2022
- Motion detection and tracking of surveillance videos under distorted environments, Taylor & Francis , ICICS, 2020,
- Deep Network Architectures for Object Detection and Tracking: A Review , Accepted Springer conference ICIMES 2022,
- Implementation of Modified Advanced Encryption Standard for Chip Level Cryptography, Advanced Science Letters 26:1207, November 2020
- A Low-Power FinFET-Based Miller Op-Amp Design with gm Enhancement and Phase Compensation, Springer Singapore, pp.291-298, January 2020
- Multifunctional Self -balancing Mobile Platform with Smart phone, IJAREEIE Vol.7, Issue 6, June 2018.
- Multifunctional Self -balancing Mobile Platform with Smart phone, IJAREEIE Vol.7, Issue 6, June 2018.
- An Efficient Design and Implementation of Coal Mine Safety System Using IoT. IJSR Volume 7 Issue 6, June 2018.

## **Patent's**

- Patent on ““System for 3D Object Detection and Recognition Using Stereo Images and Deep Learning in Autonomous Vehicles” Application No.202541001372 A, Publication Date: 17/01/2025.
- Patent on “Novel Deep Learning-Based Method for Real-Time Image Restoration and Noise Reduction in Embedded Image Processing Systems”, Application No.202441088307 A, Publication Date: 22/11/2024.
- Patent on “An Intelligent Black Gram Crop Leaf Disease Identification And Classification Robot”, Application No.202211075132 A, Publication Date : 30/12/2022

## **Workshops & FDP's**

- Completed AICTE Training And Learning (ATAL) Academy Faculty Development Program on Next Gen Solutions For Medical Challenges Powered By Ai Based Intelligent Systems at MRCET from 16/12/2024 to 21/12/2024.
- Completed workshop on chatgpt & AI Microsoft office on 13-11-2024.
- Completed course on MATLAB Onramp from mathworks Training Services on 10th September 2020.
- Completed course on Generate Synthetic Images with DCGANs in Keras offered through Coursera on 14th may 2020.
- Completed course on Linear Regression with Python offered through Coursera on 15th may 2020.
- Completed course on Image Data Augmentation with Keras offered through Coursera on 16th may 2020
- Participated in One Day Workshop on C and C++ held at MRCET, Hyderabad on 29 February 2020
- Participated one week faculty development program on Embedded systems & interfacing hands-on Organized by Electronics & ICT Academics from 10th June to 14th June 2019.
- Participated one week faculty development program on VLSI chip design Hands on using open source EDA, Organized by Electronics & ICT Academics from June 10th -14th 2019.
- Participated in two-day workshop on Research Process flow and implementation Mechanism–A thought Process on 10th & 11th May 2018.
- Participated in two days faculty development program on Multimedia signal transmission through Wireless Communication on June 12-13 ,2018.
- Participated in two-day seminar on Research Scholar Summit on 3rd & 4th December 2018.
- Attended workshop on Embedded Linux on Zynq-7000 SOC using Vivado in association with CoreEl Technologies and Digilent ., USA 25th September 2015.
- Participated in two days faculty development program on Vivado design flow using Zynq Architecture on 18th & 19th September 2017.
- Participated in two-week ISTE STTP conducted by IIT Kharagpur, on Introduction to design of Algorithms from 27th April to 30th May 2015.
- Participated in two-week ISTE STTP conducted by IIT Kharagpur, on CMOS Mixed signal and radio frequency VLSI Design from 26th December 2016 to 04th February 2017.
- Presented a paper in 8th International Conference on Innovation in ECE, ICIECE-2019.
- Completed training on Object oriented analysis & design using UML with essential of rational software architecture, IBM Workshop conducted by Mapleton from 25th November to 28th November 2013.
- Completed training on Rational Functional tester & Rational quality manager, IBM Workshop conducted by Mapleton from 29th November to 2nd December 2013.
- Participated in all India GK tests and other competitive exams at school Level.

## **SKILLS SET:**

- Ms-Office
- Matlab
- Strong communication and interpersonal skills
- Experience in account management, operations, and educational partnerships
- Project management and coordination abilities
- Ability to work collaboratively and independently
- Knowledge of curriculum integration and educational program development
- Capable to present the matter in a simple and impressive manner
- Possess excellent presentation skills
- Able to deal with different types of students
- Good at research and making new discoveries related to the subject

**Personal Details:**

Date of Birth : 04/8/1982

Father's Name : Babu Ch

Marital Status : Married

Nationality : Indian

Languages known : Telugu, English, Hindi

Address for Communication: 1-5-639/3/1 , Srinagar Colony , Near VBR Garden , Old Alwal

**Declaration:**

I hereby declare that the above-furnished information is true to the best of my knowledge.

Place: Hyderabad

Date:

(Kiran Kumar)