

# Diogenes: Lightweight Scalable RSA Modulus Generation with a Dishonest Majority

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# What is an RSA Modulus?

$$N = p \cdot q$$

**Biprime** - product of exactly two primes

# Why? RSA History

- 1977 - RSA Public-Key Encryption
- 1999 - Paillier Public-Key Encryption
- 2001 - CRS for UC setting
- 2018 - Verifiable Delay Functions (VDF)



Ethereum 2.0 = Proof of Stake!

# Why? VDF Construction

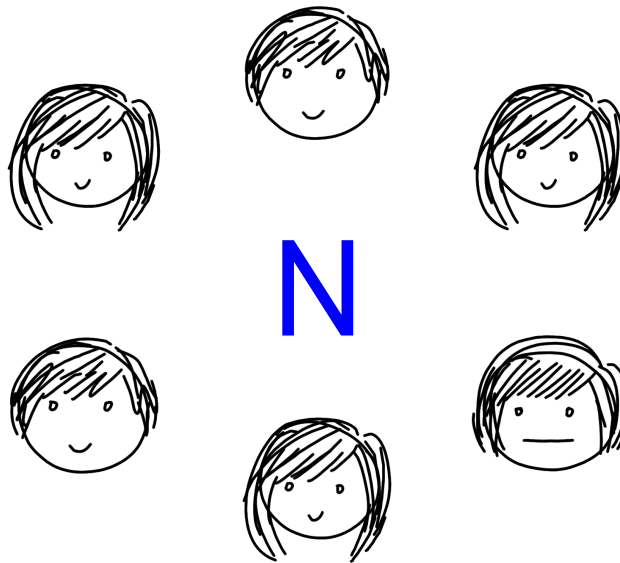
- 1996 - Rivest-Shamir-Wagner timelock puzzle

$$y = g^{2^T} \bmod N$$

- 2018 - VDF constructions by Pietrzak, Wesolowski

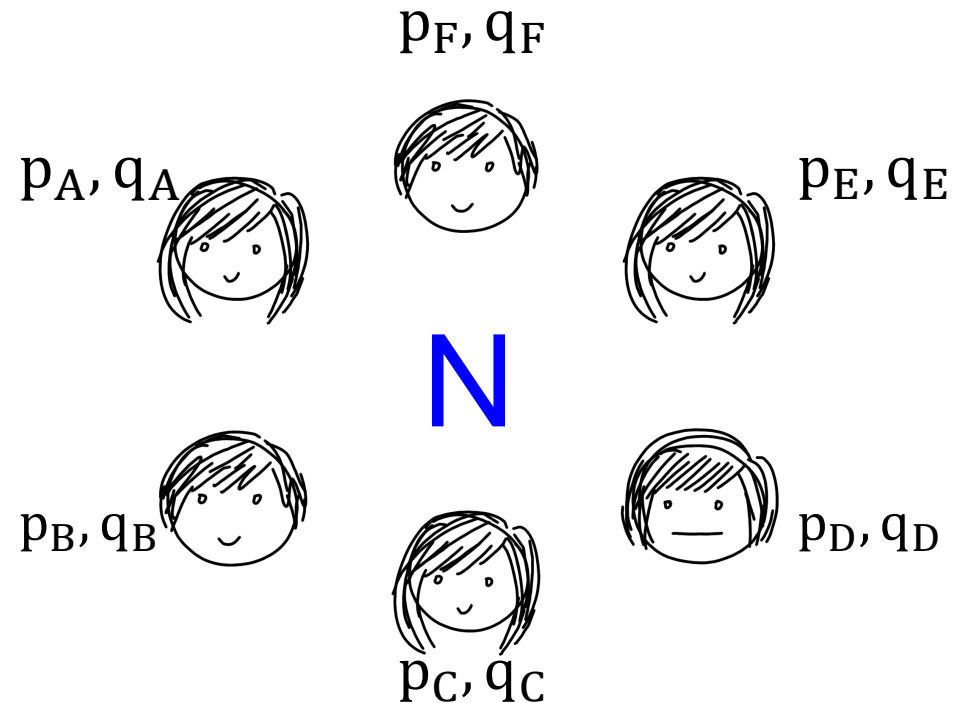
# Goal

Parties interact to jointly sample a bi-prime  
modulus  $N$



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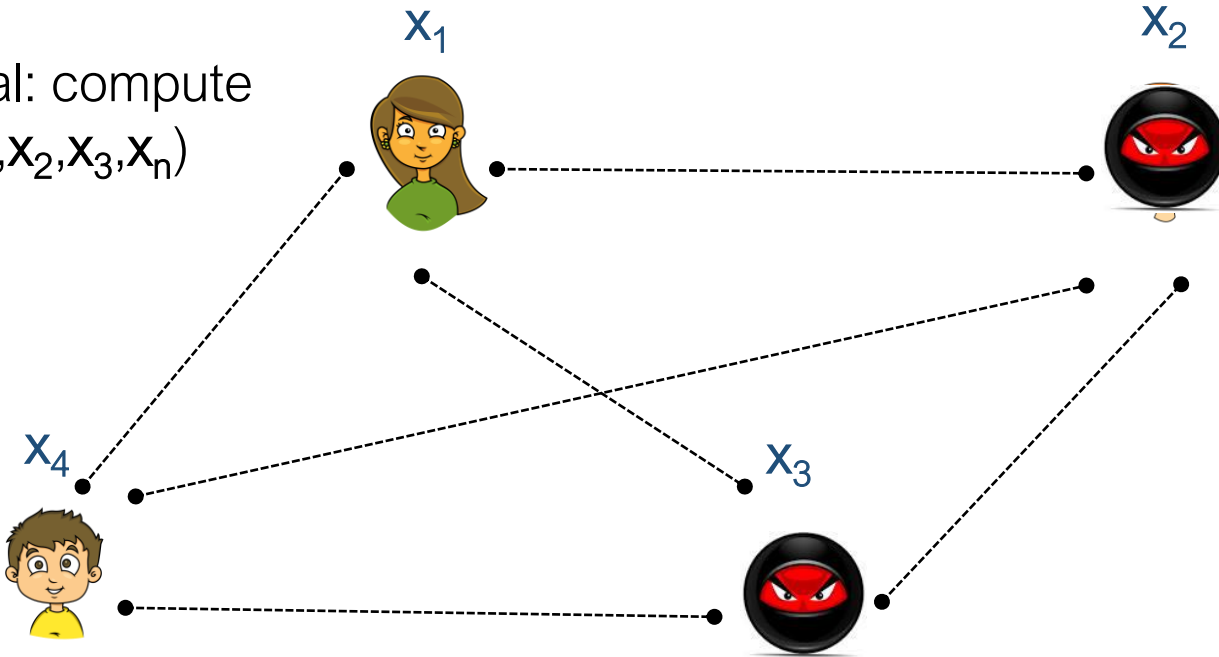
# Goal

1024 parties  
+  
(n-1) active security

Need just 1 honest participant....

# Secure Multi-Party Computation (MPC)

Goal: compute  
 $f(x_1, x_2, x_3, x_n)$



MPC is useful for  
many applications

- Auctions with private bids
- Privacy-preserving data mining
- Private health records
- Cryptographic key protection
- Secure statistical analyses
- Smart city research – gender inequality
- Private blockchains
- ...

Passive vs. Active  
Honest majority vs. Dishonest majority



# Previous Works: Overview

Milestone	Work	Adversary	Parties	Corruption Threshold
First Work	[BF97]	Passive	$n \geq 3$	$t < n/2$
	[FMY98]	Active	$n$	$t < n/2$
	[PS98]	Active	2	$t = 1$
Based on OT	[Gil99]	Passive	2	$t = 1$
	[ACS02]	Passive	$n$	$t < n/2$
	[DM10]	Active	3	$t = 1$
	[HMRT12]	Active	$n$	$t < n$
	[FLOP18]	Active	2	$t = 1$
	[CCD+20]	Active	$n$	$t < n$

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# Previous Works: Implementations

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# State-of-the-Art

	[FLOP18]
RSA Modulus Size	2048 bits
Implementation	Passive
Num Parties	2
Party Spec	8 GB RAM 8 cores CPU
Bandwidth	40 Gbps
Comm. (Per-Party)	>1.9 GB
Time	35 sec (8 threads)

# State-of-the-Art vs. Our Target

	[FLOP18]	Our Challenge
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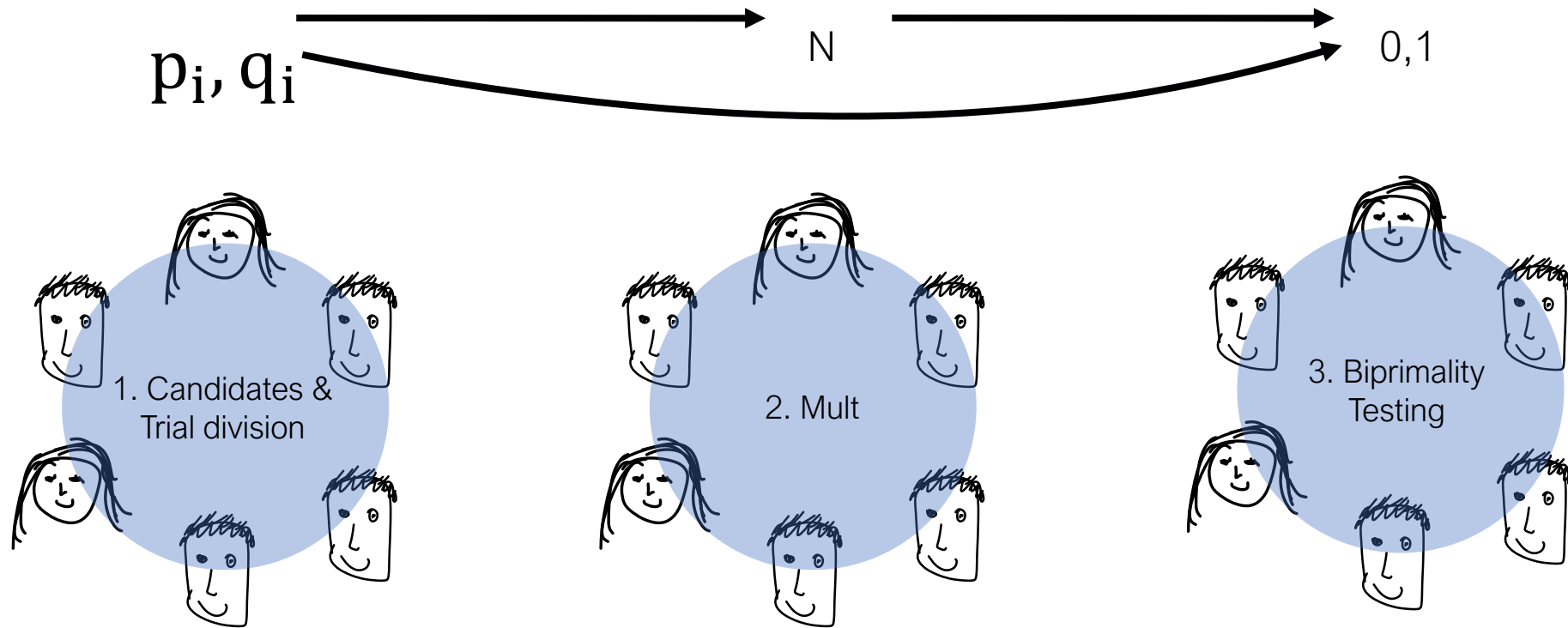
# Protocol Blueprint

Step 1: Design protocol secure against  
passive adversary

Step 2: Compile to security against  
active adversary

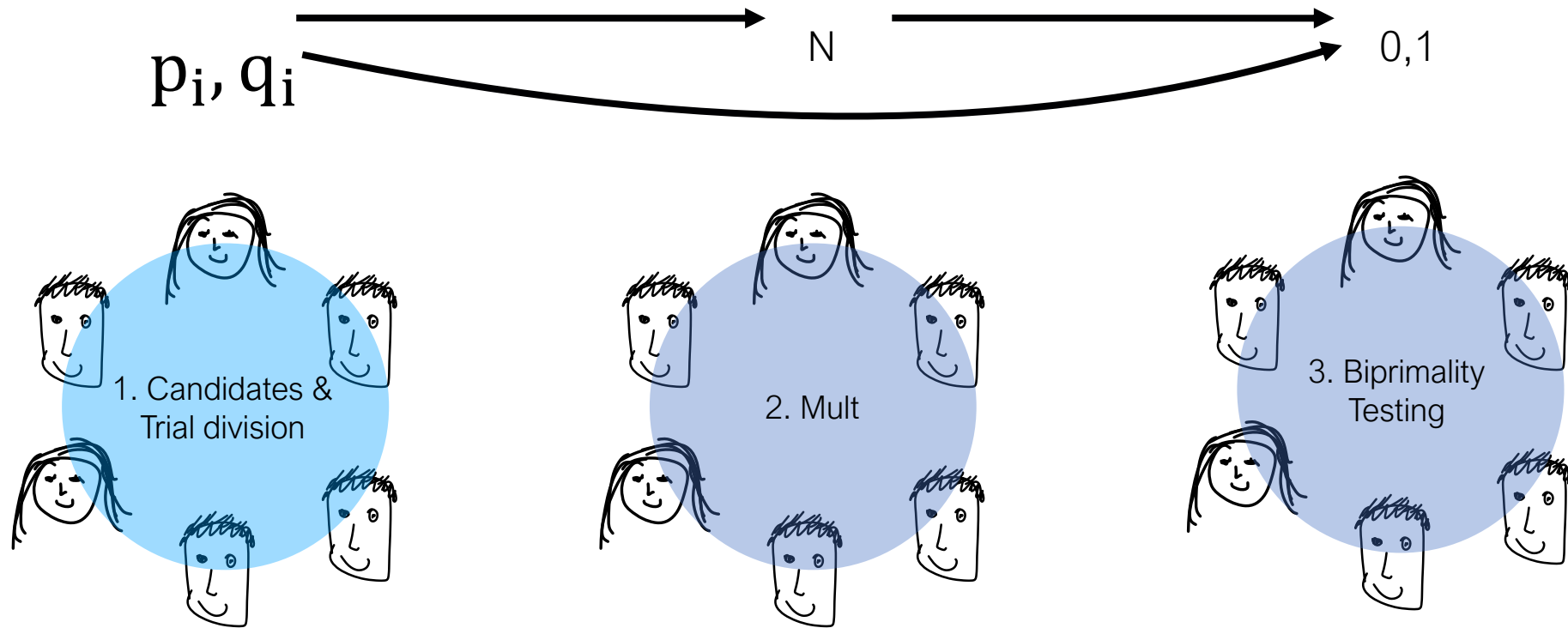
# Step 1: Scalable Passive Protocol

# Boneh-Franklin Framework<sub>[BF97]</sub>



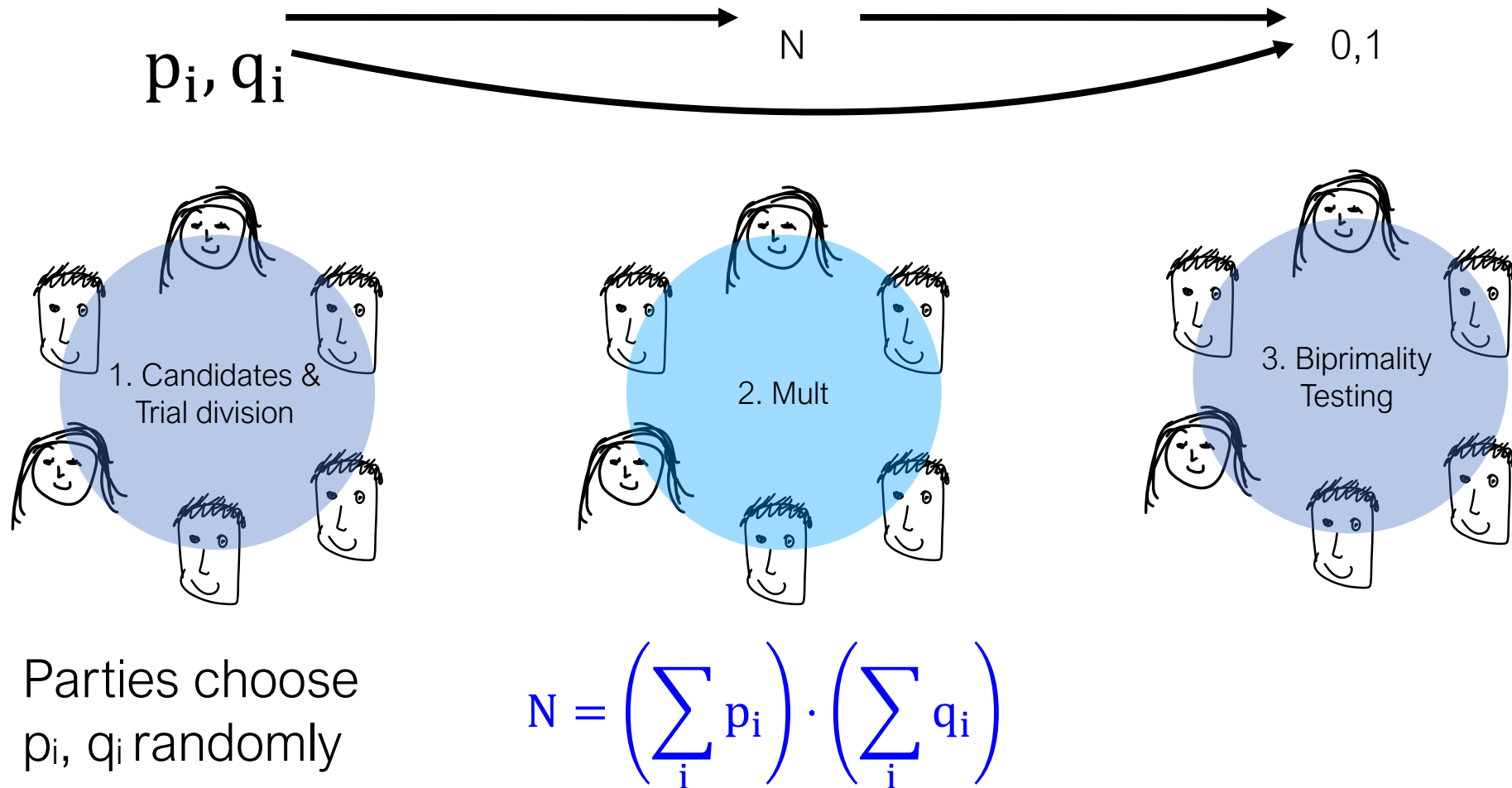


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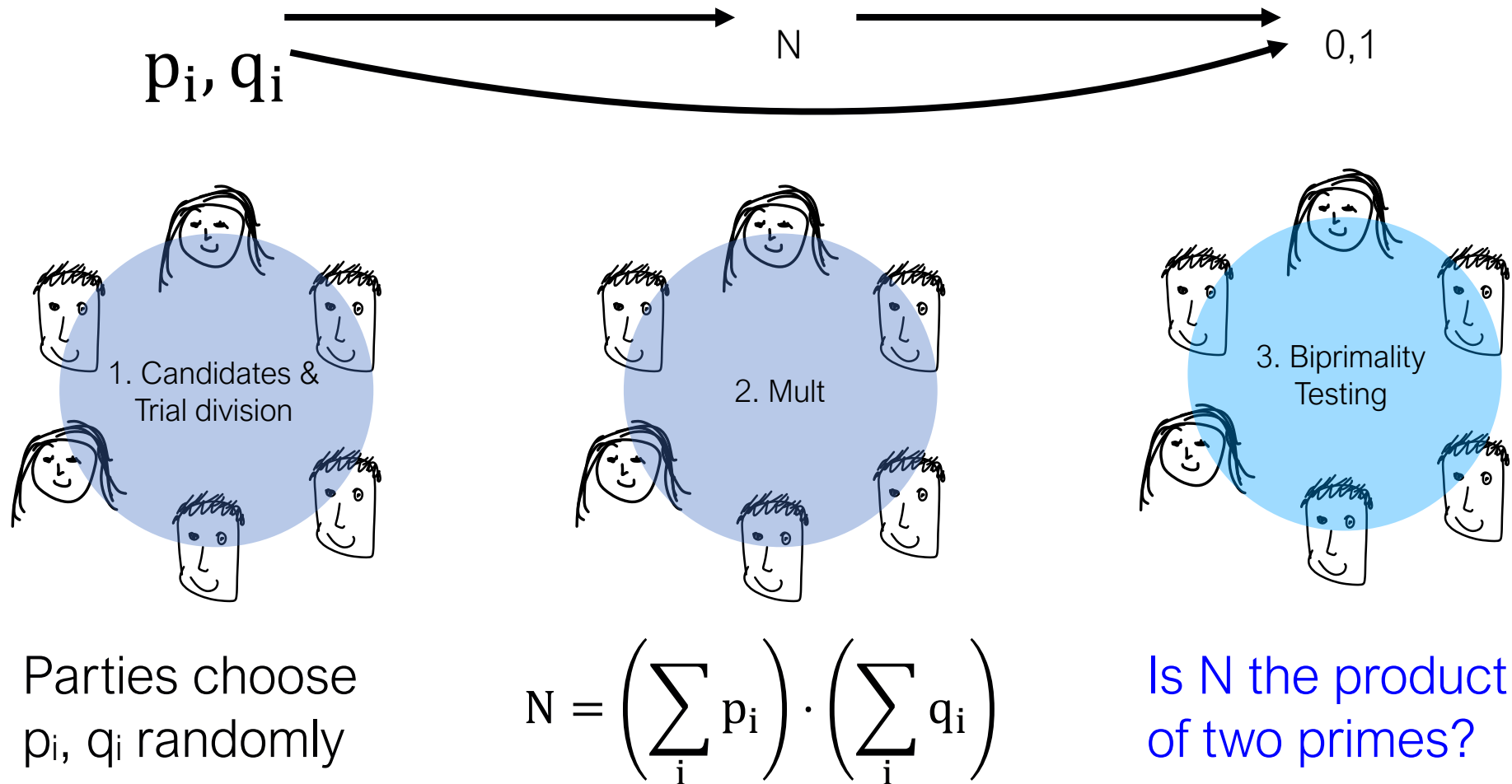


Parties choose  $p_i, q_i$  randomly

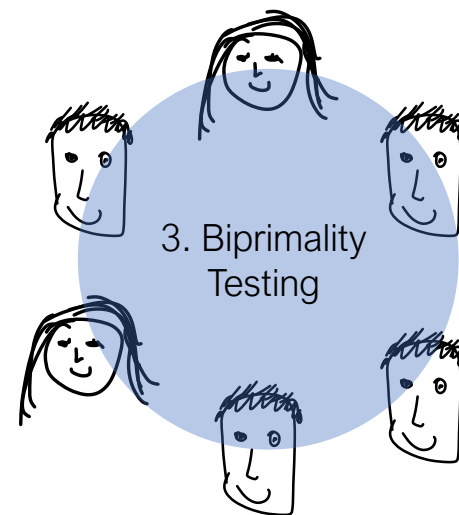
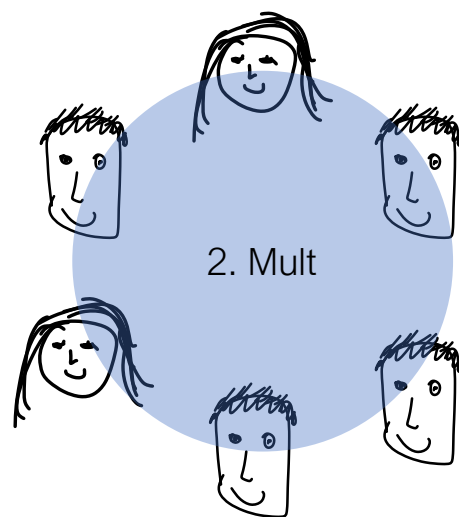
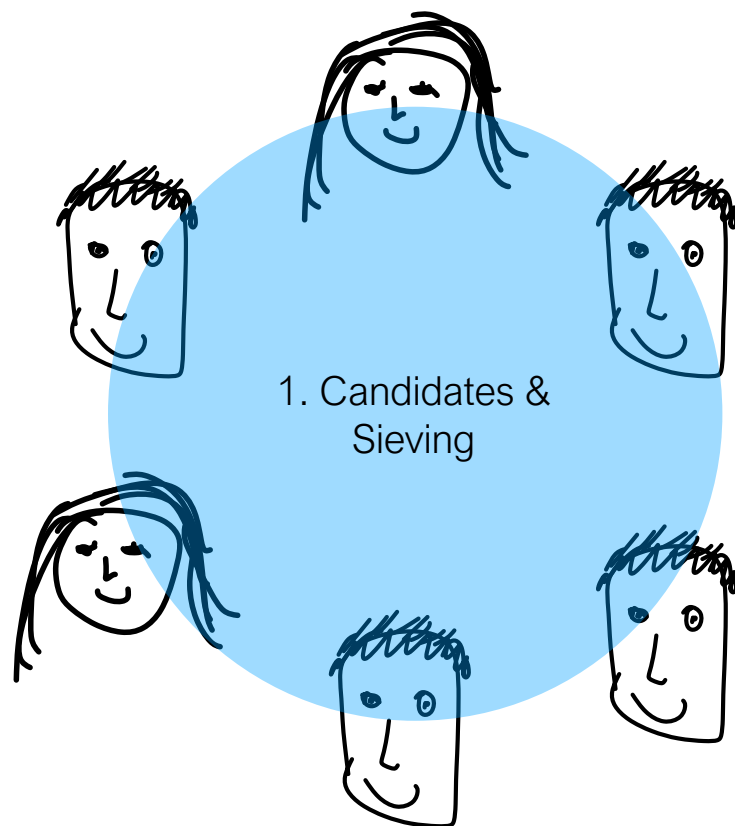
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# Boneh-Franklin Framework<sub>[BF97]</sub>



# Start with Sieving Trick



# Candidate Naïve Sampling

A = randomly sampling a 1024-bit prime

B = number is odd

$$\Pr[A|B] \approx \left(\frac{1}{500}\right)$$

$$\Pr[\text{sample biprime}|B] \approx \left(\frac{1}{500}\right)^2$$

Need 250k samples in expectation



# Candidate Trial Division [Bru50]

A = randomly sampling a 1024-bit prime

B = sieve up to 863, the 150th prime

$$\Pr[A|B] \approx \left(\frac{1}{60}\right)$$

$$\Pr[\text{sample biprime}|B] \approx \left(\frac{1}{60}\right)^2$$

Need 3600 samples in expectation



# Candidate Trial Division: Prior Works

1. Construct  $p$  and  $q$
2. Distributed sieving
3. If both pass, multiply

HMRTN12 → El Gamal

FLOP18 → 1-out-of- $k$  OT

Pairwise

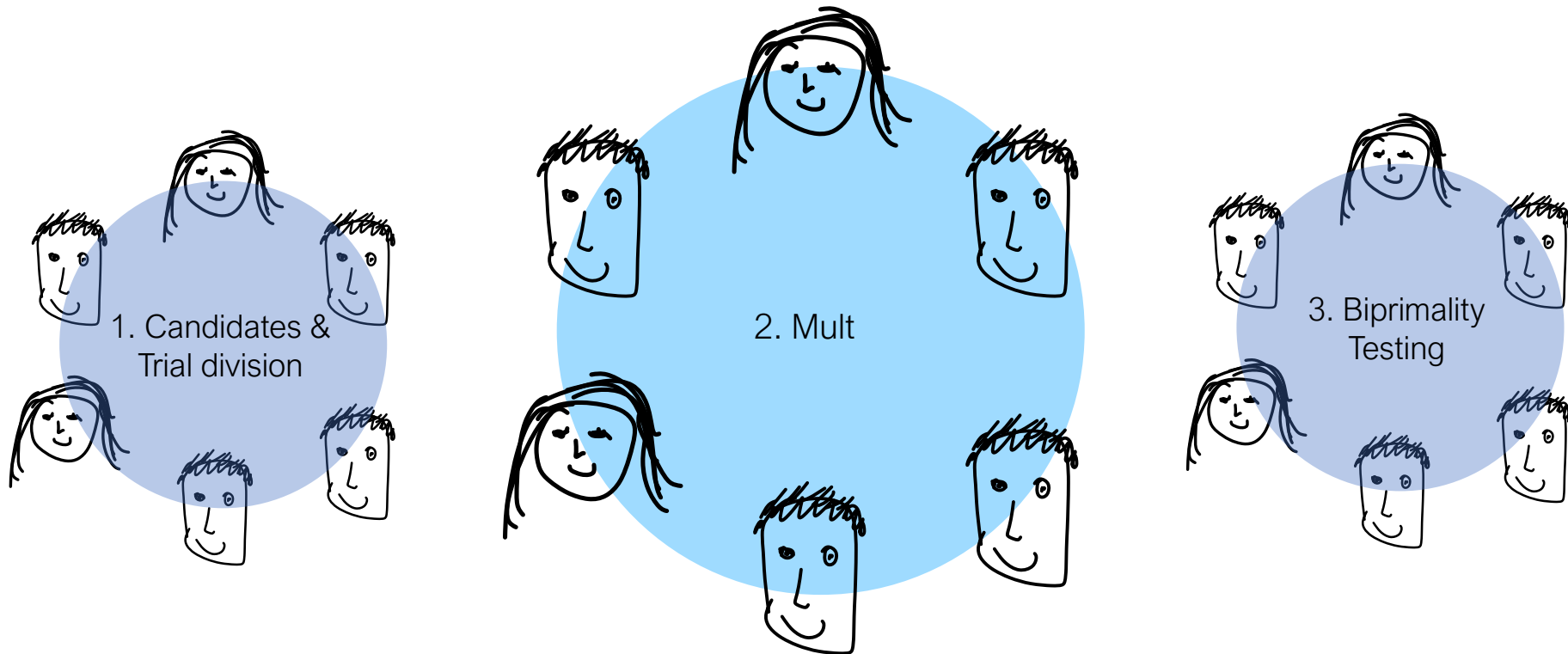
communication  
channels

# Our Approach

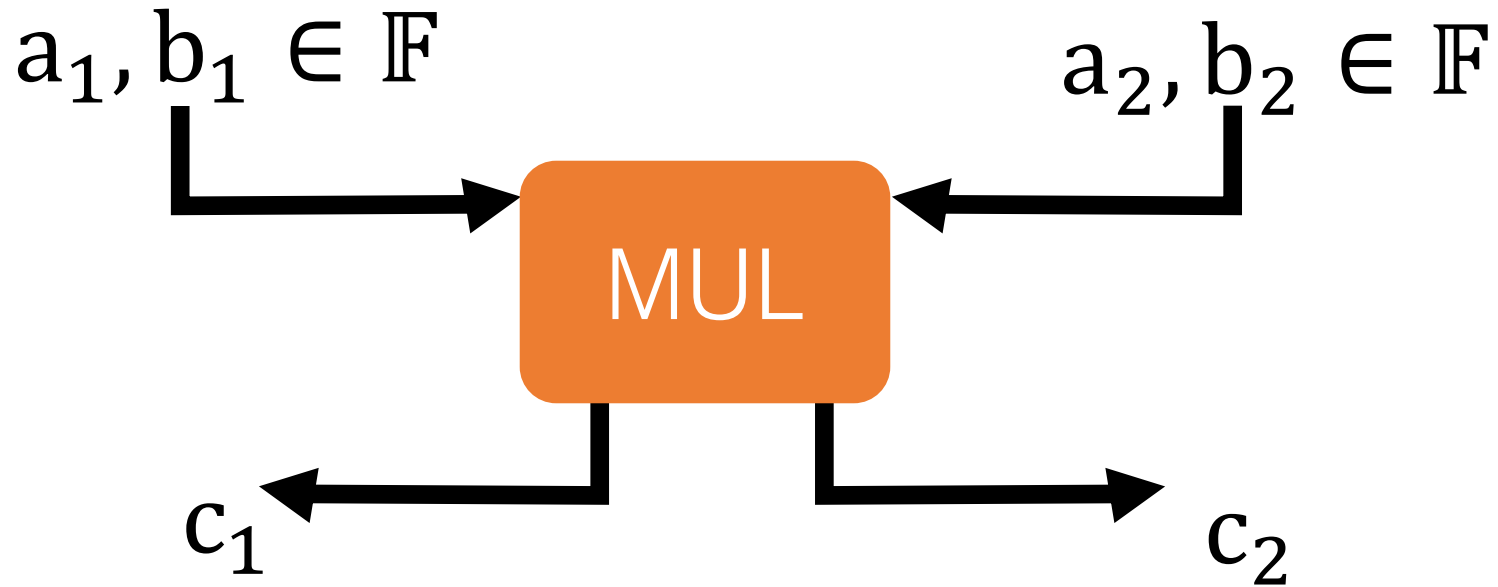
Sieve first,  
construct later<sub>[CCD+20]</sub>



# Secure Multiplication



# Secure Multiplication



$$c_1 + c_2 = (\sum a_i) \cdot (\sum b_i)$$

# Our Approach: Threshold AHE

- Distributed key generation

Public key:  $PK$     Secret keys:  $sk_1, \dots, sk_n$

- Encryption

$$Enc_{PK}(m)$$

- Distributed decryption

$$m = Dec_{sk_1}(c) + \dots + Dec_{sk_n}(c)$$

# Our Approach: Threshold AHE

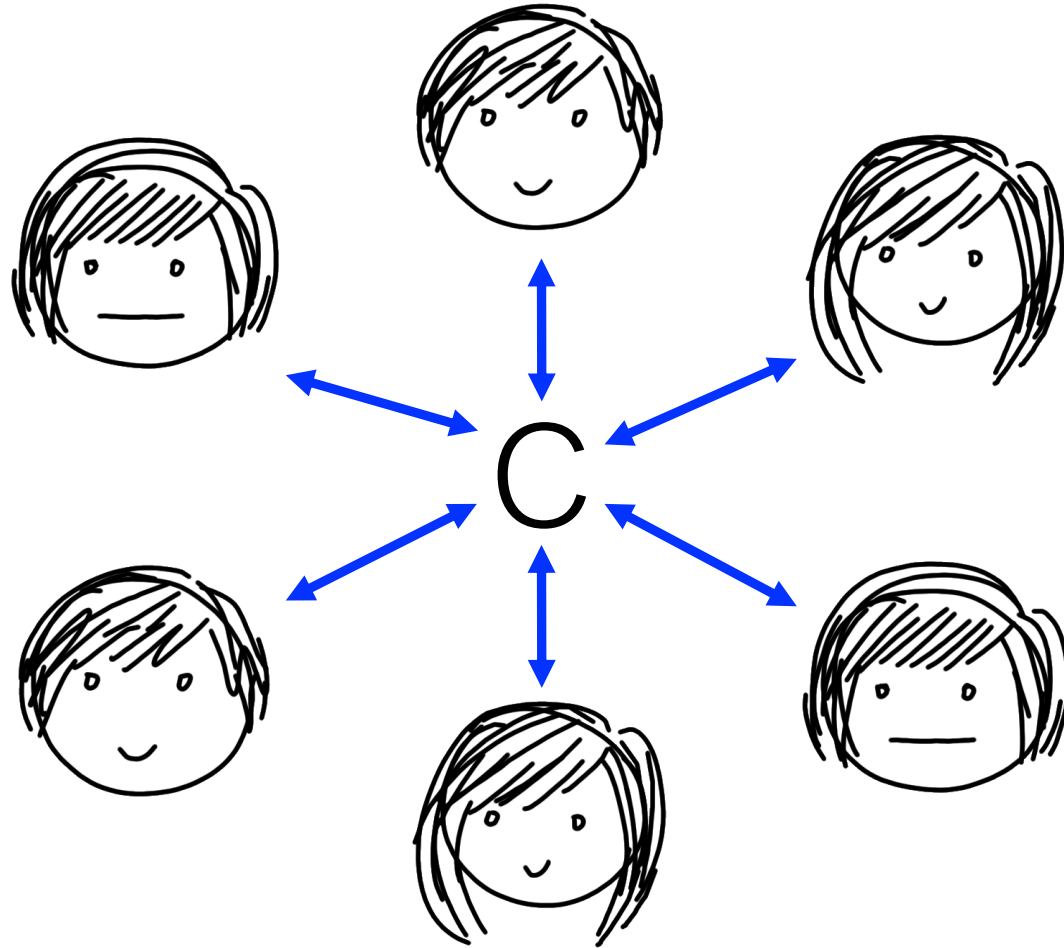
- Addition under encryption

$$\text{Enc}_{PK}(m_1) + \text{Enc}_{PK}(m_2) = \text{Enc}_{PK}(m_1 + m_2)$$

- Scalar multiplication under encryption

$$a \cdot \text{Enc}_{PK}(m) = \text{Enc}_{PK}(a \cdot m)$$

# Our Approach: Untrusted Coordinator



Performs **only**  
**public** operations

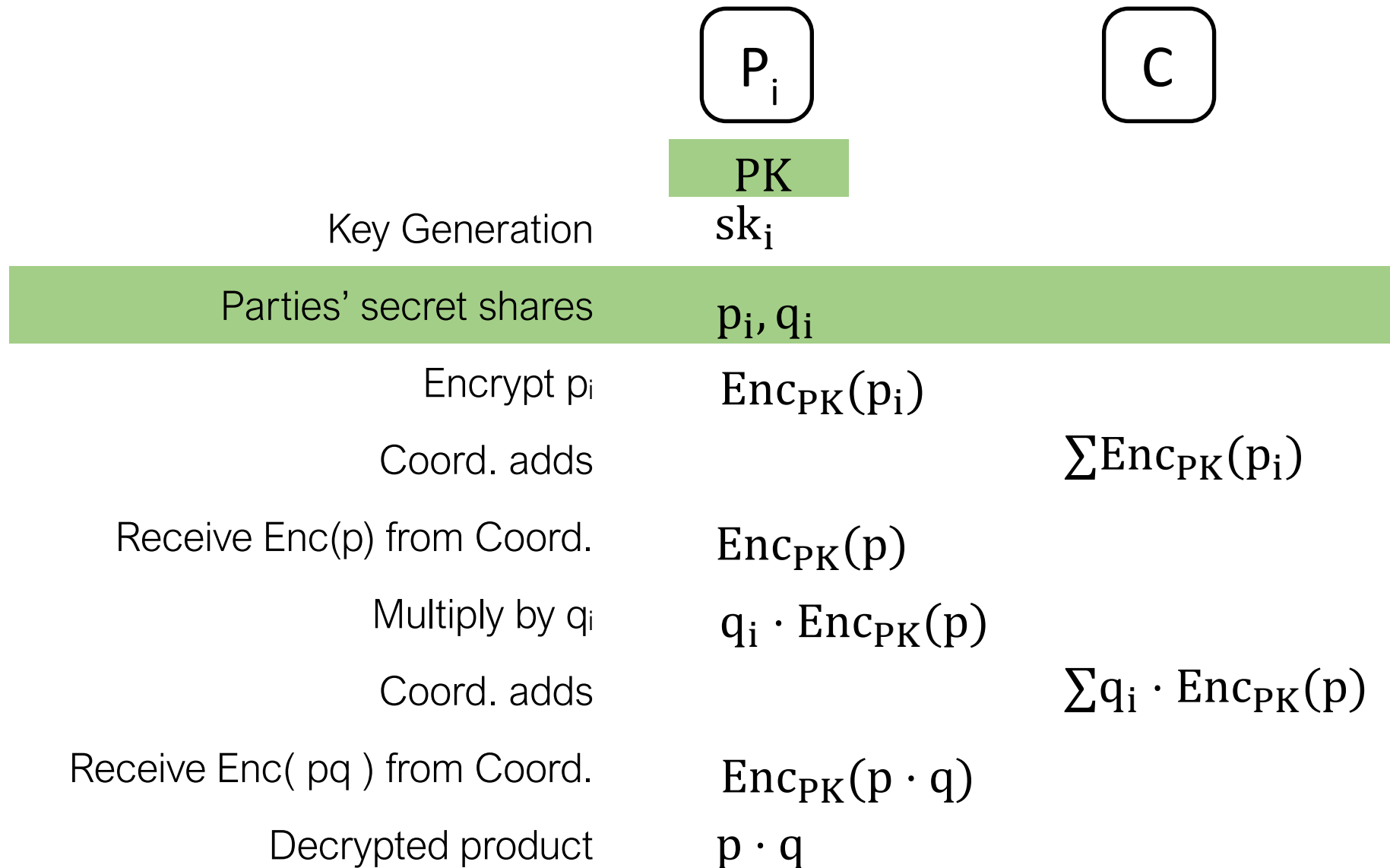
# Our Approach: Threshold AHE

$P_i$

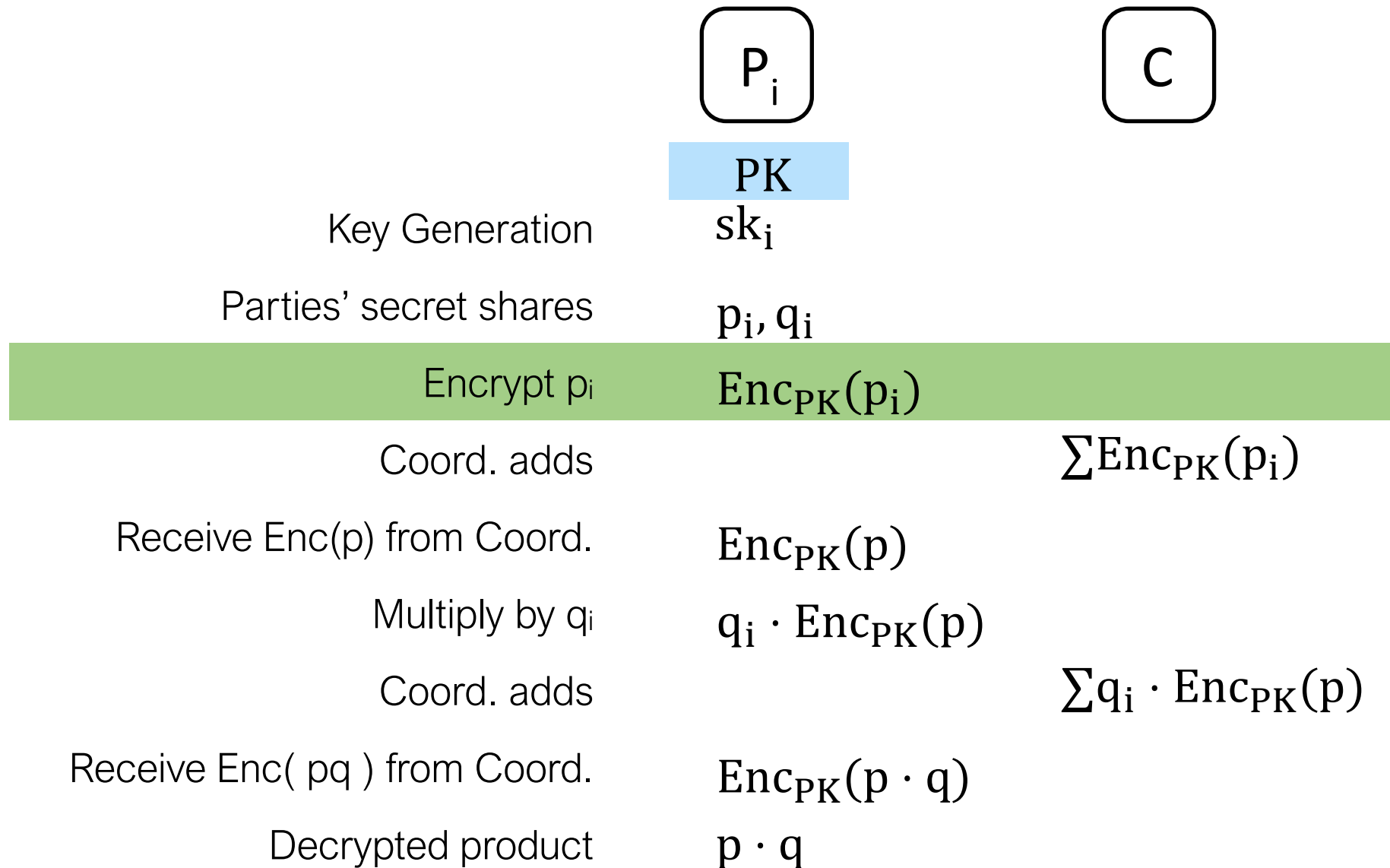
$C$

Key Generation	$sk_i$	
Parties' secret shares	$p_i, q_i$	
Encrypt $p_i$	$Enc_{PK}(p_i)$	
Coord. adds		$\sum Enc_{PK}(p_i)$
Receive $Enc(p)$ from Coord.	$Enc_{PK}(p)$	
Multiply by $q_i$	$q_i \cdot Enc_{PK}(p)$	
Coord. adds		$\sum q_i \cdot Enc_{PK}(p)$
Receive $Enc(p \cdot q)$ from Coord.	$Enc_{PK}(p \cdot q)$	
Decrypted product	$p \cdot q$	

# Our Approach: Threshold AHE

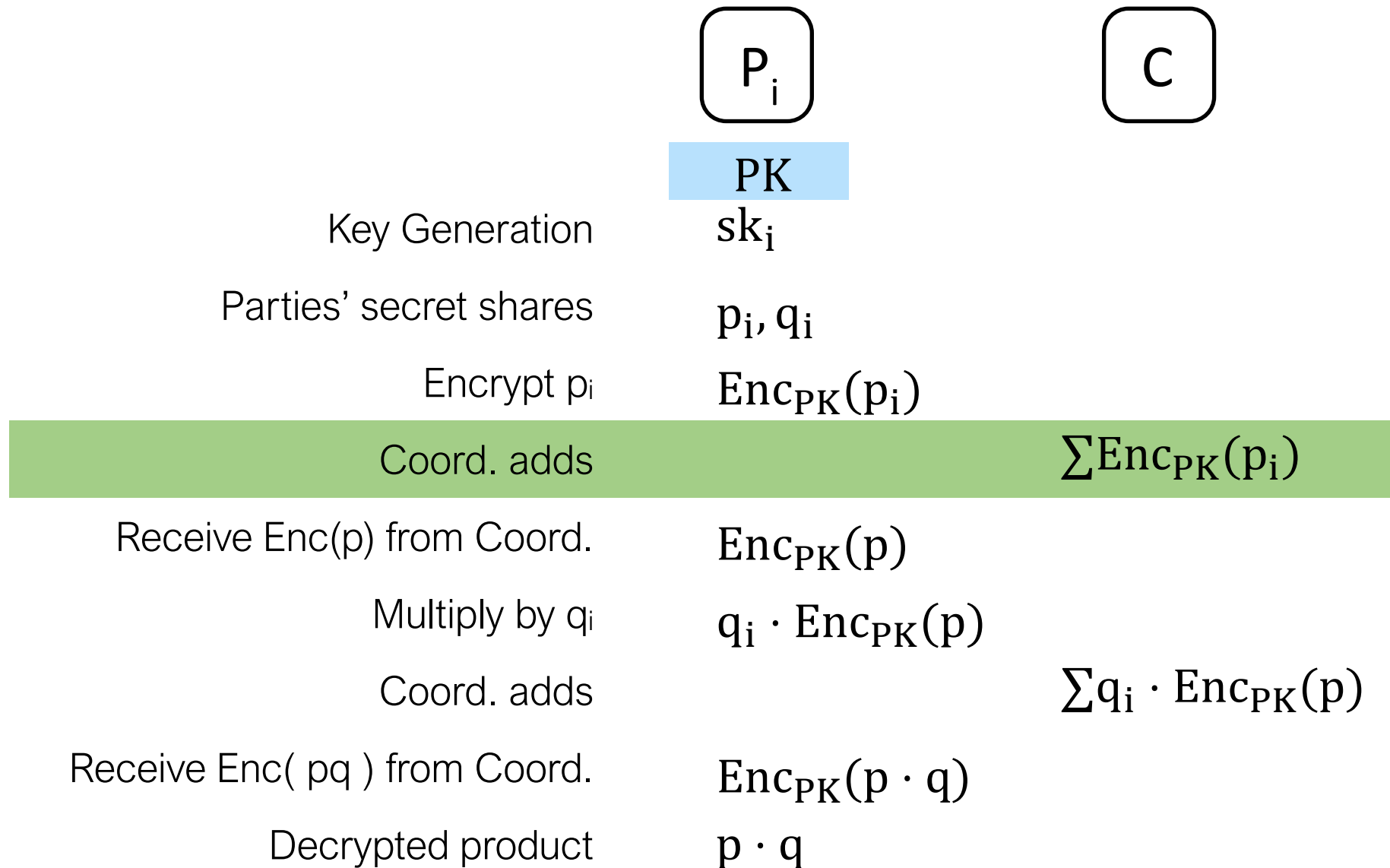


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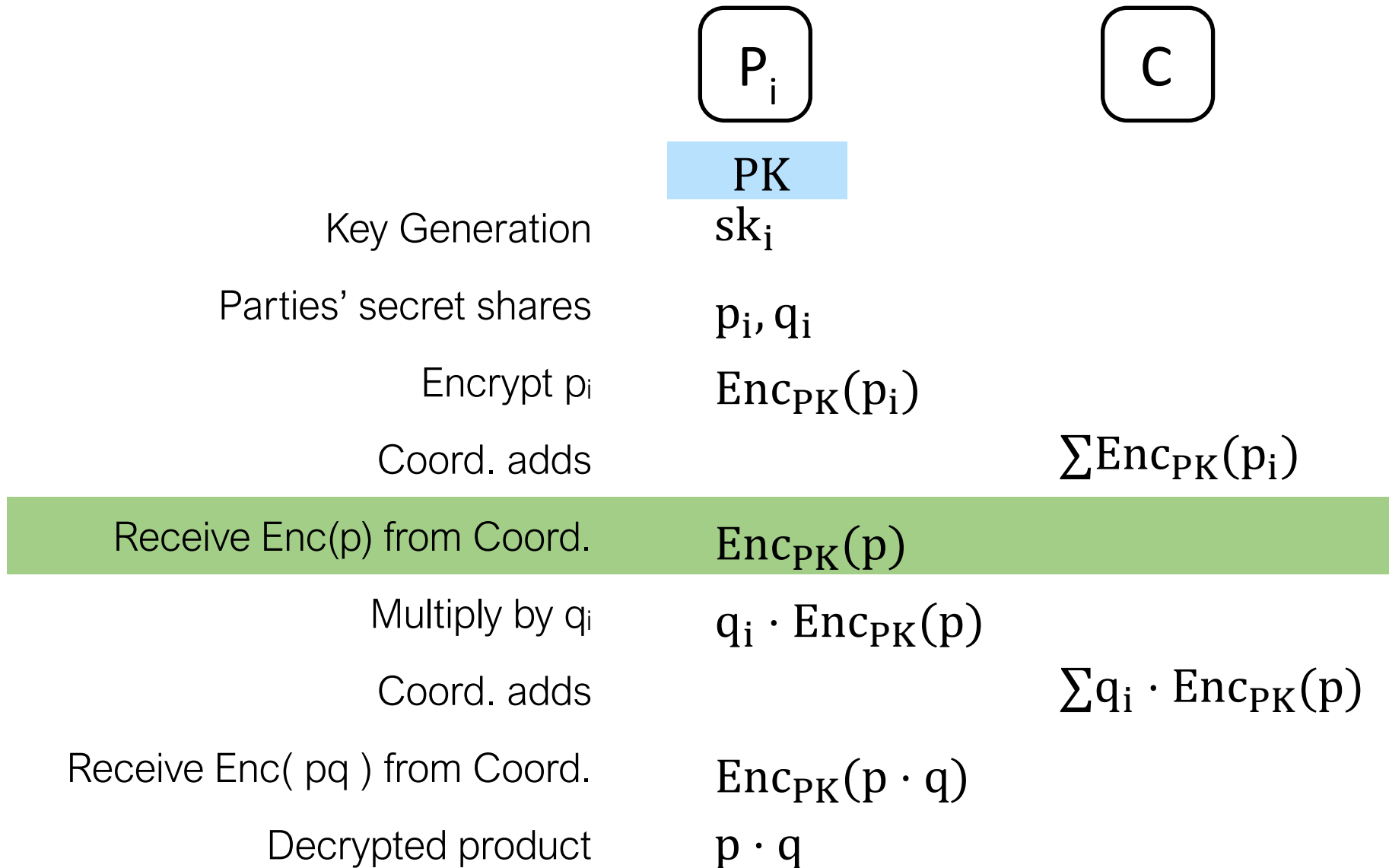




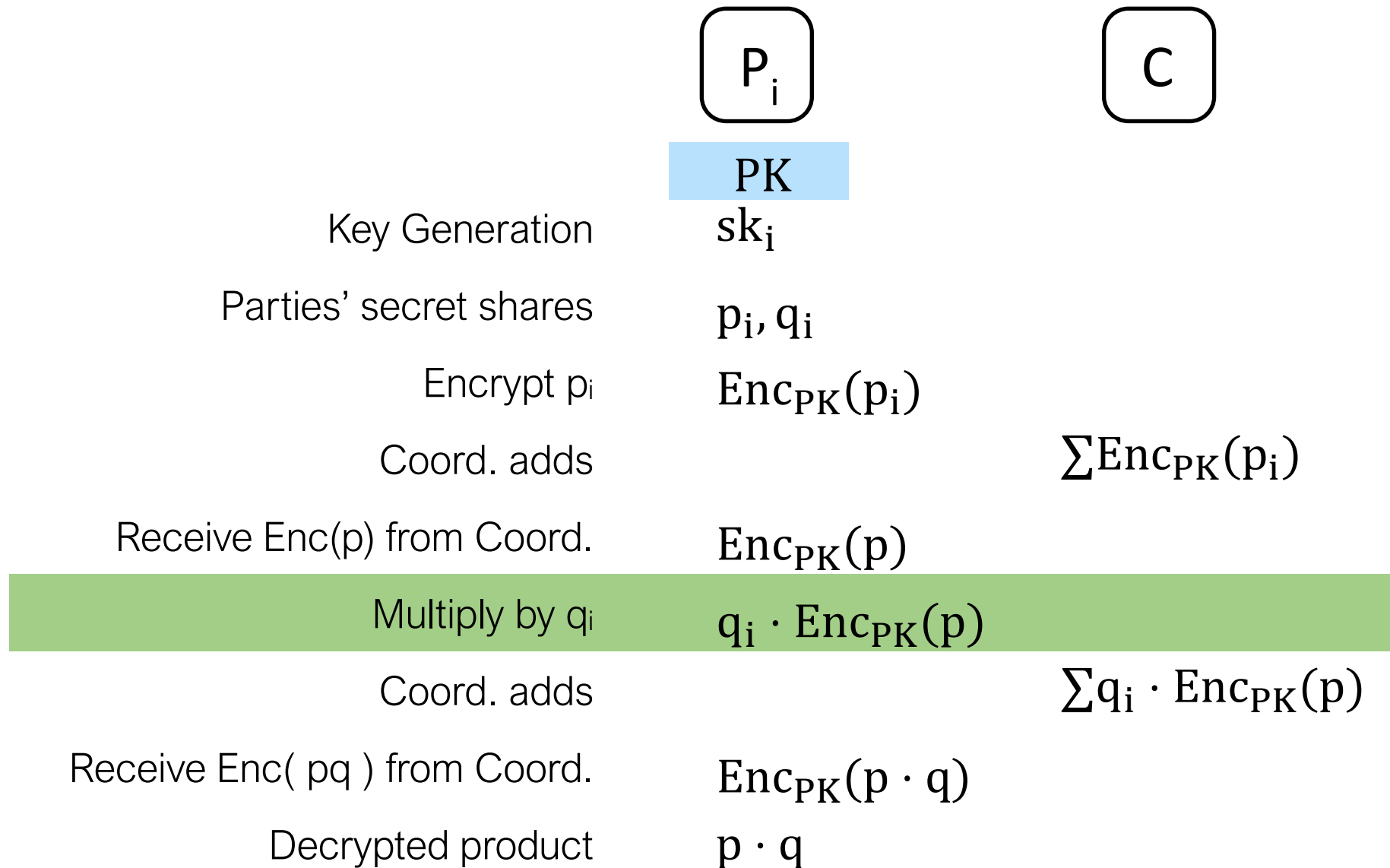
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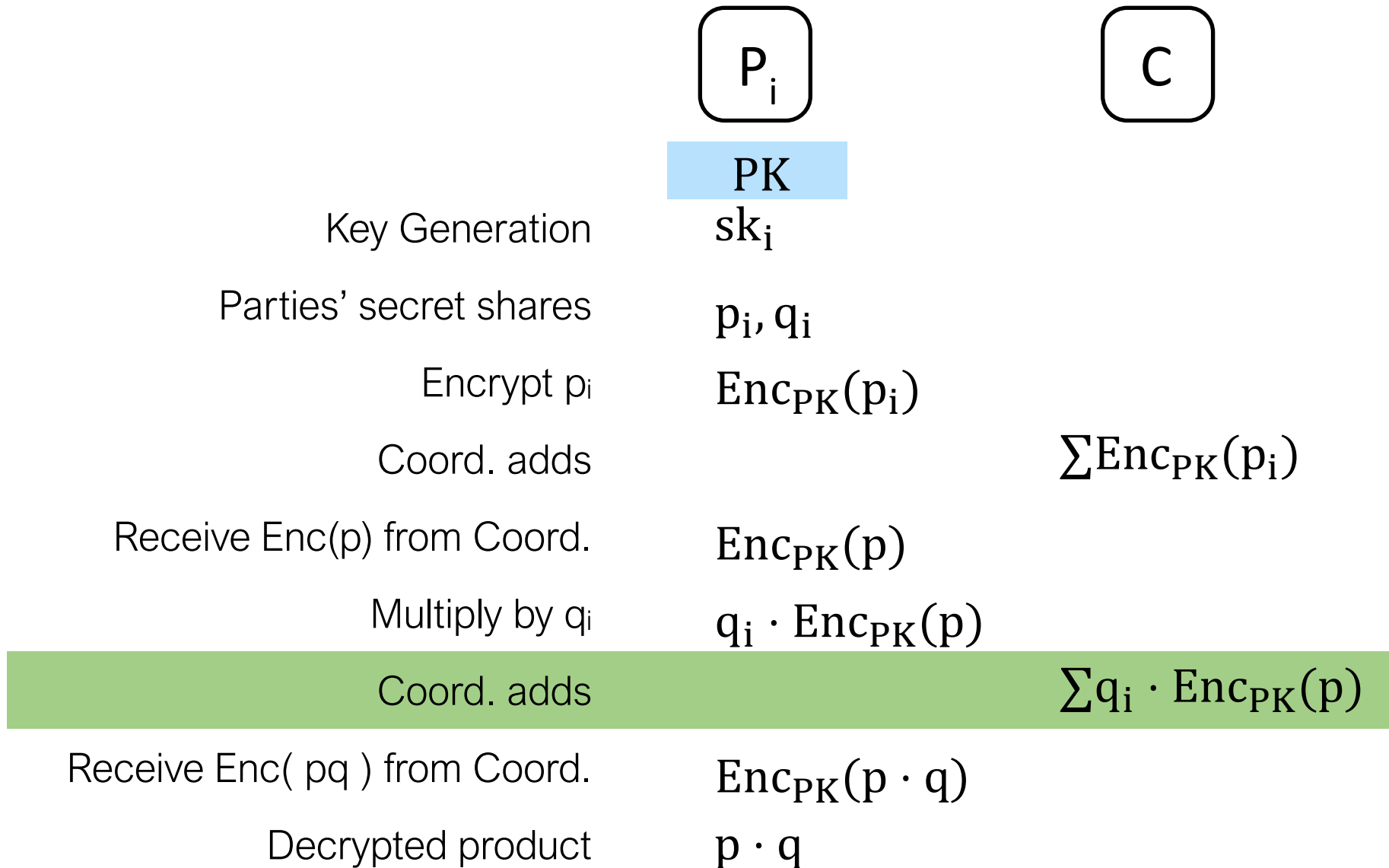
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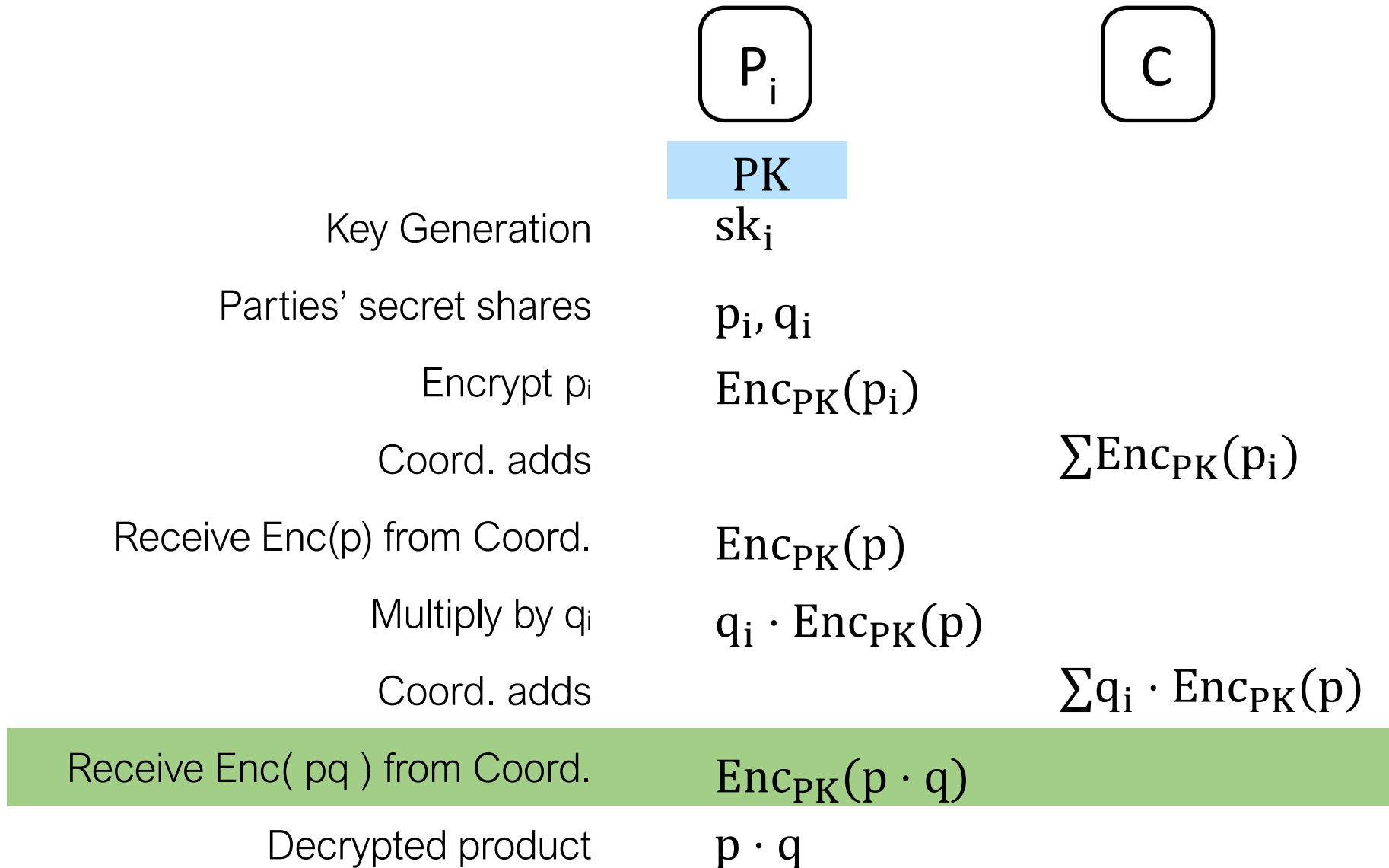
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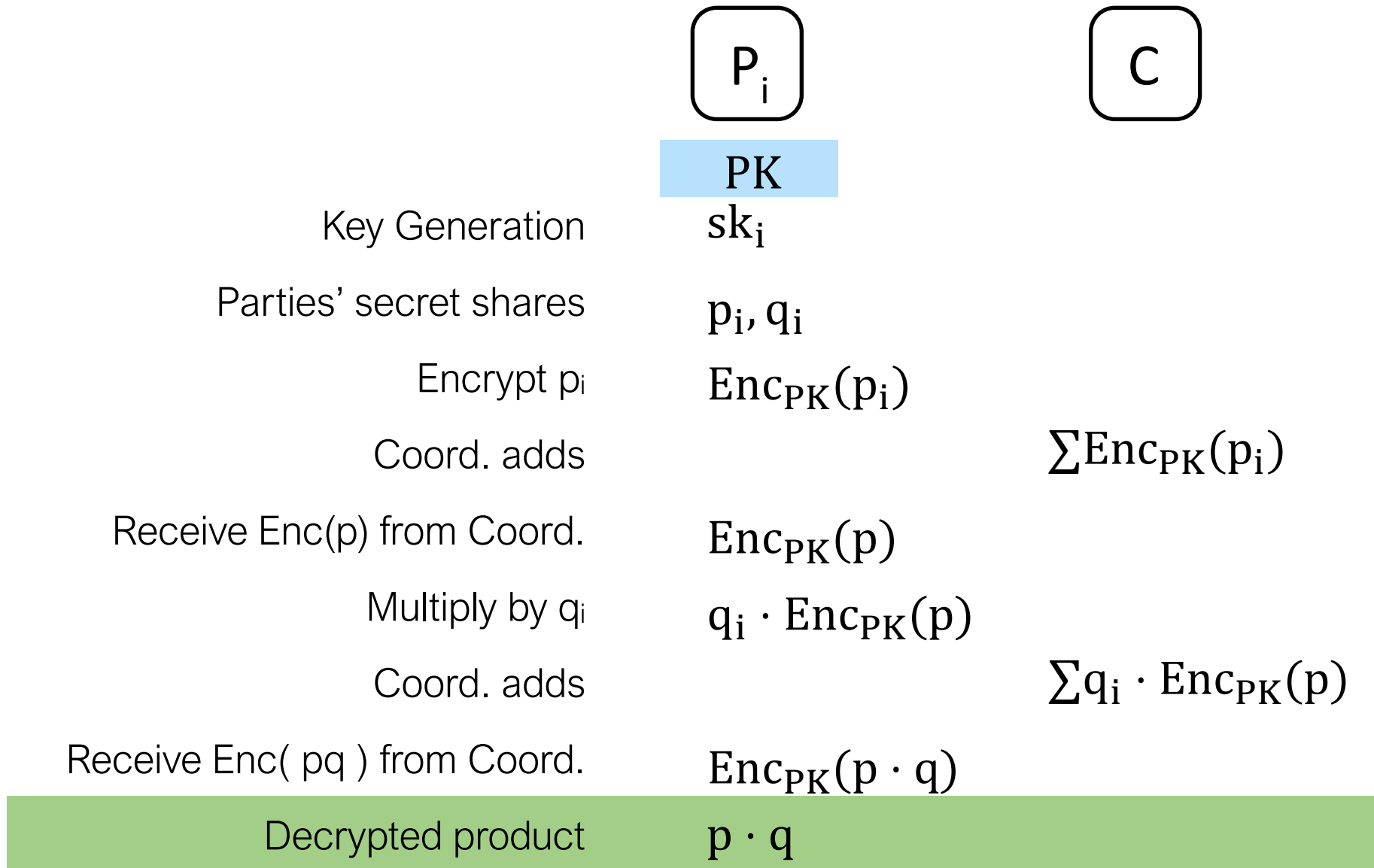
# Our Approach: Threshold AHE



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# State-of-the-Art TAHE

Paillier?

- Circular choice

El Gamal?

- Inefficient decryption (discrete log)

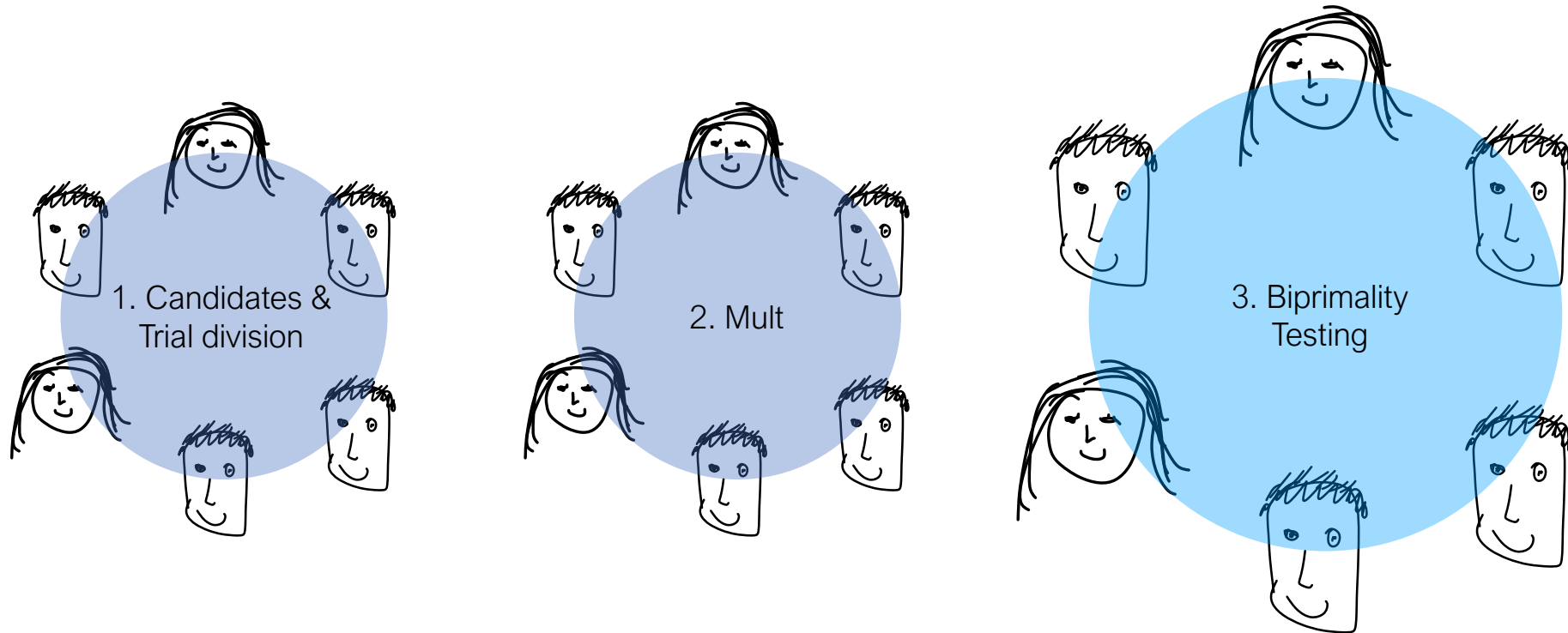
LWE?

- Does not support all AHE operations

Ring-LWE  more efficient, flexible

- Supports AHE, better parameters, packing

# [BF97]'s Distributed Biprimality Test



- Test whether  $N$  is the product of two primes
- Don't leak  $p$  or  $q$
- Extension of Miller-Rabin primality test [Rabin80]
- Probabilistic - need to repeat  $s$  times



# Step 2: Security against Active Adversaries

# GMW Paradigm

aka Zero-Knowledge Proofs

aka "I will prove I did everything honestly!"

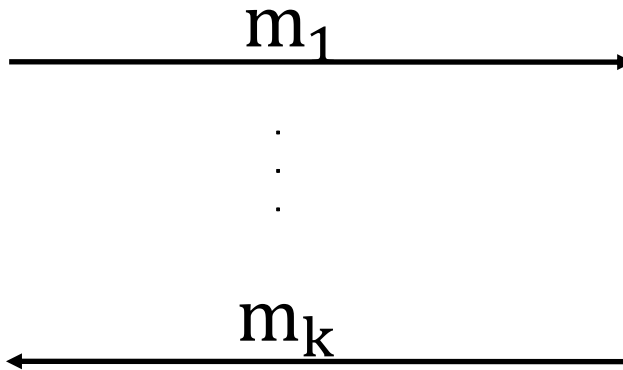
# GMW Paradigm: Passive Protocol

$P_1$

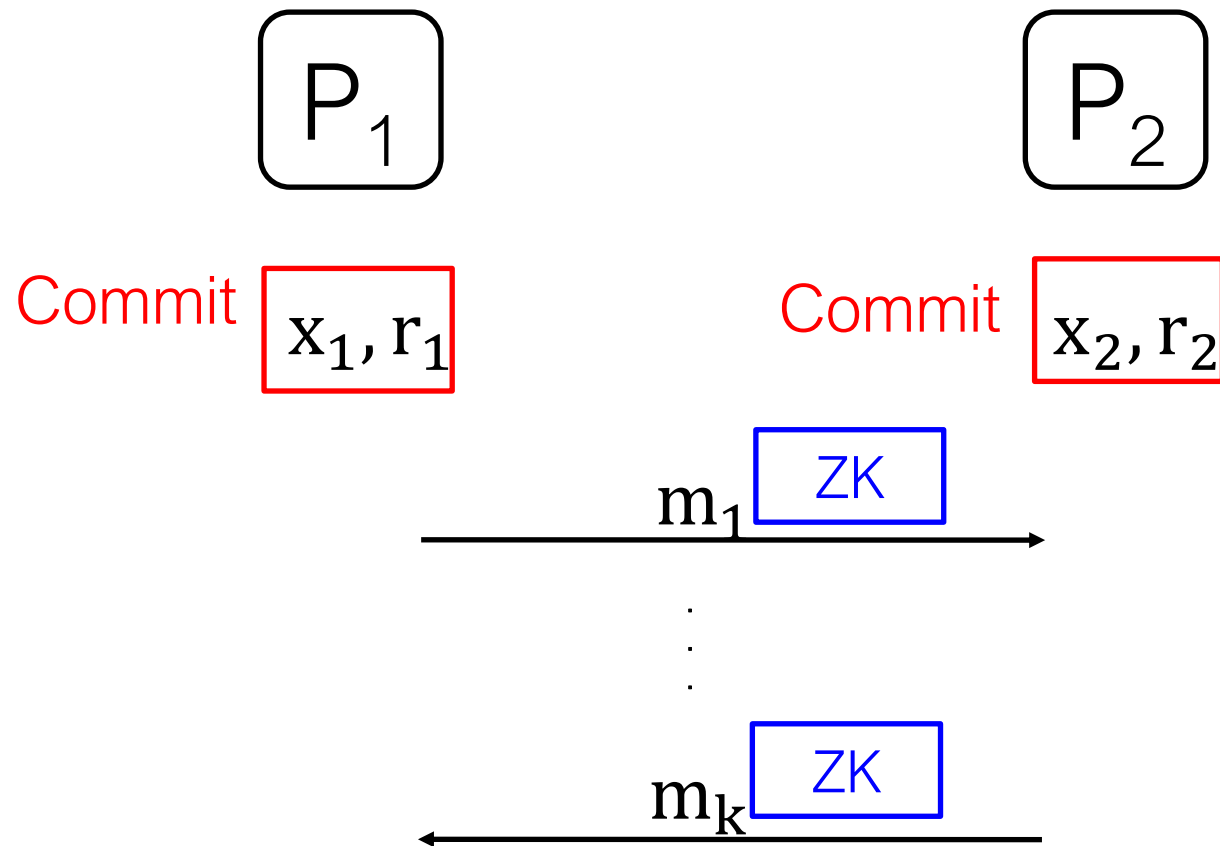
$x_1, r_1$

$P_2$

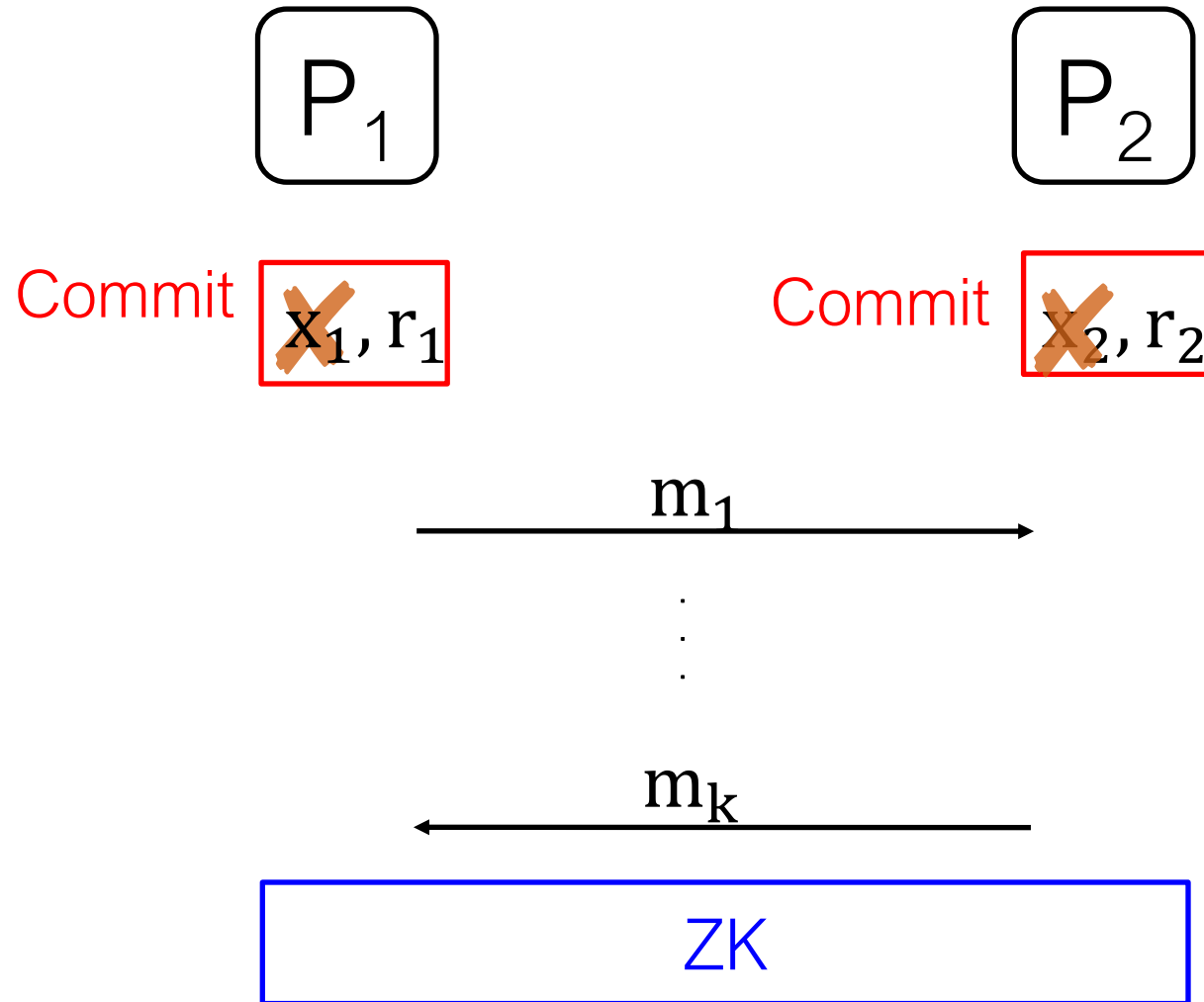
$x_2, r_2$



# GMW Paradigm: Active Protocol



# GMW Paradigm: Our Compiler



# What ZK Protocol to Use?

Need:

- Fast prover
- Prover runs on a 1 CPU 2 GB RAM machine
- Prove operations over

Lattice Operations over Ring  $Z_Q[x]/x^n+1$  where

$$Z_Q = Z_{p1} \times \dots \times Z_{p21}$$

Modulus generation - operations in

$$F_2, F_3, F_5, \dots, F_{823}$$

Jacobi test – Exponentiation operations over

$$Z_N^* \text{ (2048-bit number)}$$

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Tailor-made  
Sigma Protocol

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LIGERO

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Tailor-made  
Sigma Protocol



# Summary: Our Protocol

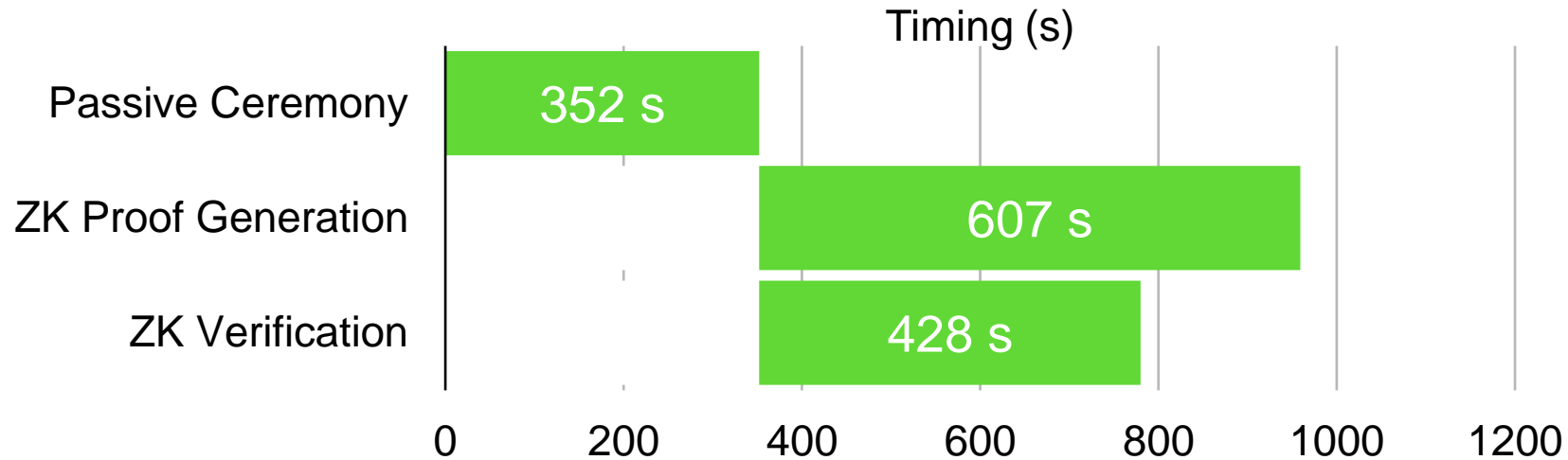
Key Setup	Generate threshold keys
Generate Candidates	Sample pre-approved primes
Compute Products	Use TAHE to compute candidates
Biprimality test	BF biprimality test
Certification	Ligero ZK + Sigma

# Performance Metrics: 10,000 Parties (Passive)

Parties	Coordinator	Total time (s)
64	m5.metal	61.8
128	”	74.3
256	”	104.8
512	”	137.6
1024	”	205.8
1500	r5.24xlarge	266.8
2000	”	416.5
4500	”	1282.6
10000	”	2111.8

# Performance Metrics: 1024 Parties (Active)

Stage	Timing Per Step	Cumulative Time
Passive Protocol	5m 52s	
ZK Proof Generation	11m 07s	
ZK Verification	7m 08s	17m 06s



# Conclusion

	[FLOP18]	Our Goal	
Modulus size	2048 bits	2048 bits	
Implementation	Passive	Active	✓
Num Parties	2	1024	✓
Party Spec	8 GB RAM 8 cores CPU	2 GB RAM single-core CPU	✓
Network speed	40 Gbps	1 Mbps 100 ms latency	✓
Comm. (Per-Party)	>1.9 GB	< <del>100 MB</del> 200 MB	✗
Time	35 sec (8 thread)	< 20 mins	✓

[https://github.com/li  
geroinc/LigeroRSA](https://github.com/ligeroinc/LigeroRSA)

Thank You