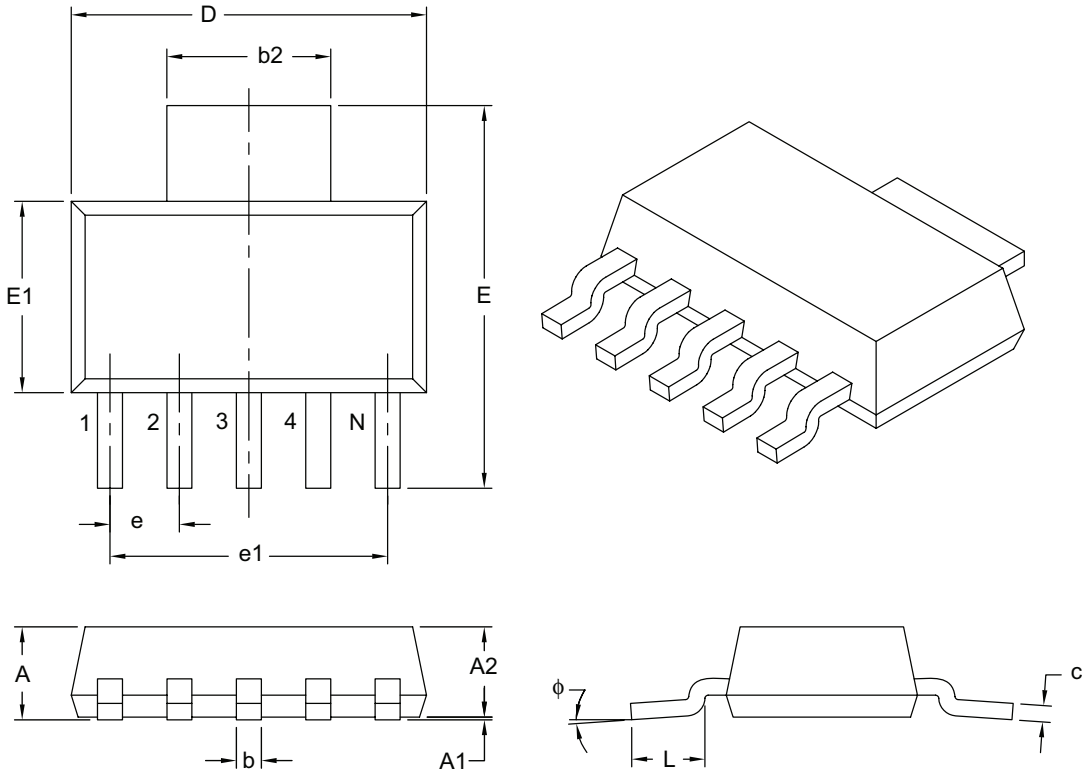
## 5-Lead Plastic Small Outline Transistor (DC) [SOT-223]

**Note:** For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging




| Units | | MILLIMETERS | | |
|---|---|---|---|---|
| Dimension Limits | | MIN | NOM | MAX |
| Number of Leads | N | | 5 | |
| Lead Pitch | e | | 1.27 BSC | |
| Outside Lead Pitch | e1 | | 5.08 BSC | |
| Overall Height | A | – | – | 1.80 |
| Standoff | A1 | 0.02 | 0.06 | 0.10 |
| Molded Package Height | A2 | 1.55 | 1.60 | 1.65 |
| Overall Width | E | 6.86 | 7.00 | 7.26 |
| Molded Package Width | E1 | 3.45 | 3.50 | 3.55 |
| Overall Length | D | 6.45 | 6.50 | 6.55 |
| Lead Thickness | c | 0.24 | 0.28 | 0.32 |
| Lead Width | b | 0.41 | 0.457 | 0.51 |
| Tab Lead Width | b2 | 2.95 | 3.00 | 3.05 |
| Foot Length | L | 0.91 | – | 1.14 |
| Lead Angle | ϕ | 0° | 4° | 8° |

**Notes:**

1. Dimensions D and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.127 mm per side.
2. Dimensioning and tolerancing per ASME Y14.5M.

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing C04-137B

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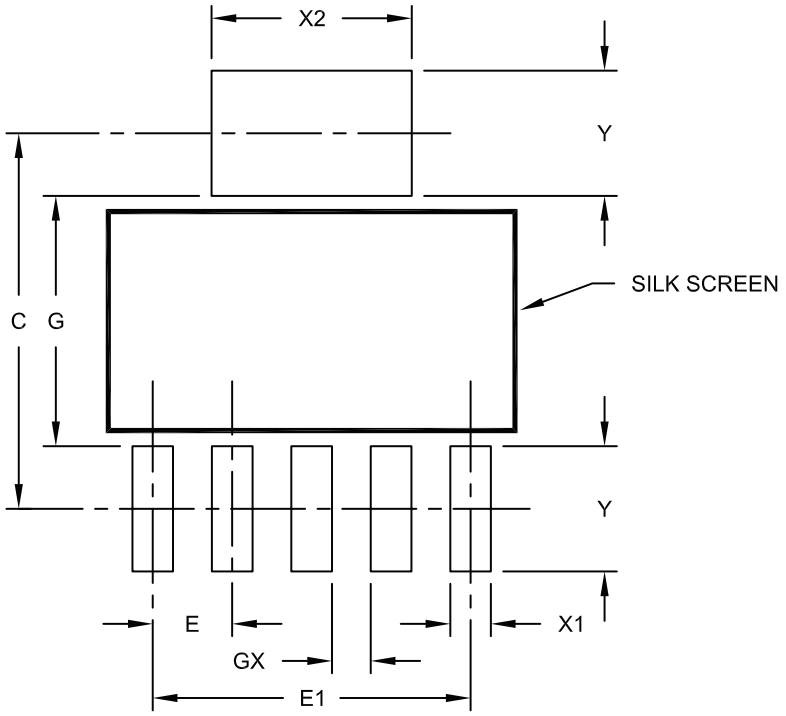


RECOMMENDED LAND PATTERN

| Units | | MILLIMETERS | | |
|---|---|---|---|---|
| Dimension Limits | | MIN | NOM | MAX |
| Pad Pitch | E | | 1.27 BSC | |
| Overall Pad Pitch | E1 | | 5.08 BSC | |
| Pad Spacing | C | | 6.00 | |
| Pad Width | X1 | | | 0.65 |
| Pad Width | X2 | | | 3.20 |
| Pad Length | Y | | | 2.00 |
| Distance Between Pads | G | 4.00 | | |
| Distance Between Pads | GX | 0.62 | | |

Notes:

1. Dimensioning and tolerancing per ASME Y14.5M

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing No. C04-2137A