

# SUKANTA DEY

## *Present Address:*

*Brahmaputra Hostel, Room number G154  
IIT Guwahati, Guwahati - 781039  
India*

## *Contact Details:*

*Ph: +91-9706260071  
email: sukantadey12@gmail.com  
email: sukanta.dey@iitg.ernet.in*

## CURRENT POSITION

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Ph.D. Research Scholar in the department of Computer Science and Engineering at Indian Institute of Technology Guwahati. Currently pursuing research on VLSI CAD. Have 3 years experience in Research & Development of software tools for VLSI Power Integrity, Reliability issues in Power Grid Network, IR/EM Analysis. Looking for an internship position in your esteemed firm so that I can use my technical knowledge for development some projects.

## ACADEMIC QUALIFICATION

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**Indian Institute of Technology Guwahati, Guwahati, India**

*Ph.D in Computer Science & Engineering* *December 2015 - July 2019 (Expected)*

PhD Thesis Title : Analysis, Optimization and Verification of VLSI Power Distribution Network

PhD Advisors : Prof. Sukumar Nandi, Dept. of CSE, IIT Guwahati &  
Dr. Gaurav Trivedi, Dept. of EEE, IIT Guwahati

*M.Tech. in Computer Science & Engineering* *July 2014 - December 2015*

CPI: 7.08/10

Project: Analog Layout Design Automation

In the project, difficulties in analog circuit layout design have been identified and an algorithm has been devised to identify the symmetrical current mirrors of any analog amplifier IC.

**Assam Engineering College, Guwahati, India**

*August 2010 - July 2014*

*Bachelor of Engineering in Electronics & Telecommunication*

Overall Grade: 73.82 %

Project: Spread Spectrum based secure communication system

In the project, a prototype of Direct Sequence Spread Spectrum Technique has been developed, which is widely used in Code Division Multiple Access (CDMA) communication and further transmitter and receiver for this communication technique was developed. Later a demonstration was shown on how this technology is used for secure communication by comparing the message in the receiver and in a third party interceptor.

**Maharishi Vidya Mandir Sr. Secondary School, Guwahati, India**

*May 2010*

*Class 12<sup>th</sup>*

Central Board of Secondary Education (CBSE), New Delhi

Overall Grade: 78.50 %

**Kamakhya Vidyalaya High School**, Guwahati, India  
*Class 10<sup>th</sup>*  
Board of Secondary Education Assam  
Overall Grade: 78.66 %

*May 2008*

## EXPERIENCE

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### **Indian Institute of Technology Guwahati**

*July 2014 - Present*

*Teaching Assistant*

Working as a Teaching Assistant for the various Under Graduate and Post Graduate courses in the Dept. of Computer Science and Engineering at IIT Guwahati.

### **Texas Instruments India, Bengaluru**

*December 2015*

*Project Trainee*

Undergone training for a project “Analog Layout Design Automation”. Interacted with the analog circuit design engineers and layout design engineers to find out the difficulties in making analog layout so as to make an automated tool.

### **Brigosh Technologies Pvt. Ltd., Guwahati**

*July 2013*

*Summer Intern*

Undergone training on embedded system and worked on projects with PIC18 micro-controller and its application.

## PUBLICATIONS

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**Sukanta Dey, Satyabrata Dash, Sukumar Nandi, Gaurav Trivedi, “Markov Chain Model using Lévy Flight for VLSI Power Grid Analysis ”** in *30th IEEE/ACM International Conference on VLSI Design 2017, Hyderabad, India, 7-11 January 2017. (accepted)*

## TECHNICAL STRENGTHS

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<b>Operating Systems</b>	Ubuntu 14.04 LTS (Linux Debian Distributions).
<b>Computer Languages</b>	C, C++, Python, Verilog, VHDL, ngSpice, Matlab.
<b>Tools</b>	Vim, gedit, Xilinx ISE Design Suite, Cadence Virtuoso Design Environment.

## PERSONAL DETAILS

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<b>Date of Birth</b>	12-November-1991
<b>Mother's Name</b>	Mrs. Anita Dey.
<b>Father's Name</b>	Mr. Subhas Ch. Dey.
<b>Hobbies</b>	Coding, Writing, Internet surfing, Swimming, Bicycling .
<b>Permanent Address</b>	Sagar Housing Complex, 102/A, Guwahati - 781012, Assam, India.