Jacob Schatzle

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EDUCATION

University of South Carolina Honors College — Columbia, SC
Bachelor of Science in Engineering in Computer Engineering — *Aug. 2021 – May 2025*
Minor in Electrical Engineering — GPA: 3.649
Related Coursework: Operating Systems | Embedded Systems | Advanced Programming Techniques

EXPERIENCE

Undergraduate Research Assistant — Columbia, SC
University of South Carolina - Heterogeneous and Reconfigurable Computing Lab — *August 2023 – Present*
- Transliterate methods between MATLAB and Python to limit computational expenses for large data processing.
- Applied code to FPGA platforms, enhancing computational efficiency and maximizing parallelization capabilities.
- Participate in weekly meetings with professors and peers to foster team accountability and promote collaborative research efforts.

Lead Undergraduate Ambassador — Columbia, SC
University of South Carolina - College of Engineering and Computing — *August 2023 – Present*
- Guide tour groups of 50+ prospective parents and students through the plethora of labs, classrooms, and spaces within the College to give an in-depth explanation of what the college has to offer.
- Answer a variety of questions and provide relevant information about the many majors, minors, student organizations, and housing options offered to help with an understanding of being a CEC student.
- Assist staff and students as a leader and liaison by performing any clerical, organizational, and ancillary tasks vital to the inner workings and success of the College whilst maintaining a high academic standing.

Server/Line Cook — Beaufort, SC
The Carolina Tavern — *July 2021 – December 2023*
- Demonstrated exceptional ability to prioritize and multitask in a fast-paced environment, ensuring efficient operations with minimal supervision.
- Assisted management with conflict resolution, involving both employees and customers, contributing to a more positive environment for staff and patrons.
- Provided valuable mentorship and support to new team members, leveraging extensive experience to ensure smooth on-boarding and maintain high standards of service excellence.

PROJECTS

CPU Design | *System Verilog, RISC-V* — August 2023 – December 2023
- Developed RISC-V assembly code leveraging all five instruction types to optimize program execution efficiency.
- Designed a CPU architecture using System Verilog, incorporating pipelining and memory hierarchy for enhanced performance and scalability.
- Implemented the CPU design on FPGA hardware, ensuring seamless integration and real-world applicability of the developed system.

ACTIVITIES AND INVOLVEMENT

Theta Tau - Professional Co-ed Engineering Fraternity — Columbia, SC
Recruitment Chair — *August 2023 – December 2023*
- Organized 7 events for 60+ brothers and 140+ rushees over the course of 3 weeks.
- Led the Fall 2023 recruitment effort, setting the Chapter's record with a potential new member class of 27.

Theta Tau - Professional Co-ed Engineering Fraternity — Columbia, SC
Service Chair — *January 2023 – May 2023*
- Worked with local businesses to coordinate community enrichment opportunities for all fraternity brothers.
- Track total hours and events attended for 60+ brothers and report non-compliant members to the executive board.

SKILLS

Technical: Java | Python | C/C++ | Linux/UNIX | Assembly | System Verilog | LaTeX | GitHub