Division of Continuing Education

Module 1: Why Use Concurrency

Topic 2.2: Hiding Latency

Hiding Latency

- Concurrency can improve performance, even without parallelism
- Tasks must periodically wait for something
 - > i.e. wait for memory
 - \triangleright X = Y + Z read Y, Z from memory
 - ➤ May wait 100+ clock cycles
- Other concurrent tasks can operate while one task is waiting



Hardware Mapping

Task 1 Task 2 Parallel Execution Core 1 Core 2 **Concurrent Execution** Task 2 Task 1 Core 1



Hardware Mapping in Go

- Programmer does not determine the hardware mapping
- Programmer makes parallelism possible
- Hardware mapping depends on many factors
 - Where is the data?
 - What are the communication costs?



