

BASIC DATA SYSTEM

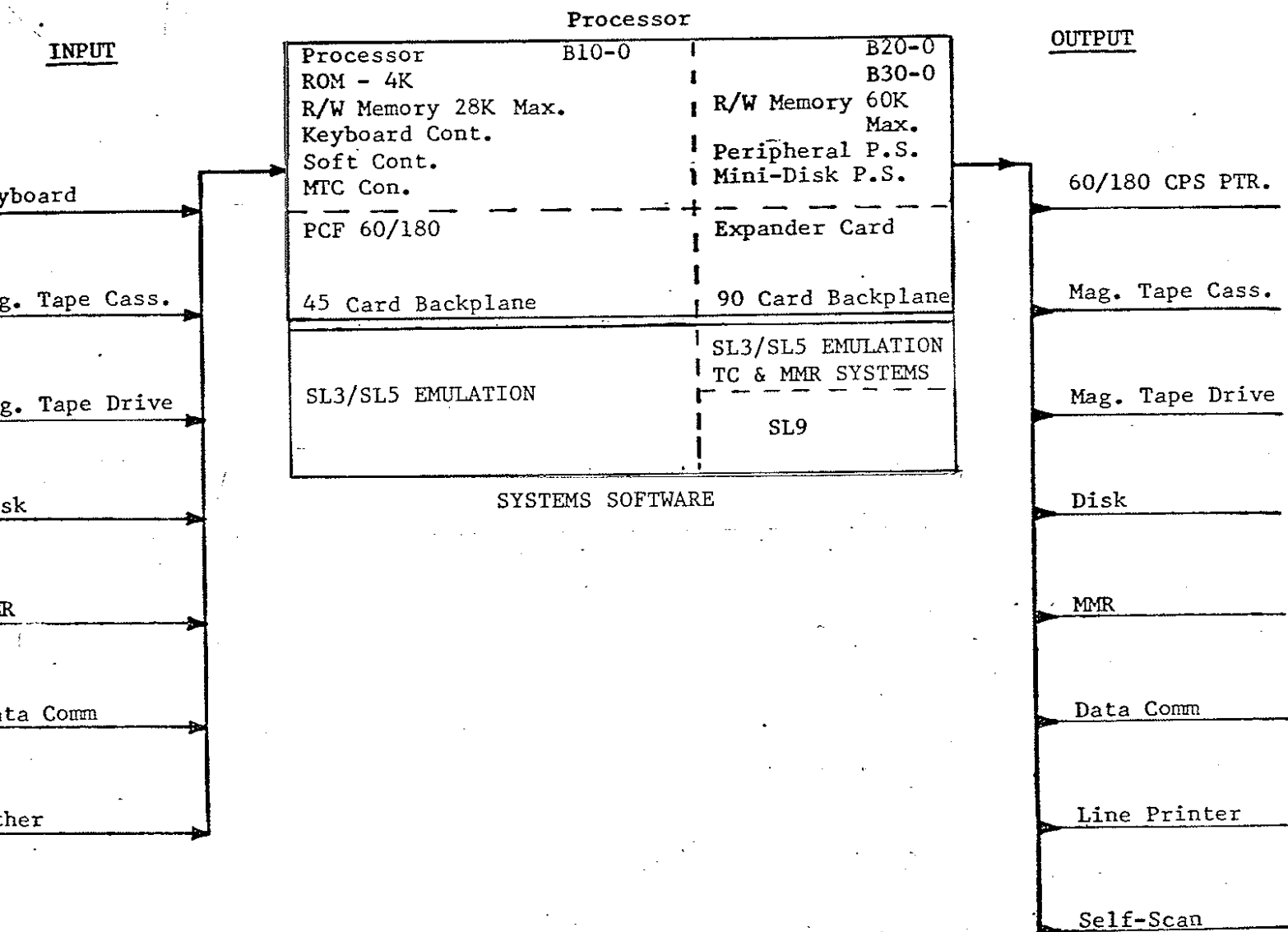
WHAT IS IT.

MAINTENANCE FEATURES.

CORPORATIONS PLANS

ITIO CERTIFICATION AND INITIAL TRAINING PLANS

# BDS SYSTEMS



BASIC DATA SYSTEMS

GROUP IV	S1000	— (PROCESSOR CARDS ONLY)
	AE500	
GROUP VI	TC5000	
	DC	
GROUP II	B80	

OTHER PRODUCTS ARE CONSIDERING BDS PROCESSOR (SP500)

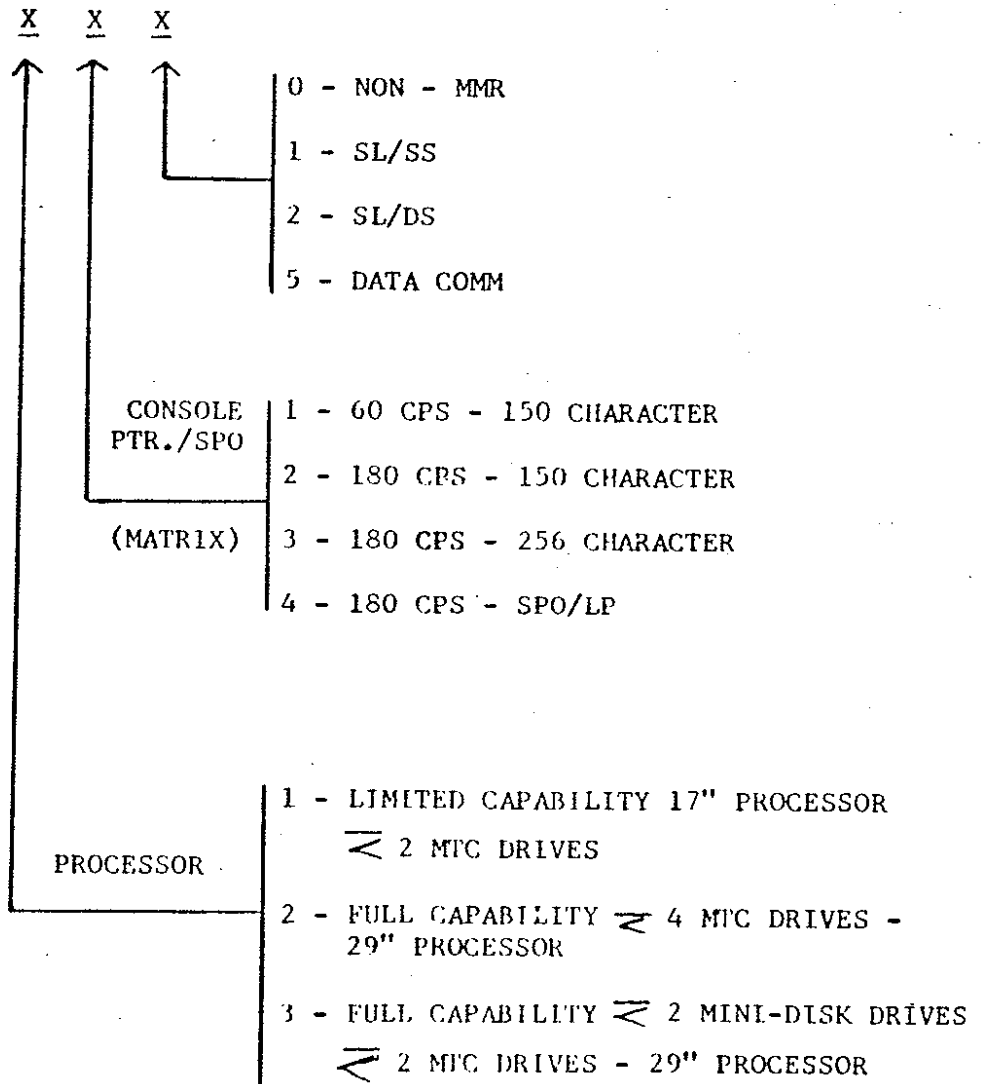
# BASIC DATA SYSTEM NUMBERING SCHEME

## SL9 SYSTEMS

B80

## SL5 SYSTEMS

L80



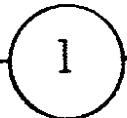
# BASIC DATA TERMINAL NUMBERING SCHEME

## CASSETTE

## PROCESSORS

MINI  
ONE

MAXI  
TWO



Limited Capability - SL3/5 Only  
4 I/O Only (KB-Console/Printer-MTC-  
One D/C Line)  
Up To 28KB R/W Memory

ONE

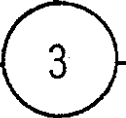
FOUR



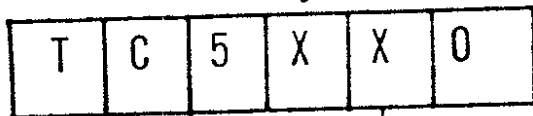
Full Capability - SL3/5 Or SL9  
(KB-Console/Printer-MTC+5 "Open")  
8 I/O - If SL3/5 = Up To Two D/C Lines  
No Disk  
If SL9 = Up To 4 D/C Lines  
AND Free-Standing Disk  
Up To 60 KB R/W Memory

ONE

TWO



Full Capability - SL9 Only  
8 I/O - (KB-Console/Printer-MTC-Mini-Di  
+4 "Open")  
One Mini-Disk Included In Console (Up  
To Two)  
Up To 60KB R/W Memory



NON MMR

B D T

## CONSOLE/MATRIX PRINTER

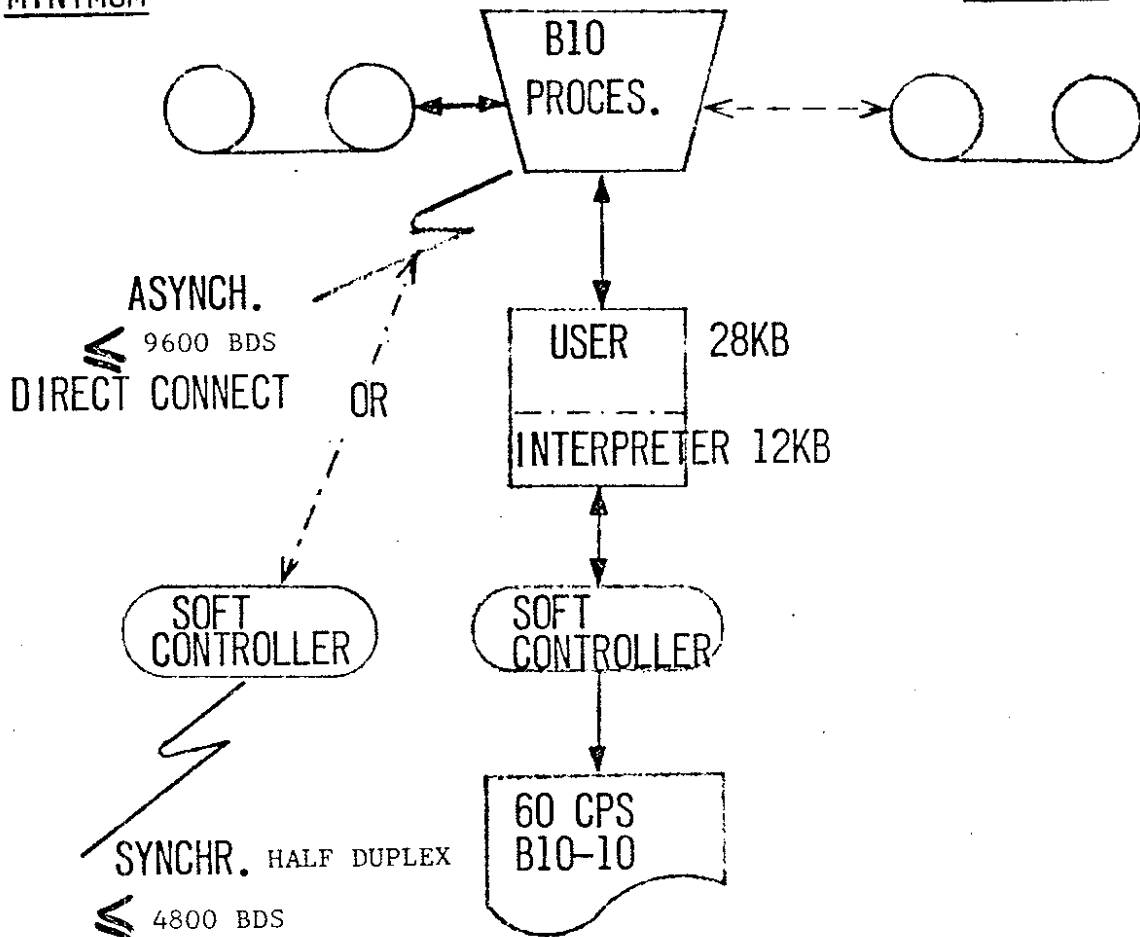
1	60CPS - 15" - One Pin-Feed
2	180CPS - 15" - Dual Pin-Feed/FF Capab.
3	180CPS - 25.6" - Dual Pin-Feed/FF Capab.
4	180CPS - 5" SPO - 17" Line Printer

# TC5110 (SL3/5)

## -LOW COST TC - REPLACEMENT TC500

### MINIMUM

### OPTIONS



### AVERAGE STANDARD COST (ESTIMATE)

#### ° ASYNCHRONOUS

CUMBERNAULD \$ 3145

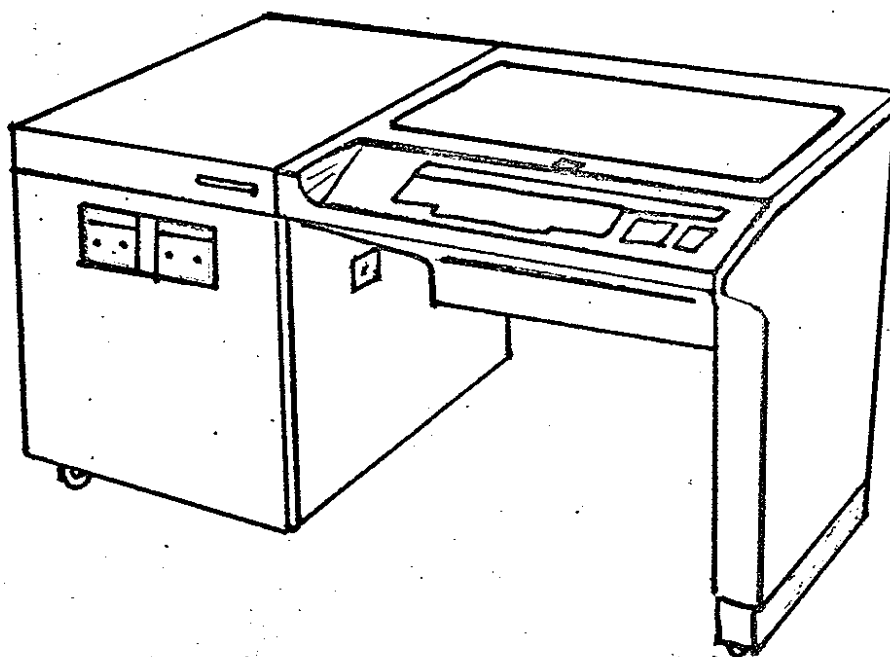
#### ° SYNCHRONOUS

CUMBERNAULD \$ 3275

74-4.	1975	1976	1977	1978	1979
FCAST	520	1050	1750	2200	2650
PRODUCTION	815	1252	1667	2189	2208

BASIC DATA SYSTEM

PHASE I



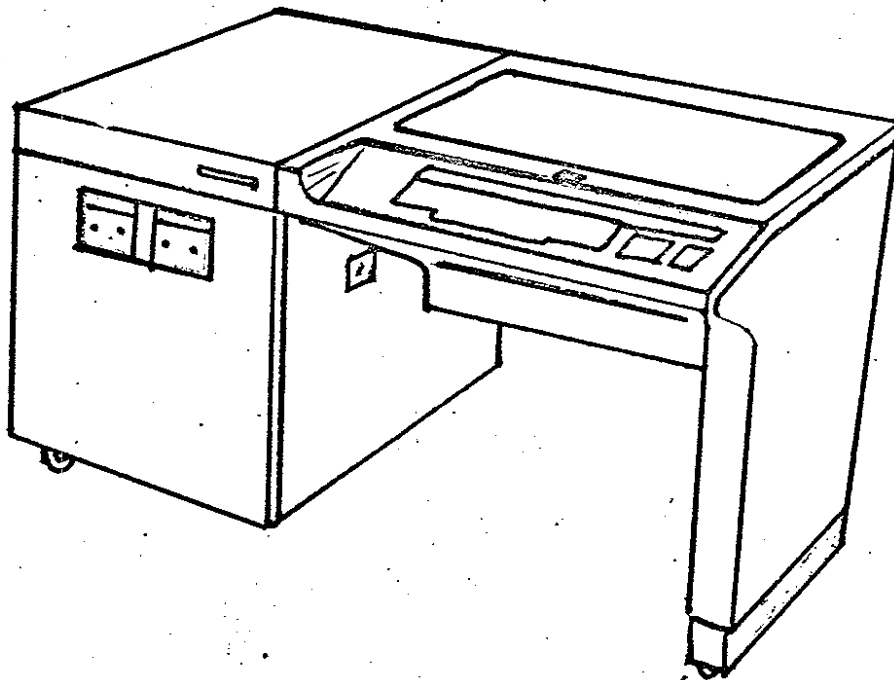
TC 5000

AE 500

SL3/SL5 EMULATION SYSTEMS

BASIC DATA SYSTEM

PHASE I



TC 5000

AE 500

SL3/SL5 EMULATION SYSTEMS



### BDS PERIPHERALS

B 9360

- SELF-SCAN DISPLAY (SCREEN AND CABLE)

A 9161-1

- MAGNETIC RECORD READER - 352 DIGITS

A 9162-1

- MAGNETIC RECORD READER - 704 DIGITS

### LINE PRINTERS

B 9249-1

- 85 LPM CHAIN PRINTER (132 PP) (ODEC)

B 9249-2

- 160 LPM CHAIN PRINTER (132 PP) (ODEC)

B 9249-3

- 250 LPM CHAIN PRINTER (132 PP) (ODEC)

B 9248-2

- 100 LPM MATRIX PRINTER (160 PP) (BDS)

B 9248-3

- 200 LPM MATRIX PRINTER (160 PP) (BDS)

### DISK FILES

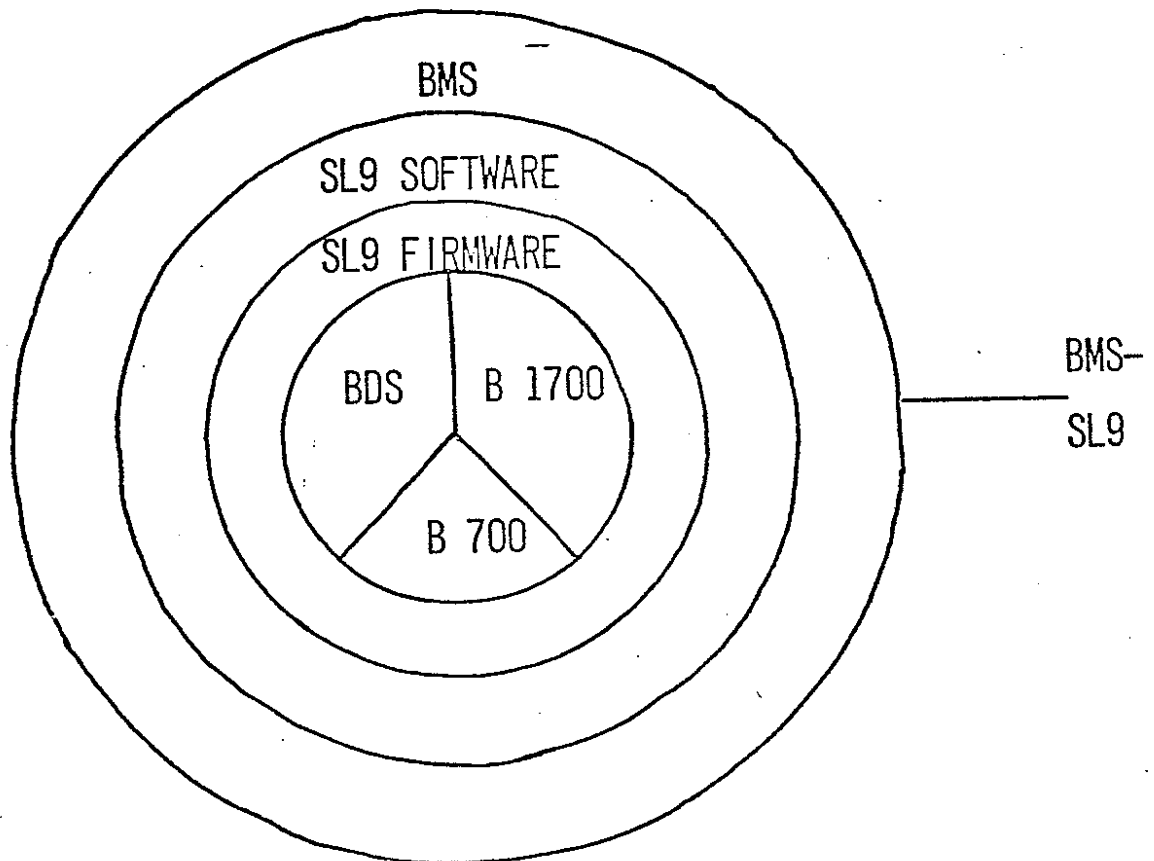
	<u>TRF. RATE</u>	<u>AVG. ACCESS TIME</u>
B 9480-12	194	- DUAL CARTRIDGE 4.6 MB, 100 TPI, 2200 BPI, 80 MS
B 9481-12	194	- DUAL CARTRIDGE 9.2 MB, 200 TPI, 2200 BPI, 80 MS
B 9482-35	390	- DUAL CARTRIDGE 18.4 MB, 200 TPI, 4400 BPI, 80 MS
B 9481-32	310	- DUAL CARTRIDGE 9.2 MB, 200 TPI, 2200 BPI, 42.5 MS
B 9489-1	45	- MASTER DRIVE 1.0 MB FLEXIBLE DISK, 64 TPI, 4774 BPI, 225 MS
B 9489-2	45	- SLAVE DRIVE 1.0 MB FLEXIBLE DISK, 64 TPI, 4774 BPI, 225 MS
B 9480-22	194	- DUAL LOW COST CARTRIDGE, 4.6 MS, 100 TPI, 2200 BPI, 145 MS
B9489-11	45	- MASTER STAND-ALONE 1.0 MB FLEX. DISK, 64 TPI, 4774 BPI, 225 MS
B9489-15		- FREE-STANDING DRIVE 243 KB FLEX. DISK, 48 TPI, 3200 BPI, 343 MS

MAGNETIC TAPE DEVICES

- B 9490-21 - MAGNETIC TAPE CASSETTE DRIVE  
10 ips - NRZ - DUAL GAP - INTERNAL MOUNTING - WESTLAKE
- B 9490-40 - AUTOMATIC M. T. CASSETTE LOADER
- B 9490-51 - MAGNETIC TAPE CASSETTE DRIVE - ECMA COMPATIBLE -  
PE/NRZ - 10 ips - DUAL GAP - INTERNAL MOUNTING  
(4th QTR. 1974) - GLENROTHES
- B 9491-2 - OEM MAGNETIC TAPE DRIVE 12.5 ips (PEC) 10 KB, NRZ,  
DUAL GAP

## A SMALL, PORTABLE BUSINESS SYSTEM

### USER ENVIRONMENT



SL9 FIRMWARE = OPERATING SYSTEM + INTERPRETERS

SL9 SOFTWARE = COMPILERS + UTILITIES

DESIGNED FOR COBOL/RPG BUSINESS DATA PROCESSING

LIMITED DATA COMM CAPABILITY

OPERATOR ATTENDED, CONSOLE ORIENTED

CUMBERNAULD - SL9 SOFTWARE RELEASE TO I.T.I.O. - BDS

	<u>1975</u>	<u>1976</u>
COBOL COMPILER - B1700	APRIL	
- BDS	JUNE	
RPG COMPILER - B1700	APRIL	
- BDS	JUNE	
COBOL S INTERPRETER	JUNE	
BIL S INTERPRETER	JUNE	
OPERATING SYSTEM	JUNE	
UTILITIES	JUNE	
SORT/MERGE	JUNE	
MCS COMPILER	DEC.*	
MCS S INTERPRETER	DEC.	
NDL S INTERPRETER		JANUARY
DATA COMM ADDITIONS TO COBOL	DEC.	
OS INTERFACE & MESSAGE SYSTEM	DEC.	
NDL COMPILER	DEC.*	

\*FROM DOWNTOWN - RELEASE TO TIO-EAST

## SL9 SUPPORT

### CURRENT PLANT RESPONSIBILITIES

- CUMBERNAULD - FIRMWARE SET (OPERATING SYSTEM & INTERPRETERS)
- COBOL COMPILER
- RPG COMPILER
- DOWNINGTOWN - FIRMWARE SET
- BIL COMPILER
- NDL COMPILER
- LIEGE - FIRMWARE SET

### ITIO CUMBERNAULD

- ACCEPTS FIRMWARE & COBOL & RPG COMPILERS
- VERIFIES DESIGN LEVEL OF BIL & NDL COMPILERS BY COMPARING OBJECT FILES FROM CUMBERNAULD TO OBJECT FILES FROM DOWNINGTOWN
- RELEASES FIRMWARE & SOFTWARE TO MARKETING

### SUPPORT PLAN

- PROBLEM ENCOUNTERED ON BDS IS REPORTED TO ITIO CUMBERNAULD
- ITIO ISOLATES PROBLEM TO SPECIFIC HARDWARE-FIRMWARE-SOFTWARE MODULE (WITH PLANT HELP IF NECESSARY)
- IF PROBLEM IS IN BIL OR NDL COMPILER, IT IS REPORTED TO TIO DOWNINGTOWN. OTHERWISE, IT IS REPORTED TO CUMBERNAULD ENGINEERING
- FIX FOR THE PROBLEM WILL APPEAR IN SUBSEQUENT SOFTWARE RELEASE FROM DOWNINGTOWN OR CUMBERNAULD

## SUMMARY OF MAINTENANCE FEATURES

BASIC MTR CONFIDENCE/DIAGNOSTIC ROUTINE RESIDENT IN ROM FIRMWARE.

ALL REMAINING ROUTINES LOADED VIA CASSETTE LOADER.

ROUTINES MAY BE RUN AUTOMATIC AS A FULL SYSTEM HARDWARE CONFIDENCE TEST  
OR MANUALLY "AS SELECTED".

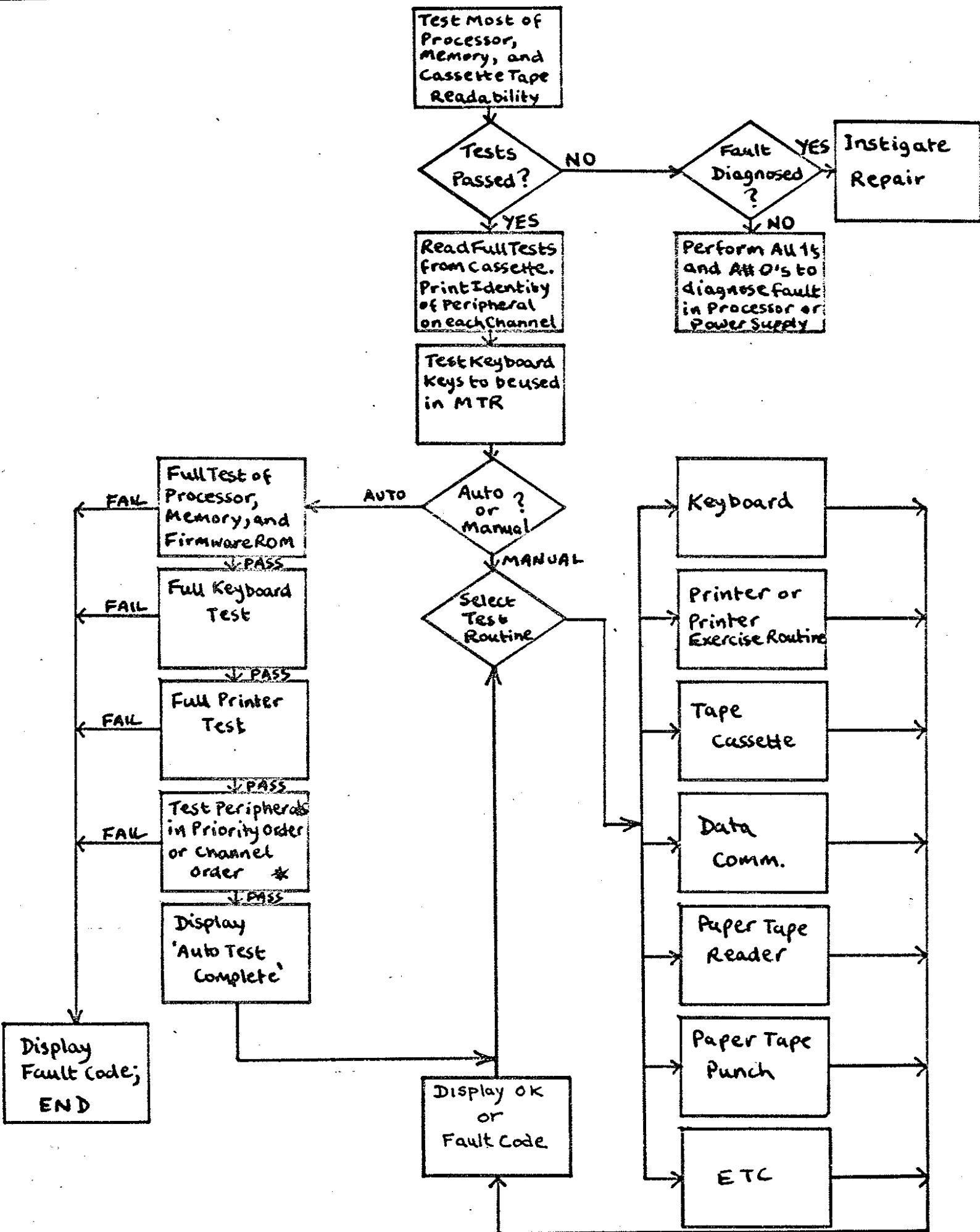
SPECIFIC ROUTINES MAY BE RUN IN A CONTINUOUS LOOP TO DETECT INTERMITTENTS.

CONFIDENCE TESTS MAY BE RUN BY OPERATOR OR SALES AND SYSTEMS SUPPORT REPS.

MEANTIME TO DIAGNOSE HARD FAILURES - 10 MINS APPROX.

MEANTIME TO REPAIR TO COMPONENT - LESS THAN ONE HOUR.

MANUAL PROCEDURES REQUIRE "WIER" DTM1000 (P/N ) AVAILABLE FROM IGDC  
CUMBERNAULD OR EQUIVALENT.



BDS RELEASE COMMITTEE

FEDERAL AND SPECIAL SYSTEMS GROUP:

S. FOURCADE - MARKETING

SMALL SYSTEMS GROUP:

- HARDWARE  
- SOFTWARE

BUSINESS MACHINES GROUP:

J. G. OWENSBY - FIELD ENGINEERING  
R. J. MANZI - GROUP VI MARKETING  
J. L. PETERSEN - GROUP II MARKETING  
R. HESS - SYSTEMS SUPPORT  
C. R. MONTGOMERY - SYSTEMS STANDARDS AND CONVERSIONS  
- SALES TRAINING

INTERNATIONAL GROUP:

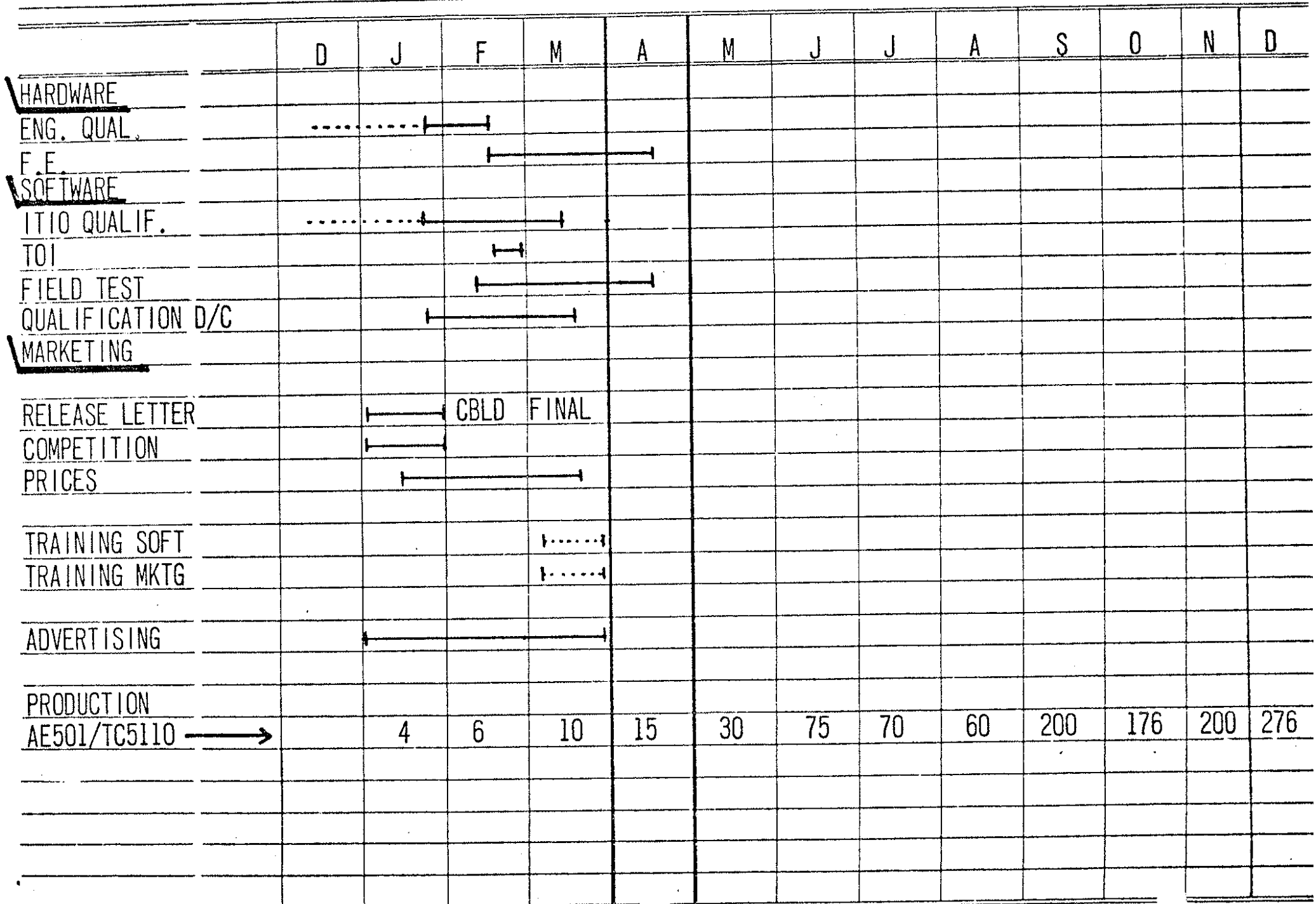
Y. LE BOURLOT - SYSTEMS SUPPORT  
J. J. HAWKINS - GROUP II MARKETING  
C. VAN DOOLJEWERT - GROUP VI MARKETING  
P. GLASGOW - SALES TRAINING  
W. MARSHALL - BUSINESS MANAGEMENT AND SCIENTIFIC SYSTEMS  
R. K. SAYED - TECHNICAL INFORMATION ORGANIZATION  
L. C. JESSUP - FIELD ENGINEERING

CORPORATE:

J. R. COX  
S. C. SCHMIDT  
P. R. BERTRAND  
J. KANE  
T. H. YEAGER



# RELEASE PLAN - TC5110



1976

Form A-3834 (Rev 59)

# BDS PRODUCTION SCHEDULE

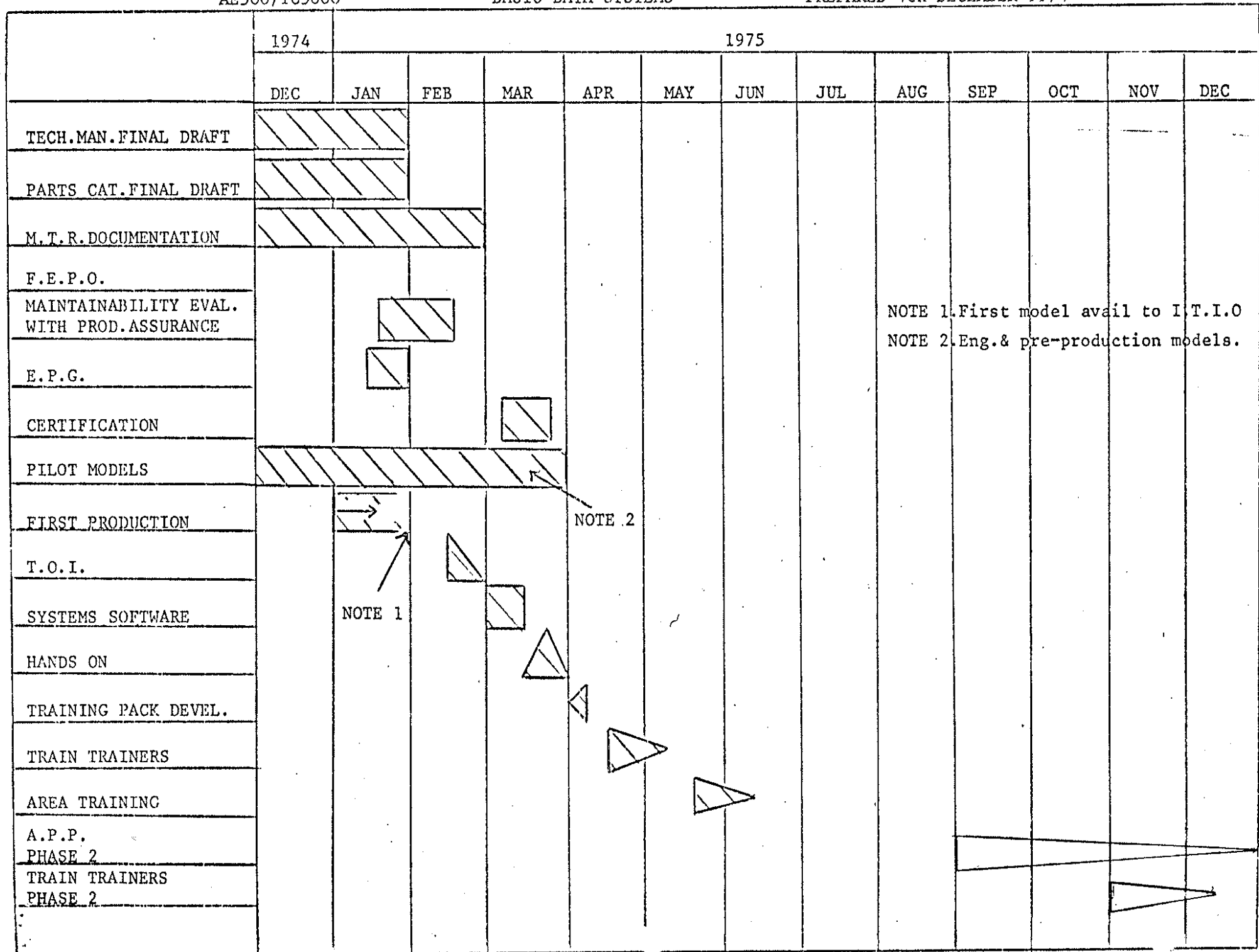
	<u>1975</u>		<u>1976</u>		<u>F&amp;SSG - PLYMOUTH</u>	
	<u>PLYMOUTH</u>	<u>CUMBERNAULD</u>	<u>PLYMOUTH</u>	<u>CUMBERNAULD</u>	<u>1975</u>	<u>1976</u>
AE501	382	307	843*	348	4	9
AE511	0	0	100	271	0	5
TC5110	326	811	1,017	933	0	15
TC5210	0	37	70	273	}	45 - 180 CPS SYSTEMS IN 1976
TC5220	0	0	200	110		
TC5240	0		154			
TC5310	0	0	130	437		
TC5340/TC5440	0	0	190	242		
TC5410	0	0	420	319		
B80 - 210	0	23	467	368		
B80-231	0	0	222	215		
B80-240	10	12	255	472	}	
B80-310	0	0	300	294		
B80-340	0	6	538	498		

PLYMOUTH PRODUCTION FOR BMG

CUMBERNAULD PRODUCTION FOR INTERNATIONAL

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\* 256 of this total of 843 is for International.



BASIC DATA SYSTEMS  
1st QUARTERLY 1974 PRODUCTION

<u>MACHINE NUMBERS</u>	<u>REQUIRED FOR</u>	<u>ESTIMATED AVAILABILITY DATE</u>
B1001-308 B1002	ITIO C'NAULD ENG. (ELECTRICAL SAFETY	JANUARY
B1003	C'NAULD ENG. (DATA COMM SYSTEMS CERTIFICATION)	
B1004	ITIO	
B1005	C'NAULD ENG. (DATA COMM SYSTEMS CERTIFICATION)	FEBRUARY
B1006	C'NAULD ENG. (ELECTRICAL SAFETY)	
B1007	A.P.P. (AREA PARTICIPATION PROGRAM)	
B1008	A.P.P.	
B1009	BMG FE	MARCH
B1010	INT'L FE	
B1011	INT'L FE	
B1012	BMG DEMO	
B1013	BMG DEMO	
B1014	BMG DEMO	
B1015	INT'L DEMO	
B1016	INT'L DEMO	
B1017	C'NAULD ENG. (FUTURE PROGRAM CAPTIVE)	
B1018	PLYMOUTH PLANT	
B1019	C'NAULD ENG. SOFTWARE	
B1020	C'NAULD ENG. SOFTWARE	

# BDS PHASE I APP & TRAIN THE TRAINERS

1975

JAN				FEB				MAR				APR				MAY				JUNE				JULY				AUG				
6	1	2	2	1	1	2	3	1	1	2	3	1	2	2	5	1	1	2	6	2	9	6	3	0	7	4	1	8	4	1	8	5

Transfer of Information

Software/Firmware

Hands On

Training Package Development

Train Trainers

APP  
CUMBERNAULD

TRAIN TRAINERS  
U.K.

U.K.  
FRANCE  
HOLLAND  
CANADA  
BRAZIL  
AUSTRALIA  
JAPAN  
BMG

GERMANY  
ITALY  
SWITZERLAND  
ARGENTINA  
MEXICO  
SOUTH AFRICA

PRE-REQUISITE - ANY MICRO BASED SYSTEM PLUS DATA COMM KNOWLEDGE

## MINI DISK STATUS B9489

20 PRE PRODUCTION UNITS SCHEDULED FOR 1ST QUARTER 1975

F.E. TRAINING TO COINCIDE WITH BDS PHASE II (MID 75)

### RELIABILITY

PROTOTYPE TESTING INDICATES HIGH MTBF (PRODUCT SPEC - 3000 HRS)

NO P.M. IS CALLED OUT.

HEAD WEAR IS MINIMISED, BY HAVING ONLY ONE HEAD IN CONTACT WITH DISK AT A TIME.

### OPERATOR MAINTENANCE

PERIODIC HEAD CLEANING USING CLEANING DISK.

### ADJUSTMENTS

HEAD ALIGNMENT, PRELOAD NUT, SECTOR INDEX DELAY, DATA WINDOW, WRITE CURRENT AND ERASE CURRENT.

### TOOLS REQUIRED

HAND TOOLS.

DIGITAL METER OR OSCILLOSCOPE.

ALIGNMENT CARTRIDGE.

(DISK EXERCISER?)

### TROUBLESHOOTING

PROCESSOR DRIVEN ROUTINES.

F. E. MONITORS RESULTS WITH DIGITAL METER AND PERFORMS ANALYSIS.

REPAIR TO COMPONENT.-

MTTR - 3 HOURS .

MINI DISK PRODUCTION

BURROUGHS 1MB:

<u>1975</u>	<u>1976</u>	<u>1977</u>	<u>1978</u>
1	1420	3000	5817

INDUSTRY COMPATIBLE 2.43KB:

<u>1975</u>	<u>1976</u>	<u>1977</u>	<u>1978</u>
53	245	612	1128



## B700 IMMEDIATE ACTION PROGRAM

### OBJECTIVES:

- RAISE ORDER LEVEL
- MAXIMIZE REVENUE & PROFIT POTENTIAL OF B705, B711 & B721

### STRATEGY:

- INCREASE COMPETITIVENESS OF B705 & B711 BY
  - LOWER-COST PERIPHERALS
  - PRICING ACTION
  - AVAILABILITY OF WIDER RANGE OF BMS'S
- RELEASE & PROMOTE B721
  - ADDED CAPABILITIES
  - FIRST B700 MICR SYSTEM
- IMPROVE MARGINS ON B705 & B711
  - COST REDUCTION PROGRAMS (E.G. MOS MEMORY)
- INCREASE PERFORMANCE, AVERAGE PRICE & GPM ON B721
  - RELEASE DIRECT AUDIT ENTRY WITH BACKGROUND BATCH PROCESSING
  - PROGRAMMABLE MICR
  - SL9 COBOL
  - 2 MHZ MACHINE
  - 205/206 DISK

} DOUBLE PERFORMANCE

HIGHER SPEED PERIPHERALS

## B700 COST REDUCTION PROGRAM

### 1975 PROGRAM

- REPLACE B711 CORE MEMORY WITH MOS
  - ELIMINATE A BACK-PLANE & CARD CAGE
  - CHANGE POWER SUPPLY
- LOWER-COST SOCKETS
- LARGER CHIP FOR NANO-MEMORY

NET COST REDUCTION FOR B705/711 ..... \$600 (BY 3Q75)

- SHOULD BE REFLECTED IN 1976 WCTP

### CSG ESTIMATE OF BASIC COST REDUCTIONS (74-3 FORECAST)

	<u>1976</u>	<u>1977</u>	<u>1978</u>
	VS	VS	VS
	<u>1975</u>	<u>1976</u>	<u>1977</u>
B 711	354	423	323
B 721	851	495	599
32 KB MOS MEMORY	636	264	180

NEED TO IDENTIFY FURTHER ENGINEERING COST REDUCTIONS

B 711-1

INCORPORATION OF B9343-21/41 (ELECTRONIC K/B) CONSOLES AND NEW  
CONSOLE CONTROLLER IN B711/705 PROCESSORS.

MTR RELEASE 5 (FEB. 1975) HAS PROGRAMS FOR B711-1.

NEW CONSOLE AND CONSOLE CONTROL INCORPORATED IN B721 TRAINING.

SALES ANNOUNCEMENT ON P.I.B. #29 IIS.

B 721

B700 PROCESSOR WITH MOS MEMORY, DMAC, PMLC, ELECTRONIC KEYBOARD  
CONSOLE.

BISA TRAINING                      JANUARY 27                      FRANCE

A & P TRAINING                      FEBRUARY 3                      WAYNE, PA.

PRODUCT RELEASE                      JAN/FEB. 1975

PRODUCTION SCHEDULE (INTERNATIONAL)

<u>1974</u>	<u>1975</u>	<u>1976</u>	<u>1977</u>	<u>1978</u>
10	329	387	747	1184

December 6, 1974

Mr. P. Knudsen  
Burroughs International S.A.  
18, rue St. Pierre  
1700 Fribourg, Switzerland

Mr. H.W. Dixon  
World Headquarters  
Room 3C27

Subject: B721 Area Training

Dear Per & Dick:

The attached pages outline the current plans for B721 Divisional Training. This is an update and revision to the schedules outlined in my letters to C.E. Payne, R.W. Haskell and C. Vincent dated September 24, 1974.

Sales training will be completed by the end of January 1975. The marketing plan is to have initial demonstration systems ready by the end of February 1975.

I do not anticipate any further delays or changes to this program. The Society General system is already in Paris, another two systems will be shipped to France this year. BMG Equipment will be used for the A & P training in Wayne, PA.

Advance copies of all available technical literature and a complete MTR release will be mailed direct to Cergy Pantoise, Attn. F. Armaingaud by TIO Downingtown.

One copy of all technical information generated at the B721 APP is being mailed to F. Armaingaud for use by K. Treherne and G. Pilon in training course preparation.

L.C. JESSUP  
F.E. Product Manager

LCJ/rjo (Attachment)

cc: F. Armaingaud  
W. Douglas  
C. Habert  
R. Haskell  
E.A. Lawrence  
W. Mahoney  
C.E. Payne

J. Peggram  
K. Ploemacher  
N.J. Sendler  
A. Sichello  
H.P. Smith  
C.B. Turner  
C. Vincent

B721 TRAINING OUTLINE JAN. TRAIN TRAINERS

WEEKS

<u>1/2</u>	<u>2</u>	<u>1</u>	<u>1/2</u>
SOFTWARE	PROCESSOR		
BIG PICTURE OF SL7	DIFFERENCES	PMLC	HANDS
& INTERPRETORS	NEW CONSOLE	&	ON
EXPLANATION OF	& I/O CONTROL	DATA COMM	
VARIOUS ERROR	DISK DMAC	NDL &	
MESSAGES		MINI D	
SPECIAL ATTN TO		SOFTWARE	
LOADER			

OBJECTIVE - PROVIDE ADD-ONS TO EXISTING B700 TRAINING

ELIMINATE THE B700 PRE-REQUISITE FOR B721

B721 DEMMO CONFIGURATION

PROCESSOR WITH 48 KB MEMORY

CONSOLE B9343-22 AND CONTROL

B9480

DISK AND DMAC CONTROLLER

B9481

I/O EXPANSION UNIT

SLC AND TD 700 (ENQUIRY)

PMLC WITH SPM AND LINE ADAPTERS

TD 800 AND TC 3500 WITH CASSETTE

A9114 CARD RDR AND CONTROL

A9419-2 96CC AND CONTROL

A9249-3 PRINTER AND CONTROL (B9247 LINE PRINTER AND  
CONTROL OPTIONAL)

A9490 AND CONTROL

DATA SETS AND CABLES

1975 B721 ALLOCATIONS

DEMO SYSTEMS

BISA:

FRANCE (F.E. & SALES)

ITALY

GREAT BRITAIN (F.E. & SALES)

HOLLAND

SOUTH AFRICA

SWEDEN

SWITZERLAND

SPAIN

LATIN AMERICA:

BRAZIL (F.E. & SALES)

MEXICO

PAC. & CANADA:

AUSTRALIA

CANADA (F.E. & SALES)

NEW ZEALAND

JAPAN (F.E. & SALES)

NOTE: SYSTEMS MUST BE ORDERED BY SUBSIDIARIES. SEE C. HABERTS  
LETTERS TO SALES DEPTS DATED JULY 11 AND AUGUST 30, 1974  
FOR FULL DETAILS.



INTERNATIONAL TRAINING PLANS

K. TREHERNE (U.K.)

INSTRUCTORS

G. PILON (CANADA)

- 1) J.C. ECREPONT INSTALL SOCIETY GENERAL SYSTEM IN CERGY PANTOISE  
DECEMBER 1974.
- 2) JANUARY 6, 1975 - TREHERNE AND PILON WORK WITH ECREPONT IN PARIS  
TO UPDATE KNOWLEDGE GAINED DURING APP AND PREPARE HARDWARE TRAIN-  
ING COURSE.
- 3) PREPARE SOFTWARE TRAINING FOR SL7 SYSTEMS (B711 AND B721) INPUTS  
MAY BE REQUIRED FROM ILLINGWORTH AND ECREPONT.

DURATION (1,2,3) 2-3 WEEKS

- 4) RUN DIVISIONAL TRAINING JANUARY 1975

A & P  
G. PILON \*  
JANUARY 27

BISA  
K. TREHERNE  
J. ECREPONT (CONSULTANT)  
JANUARY 20 or 27 IN CERGY PANTOISE

---

BRAZIL

MEXICO

AUSTRALIA

NEW ZEALAND

JAPAN

CANADA

---

SPAIN

HOLLAND

U.K.

SOUTH AFRICA

SWEDEN

SWITZERLAND

ITALY

FRANCE

\* COURSE IN WAYNE, P.A. IN CONJUNCTION WITH BMG.

B 721 1975 ENHANCEMENTS

B489-4 DISK MULTIPLEXOR 2ND QUARTER 1975

INTERNATIONAL MICR HANDLER 4TH QUARTER 1975

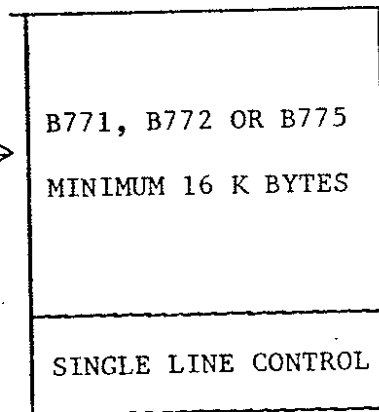
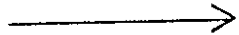
PROVIDES PROGRAMMABLE FIELDS TO ENABLE NATIONALISTIC PROGRAMS  
WITH B9135 READER SORTER.

IBM RJE FOR B770 SERIES

2780/3780 LOOK-ALIKE

INPUT

80 COLUMN CARD  
CASSETTE TAPE  
MAG TAPE  
DISK CARTRIDGE



OUTPUT

LINE PRINTER  
80 COLUMN CARD  
CASSETTE TAPE  
MAG TAPE  
DISK CARTRIDGE



BINARY SYNCHRONOUS SYSTEM INTERFACE

HASP INTERFACE

POWER INTERFACE

AVAILABILITY - 3780 LOOK-ALIKE OR B771/2 - 8/75

3780 LOOK-ALIKE OR B775 - 11/75

2780 LOOK-ALIKE - DATE IN REVIEW

B775 SYSTEM & COMMUNICATION PROCESSOR

PRICING - IN APPROVAL CYCLE

PRODUCT ANNOUNCEMENT - FEBURARY 1975

FIRST PRODUCTION AVAILABILITY - SEPTEMBER 1975

WILL INCLUDE:

SL9 INTERPRETER

MCS COMPILER - ON BOARD

COBOL COMPILER - ON BOARD

NDL COMPILER - ON BOARD

OPERATING SYSTEM

PERIPHERALS - DISK CONSOLE PRINTER, CARD READER,  
& LINE PRINTER

PROCESSOR - 96 K BYTES MEMORY

8 I/O PORTS

16 DATA COMM LINES - 1 DCP

REAL TIME CLOCK

## PROJECT STATUS

### 1. INVESTIGATING IMPROVED THROUGHPUT FOR COMMUNICATION PRE-PROCESSOR.

IMPROVEMENT OVER PREVIOUS PREDICTIONS BASED ON MORE DETAILED ANALYSIS.

	<u>TOP DOWN SCAN</u>	<u>ROUND ROBIN SCAN</u>
1 LINE	50 K BITS PER SECOND	50 K BITS PER SECOND
4 LINES	43 K BPS TOTAL	9.6 K BPS EACH LINE
8 LINES	38 K BPS TOTAL	4.8 K BPS EACH LINE
16 LINES	27 K BPS TOTAL	2.4 K BPS EACH LINE

EMIT M OBJECT CODE FOR COMM. PROC. AND EXECUTE DIRECTLY RATHER THAN INTERPRET -- PREDICTED TO BE 2 TIMES PERFORMANCE INCREASE.

BIPOLAR MPM FOR COMMUNICATION PRE-PROCESSOR--40% PERFORMANCE INCREASE.

### 2. ENGINEERING STUDY IN PROCESS TO DETERMINE THE NUMBER OF COMMUNICATION PRE-PROCESSORS THAT CAN BE CONNECTED. RESPONSE BY END OF FEBURARY, 1975.

### 3. BUDGET APPROVED AND NEW PDA IN PROCESS TO COVER:

2 MHZ B776 PROCESSOR

COMMUNICATION PRE-PROCESSOR IMPROVEMENTS

RECOVERY & STANDBY CAPABILITIES

LINE SWITCHING

PERIPHERAL SWITCHING

PROCESSOR TO PROCESSOR COMM.

SHARED DISK

POWER FAIL/RECOVERY

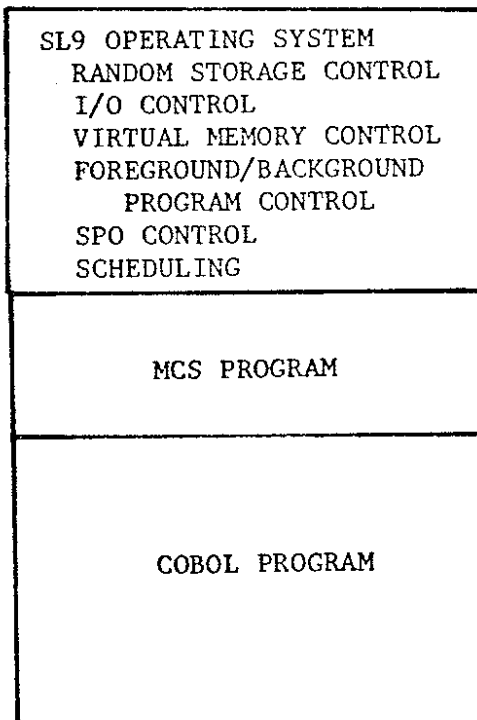
NETWORK DIAGNOSTICS

ERROR LOGGING

SL9 SYSTEM. - B775

SL9 INTERPRETER

COBOL	MCS	NDL
RESIDES IN BASE B775 PROCESSOR		RESIDES IN PRE-PROCESSOR



# DOWNINGTOWN B700 PROGRAM

1975

1976

