COE 0147 Spring 2013 Lab #7: Multiplication and Division

Each of you should submit your own solution, according to the instructions at your TA's website. Each person must turn in their own copies of the lab. If you choose to work with a neighbor/partner, put your partner's name on your submitted copy of the lab.

For the written part, you should turn in a hard copy of this assignment at the beginning of the following recitation meeting either in recitation class or in your TA's mailbox. Staple multiple pages together and do not forget to put your name on. Do not present answers out of order.

- 1. Perform three subtractions on 9-bit 2's complement binary numbers as follows:
 - a) 195 47
 - b) 87 59
 - c) -46 71

For each of these subtractions, convert the numbers into 9-bit 2's complement, add those and convert the resultant binary 2's complement number into decimal form. Show all your work.

a)
$$195 = 2^{3} + 2^{6} + 2^{1} + 2^{0}$$
 $= 011000011$
 $105 - 47 = 011000011$
 $+ 111010001$
 $= 2^{3} + 2^{4} + 2^{1} + 2^{1}$
 $= 010010100$
 $= 2^{3} + 2^{4} + 2^{1} + 2^{1}$
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2. Show the steps for the multiplication of 10010101b and 10101111b (unsigned) using **Hardware Design 3** (http://www.pitt.edu/~kmram/CoE0147/lectures/numbers3.pdf). Here 10010101b is the multiplicand and 10101111b is the multiplier.

Draw a table and fill up the columns:

Iteration	Multiplicand	Implementation 3	
		Step	Product(16-bit)
0	10010101	mit	0000 0000 1010 1111
1	1	1 > product = product + multiplicated shift right	0100 1010 1101 0111
2		1 → product = product t multiplicand Shift night	0110 1111 1101 0111
3	10 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1 - product = product + multiplier	0000 0100 1110 1011
4	department of the second	Shift tight = product imultiplier Shift right	1000 1011 0111 0101
5	and the second s	07 ho 0 p Shift right	0100 0101 1101 1101
6		1- product = product + multiprer Shift make	0148 4101 0110 1110
7		0-3 noop Shift nahr	0014 0110 1011 0111
8	Ţ	1-> product = product + multiplier Shift right	010 1011 1011 011

Show your work (addition of product and multiplicand in each iteration) on the next page.

- 3. Convert the following 8-bit binary numbers into Booth's encoding form:
 - 4) 01010011; 11010101; 01000001°

4. Convert the following decimal numbers into 9-bit binary numbers in Booth's encoding form:

bath's:

$$-201$$
 : $201 = 2^7 + 2^6 + 2^3 + 2^0$

two's comp:

100110110

books: 1010-100-1

= 000110000

wois comp:

111001111

+000000001

1110100000

booth's:

00-11-10000

5. Show the steps for the multiplication of 10010101b and 10101111b (signed) using Booth's algorithm ($http://www.pitt.edu/\sim kmram/CoE0147/lectures/numbers3.pdf$). Here 10010101b is the multiplicand and 10101111b is the multiplier.

Draw a table and fill up the columns:

Iteration	Multiplicand	Booth's Algorithm		
		Step	Product(17-bit)	
0	10010101 (+)	thit	0000 0000 1010 1111 0	
1)	10 + prod = prod - mult Shift right	0110 1011 1010 1111 0	
2		shift right	0001 1010 1110 1011 1	
3	and the second second	11-> no op Shiff right	0000 1101 0111 0101 1	
4	di interpreta	11= no op Shiff nght	0000 0110 1011 1010 1	
5		01 -> prod = prod + mult Shift right	1100 1101 1101 1101 0	
6		10 > prod = prod - mult shat muht	0011 1000 1101 1101 0	
7		01-> prod = prod + mult Shift hant	1011 0001 0110 1110 1	
8	1	10 + prod = prod - mult Shift right	0100 0011 1011 0111 0	

Show your work (addition of product and multiplicand in each iteration) on the next page.

+ 01101011

1 01000011

6. Show the steps for the multiplication of 01100001b and 01011011b (signed) using Booth's algorithm (available here: http://www.pitt.edu/~kmram/CoE0147/lectures/numbers3.pdf). Here 01100001b is the multiplicand and 01011011b is the multiplier. Draw a table similar to the following one and fill up the columns:

Iteration	Multiplicand	Booth's Algorithm		
		Step	Product(17-bit)	
0	0110 0001 (1)	Init	0000 0000 0101 1011 0	
1		10 -> prod = prod -mult shift right	1001 1111 0101 1011 0	
2		11 - no op Shiff right	1110 0111 1 101 0110 1	
3		01 -> prod = Prod +mult	000 1000 1101 0110 1	
4		Shift right 10 - prod = prod - mult shift right	1100 0011 0110 1011 0	
5		11 - no op Shift right	1111 0000 1101 1010 1	
6		ol → prod = prod + mult Shift right	01010001 1101 1010 1	
7		10 - prod = prod - mult Shift ngut	1100011 1110 1101 0	
8		01 -> prod = prod + mult Shift right	01000100 1111 0110 1	

15 14 13 12 11 109 18 14 14 15 11 15

7 In a table similar to the following one, show the steps for computing 00111000b (the dividend) divided by 0011b (the divisor, both numbers are unsigned) using **non-restoring division**. Non-restoring division is described online at: http://www.pitt.edu/~kmram/CoE0147/lectures/division.pdf. An example of doing non-restoring division is also shown.

Iteration	Step (description)	Divisor (4 bits)	Dividend (8 bits)	Remainder Register (8 bits)
0	init Shiff left	0011	00111000	0000 011 0000
1	rem = rem -dnsar remco > sl, 10=0			1010 1110 0000
2	remed = st, r0 = 0		The state of the s	101 1110 0000
3	rem = rem + divisor rcm <0 >SL, r0=0	1000	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1110 1100 0000
Ч	rem = rem + divisor rem >0 > SL, r0=1		1 (4) + 1 (4) (4) (4) (4) (4) (4) (4) (4) (4) (4)	0000 1000 0000
G	19m = 19m-div 1em = 0 = SL, +0=0			1110 0000 0010
Ь	rem = rem + div remco & SL, 10=0			111 0 0000 0010
7	1cm = 1cm + dN 1cm > 0 + SL, YO=1	1	1	00 1 0000 0100
8	rem = 10m - div	:		1110 0001 0010
done	shift LH right remed = add dmsn			0010 0001 0010