

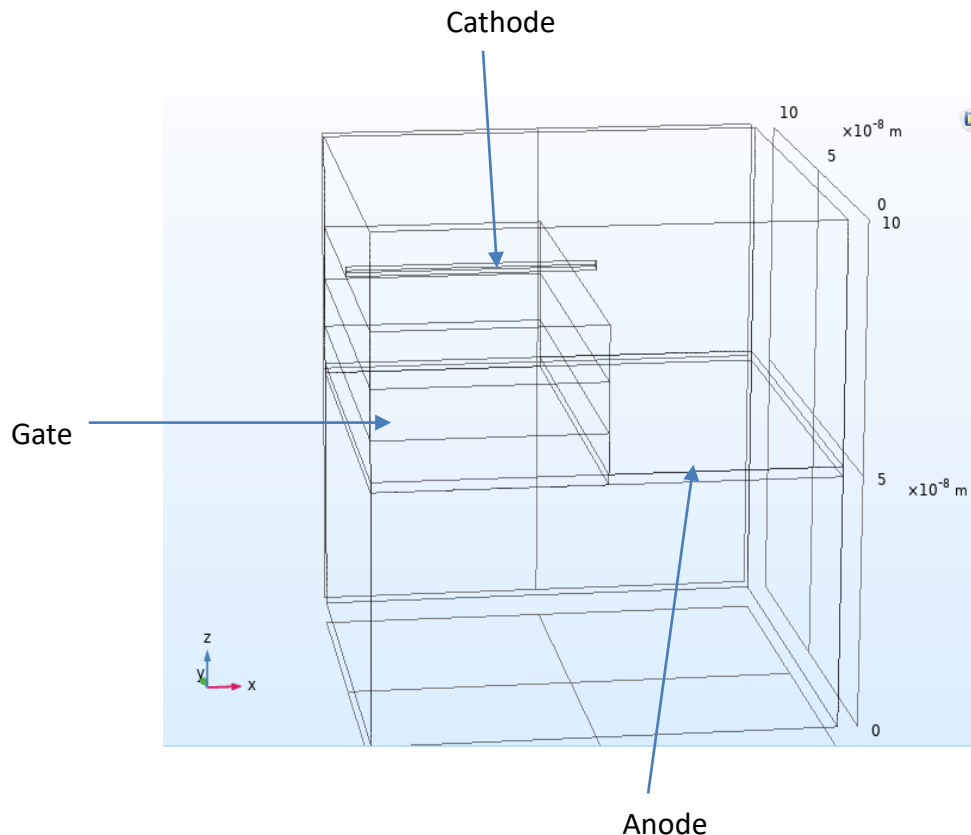
ANALYSIS OF CNT BASED VACUUM TRANSISTOR

-MODELING OF 3 TERMINAL DEVICE

Savankumar Prajapati, Electrical Engineering

Prof. Ivan Puchades, Electrical & Microelectronic Engineering

3 Terminal Model



- Gate terminal is between the anode and cathode terminal.
- Al is used as Gate terminal as well as Anode material.
- Cathode is made of CNT.
- Maximum electric Field is extracted from the Cathode terminal and by using F-N equation, current density is obtained.
- Anode to Gate terminal distance is 26 nm.

Calculations

- ❑ Fowler Nordheim equation to extract the current density:

$$J = \frac{K1 \times \beta^2 \times E^2}{\phi} \exp\left(\frac{-K2 \times \phi^{1.5}}{\beta \times E}\right)$$

Where J = Emitted Current density from the cathode

$$K1 = 2.73 \times 10^{-7} \text{ AV}^{-2} \text{ eV}$$

$$K2 = 6.83 \times 10^9 \text{ VeV}^{-1.5} \text{ Vm}^{-1}$$

$$\beta = \text{Field Enhancement Factor} = 15$$

$$\phi = \text{Work Function of Cathode material} = 5 \text{ eV}$$

- ❑ Current Density is multiplied by the area of the cathode terminal to get the emitted current.

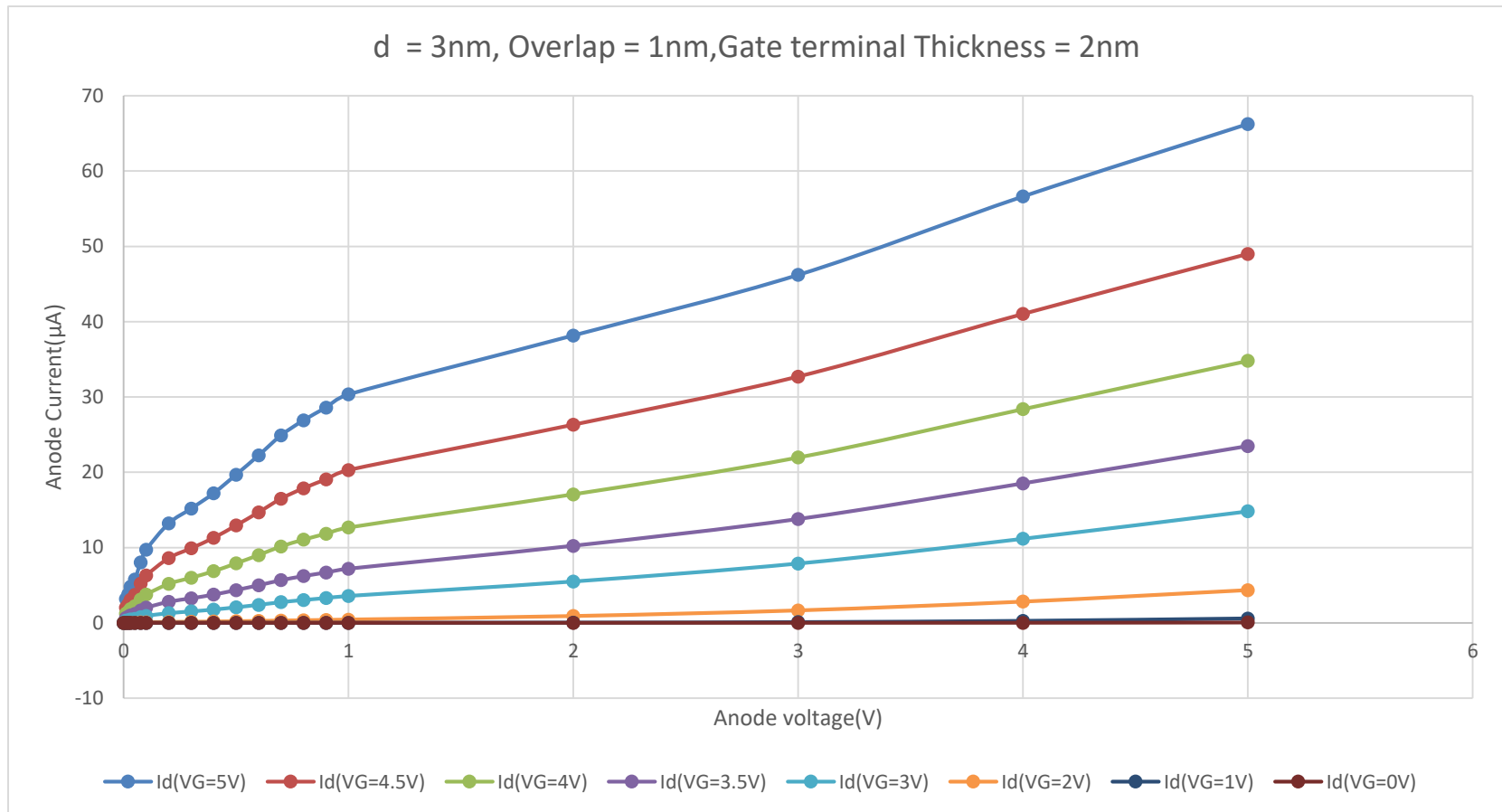
Procedure

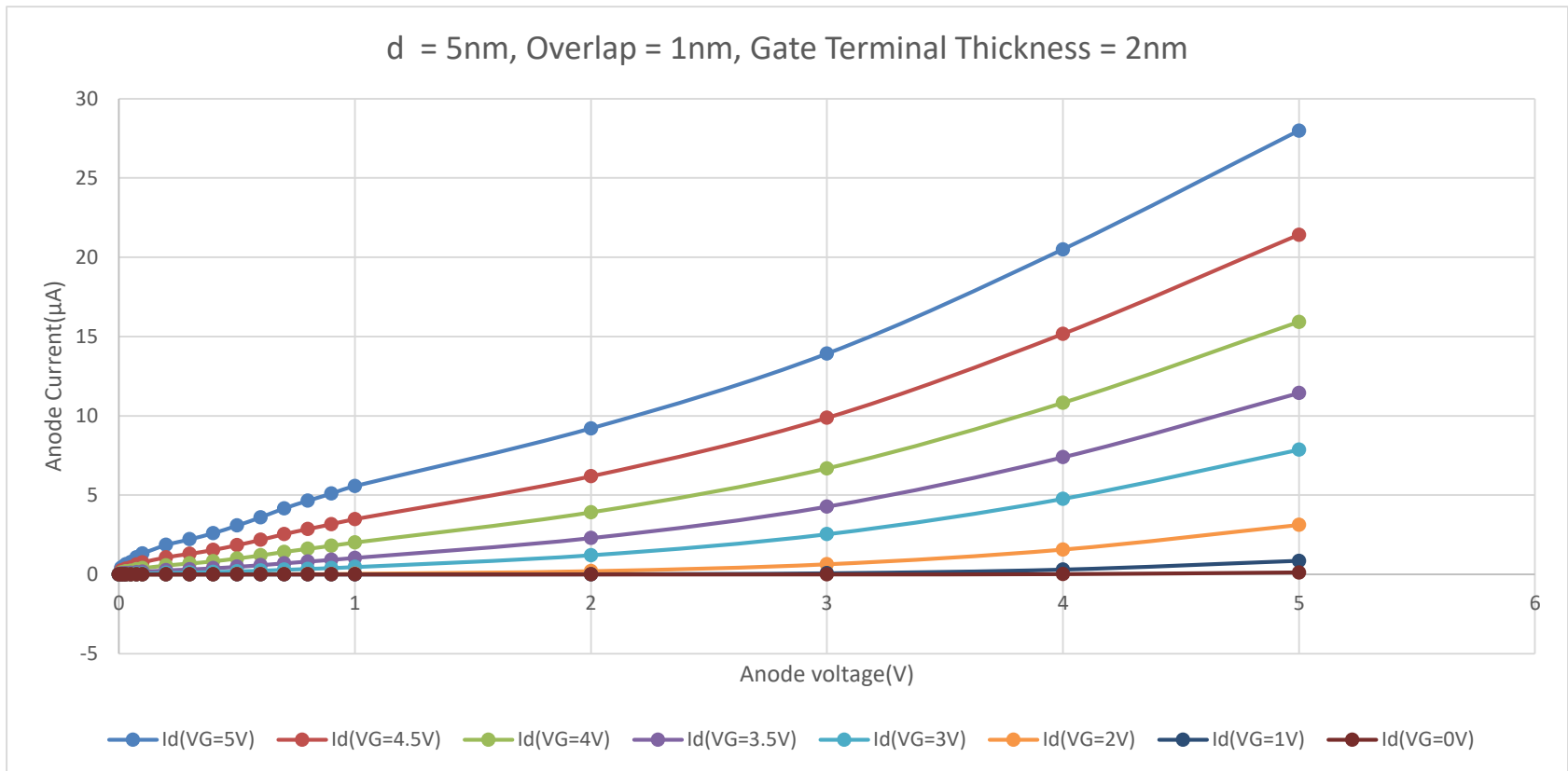
- ❑ All emitted electrons will not reach the anode terminal due to the gate leakage current.

$$\text{Anode Current} = \text{Collection Efficiency} \times \text{Emitted Current}$$

- ❑ Family of Curves are plotted for the different gate voltages.
- ❑ Different cases are considered for family of curves and best case is considered.
- ❑ For the 2 best cases, threshold voltage is measured and Compared.

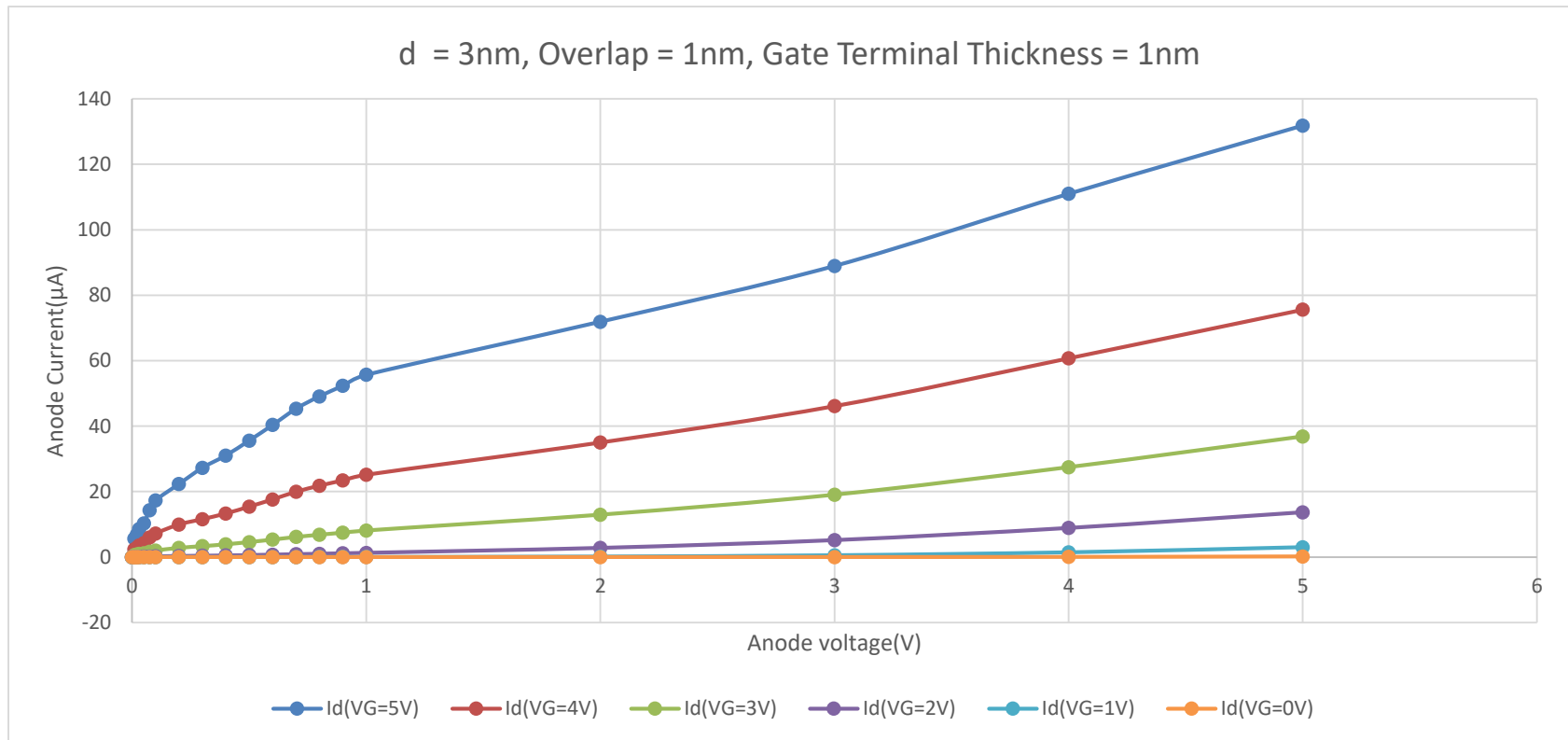
Case Study : 1 Changing the Distance between Cathode and Gate Terminal(d) and Overlap





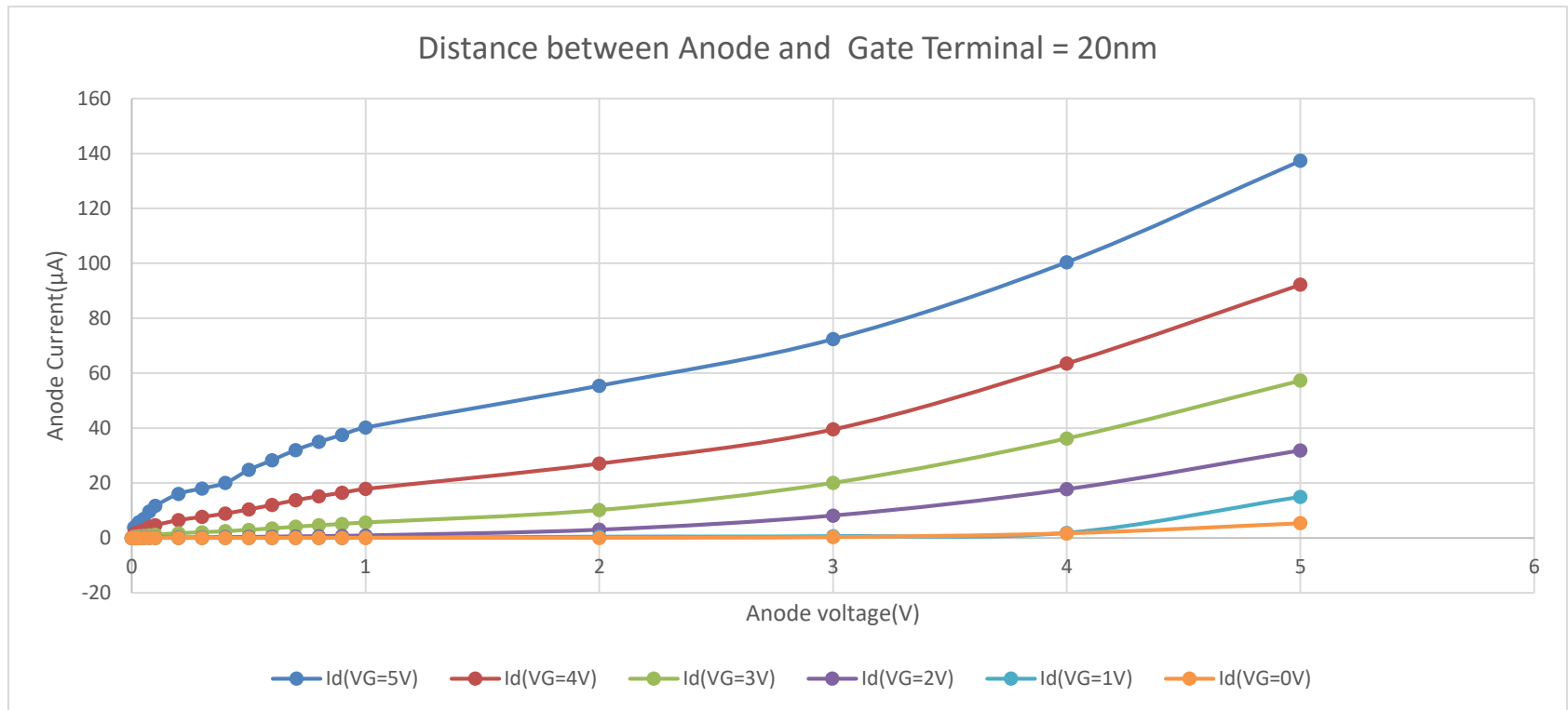
❑ As the distance between cathode and gate terminal increases, Anode current decreases.

Case Study : 2 Varying thickness of Gate Terminal



❑ As the thickness of Gate Terminal decreases, Anode current increases.

Case Study : 3 Changing the distance between Anode and Gate Terminal



❑ As the distance between Anode and Gate terminal decreases, Anode current increases.

Results-Conclusion

- For the best case possible, $d = 3$ nm (Cathode to Gate distance)

Gate terminal Thickness = 1 nm

Distance between Anode and Gate terminal = 20nm

