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Ground DPI GPCLK JTAG 1-WIRE PCM SDIO I2C
SPI UART WiringPi

Raspberry Pi Pinout

3v3 Power	1	2	5v Power
BCM 2 (SDA)	3	4	5v Power
BCM 3 (SCL)	5	6	Ground
BCM 4 (GPCLK0)	7	8	BCM 14 (TXD)
Ground	9	10	BCM 15 (RXD)
BCM 17	11	12	BCM 18 (Backlight Control)
BCM 27	13	14	Ground
BCM 22	15	16	BCM 23
3v3 Power	17	18	BCM 24 (RT Interrupt)
BCM 10 (MOSI)	19	20	Ground
BCM 9 (MISO)	21	22	BCM 25 (TFT Data/Command)
BCM 11 (SCLK)	23	24	BCM 8 (TFT Chip Select)
Ground	25	26	BCM 7 (RT Chip Select)
BCM 0 (ID_SD)	27	28	BCM 1 (ID_SC)
BCM 5	29	30	Ground
BCM 6	31	32	BCM 12 (PWM0)
BCM 13 (PWM1)	33	34	Ground
BCM 19 (MISO)	35	36	BCM 16
BCM 26	37	38	BCM 20 (MOSI)
Ground	39	40	BCM 21 (SCLK)

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PiTFT Plus 3.5"

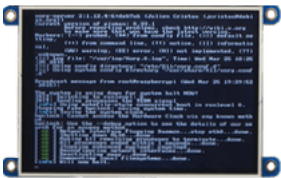
The PiTFT+ features a 3.5" display with 480x320 16-bit color pixels and a resistive touch overlay and is engineered specifically to work with the Raspberry Pi 3, 2 and the Model A+/B+.

The display uses the hardware SPI pins as well as GPIO #25 and #24. GPIO #18 can be used to PWM dim the backlight. A 2x13 'classic' GPIO header on the bottom, with additional GPIO pins broken out into solder pads, allows you to use more of the GPIO.

The PiTFT+ can be used as a display for running the X interface, or the console. You can also have an HDMI display separately connected to complement the setup, keeping in mind that there can only be one X session running (so you'll need to choose where X should be output, on the HDMI or the PiTFT+).

Details

- Made by [Adafruit](#)
- 40 pin header
- EEPROM auto configuration
- Needs 5v and 3v3 power
- Uses 8 GPIO pins
- Communication over SPI
- [More Information](#)
- [GitHub Repository](#)
- [Schematic](#)
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Spotted an error, want to add your board's pinout? Head on over to our [GitHub repository](#) and submit an Issue or a Pull Request!
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