Dear Editor and Reviewers:

This is an expanded conference paper which was accepted for oral presentation in China Semiconductor Technology International Conference (CSTIC).

The conference paper information:

Huiming Tian, and Zhufei Chu, "A Novel Cellular Array Design Using Quantum-dot Cellular Automata", CSTIC' 20, China Semiconductor Technology International Conference, Mar 2020 (delayed due to Covid-19 virus situation), Shanghai, China. (FULL TEXT is attached)

The main difference between the expanded version and the original one are:

1. A new arithmetic unit (AU) QCA design is proposed for an improved area and latency.
2. The QCA layout using a single-layer approach is established for the proposed AU, which is more practical for physical fabrication.
3. Designs and experimental results of several *n*-bit GPCA are demonstrated. And the theoretical discussion of the latency is also addressed.
4. More technical details, additional experiments and evaluations are added.

We summarized the related work and highlight the major difference in Section 2.

Sincerely yours,

Dr. Zhufei Chu

Ningbo University, China