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C/C++ tip: How to detect the processor type using compiler predefined macros

February 9, 2012 Topics: C/C++

Processor macros are predefined by all C/C++ compilers to enable #if/#endif sets to wrap processor-specific code, such as in-line assembly for SSE instructions on x86 processors. But there are no standards for processor macros. The same compiler may have different macros on different operating systems, and different compilers for the same processor may have different macros. This article surveys common compilers and shows how to use predefined macros to detect common desktop and server processors at compile time.

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Further reading

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How to list predefined macros

See How to list compiler predefined macros for instructions on getting a list of macros for the compilers referenced here.

How to detect the processor type

Throughout the following sections note:

- Red text indicates deprecated macros that don't start with an underscore. C++ compilers, and C compilers in standards compliance mode, do not define them.
- Green text indicates recommended macros that are well-supported and useful for detecting a specific OS.

Itanium

A.K.A.: IA64

Developer: Intel

Processors: Itanium, Itanium 2, Itanium 2, 9000/9100/9300, etc.

Itanium

				•		
	GNU GCC/G++		HP C/aC++	Intel ICC/ICPC	Microsoft Visual Studio Windows	
	BSD,	UD IIV	UD IIV	Linux		
Macro	Linux	HP-UX	HP-UX	-	windows	
ia64				yes		
ia64	yes	yes	yes	yes		
ia64	yes	yes		yes		
IA64		yes				
itanium	yes	yes		yes		
_M_IA64					yes	

Notes:

- "IA64" is the old name for the processor architecture. Intel now prefers "Itanium".
- There is no single Itanium processor macro defined by all compilers on all OSes. An #if/#endif that checks multiple macros is required.
- Microsoft's support for Itanium ended after Visual Studio 2010 and Windows Server 2008.
- Clang/LLVM currently does not support Itanium processors.

POWER

A.K.A.: PowerPC **Developer:** IBM, Freescale

Processors: PowerPC, POWER 1/2/3/4/5/6/7, G1, G2, G3, G4, G5, etc.

POWER 32-bit

	Clang/LLVM	GNU GCC/G++							IBM XL C/C++	
Macro	BSD, Linux, OSX	AIX	FreeBSD	Linux	NetBSD	OpenBSD	osx	AIX	Linux	
_ARCH_PPC	yes									
_POWER		yes								
powerpc			yes	yes						
powerpc			yes	yes				yes	yes	
powerpc	yes	yes	yes	yes	yes			yes	yes	
PowerPC			yes							
POWERPC	yes						yes			
PPC			yes	yes		yes				
ppc	yes		yes				yes			
PPC			yes	yes		yes		yes	yes	

__PPC__ yes yes yes yes yes yes

			POWER 6	64-bit				
Clang/LLVM	IBM XL C/C++							
BSD, Linux, OSX	AIX	FreeBSD	Linux	NetBSD	OpenBSD	osx	AIX	Linux
yes								
yes								
	yes							
			yes				yes	yes
yes	yes	yes	yes	yes			yes	yes
yes		yes	yes				yes	yes
		yes						
yes						yes		
yes		yes						
							yes	
	BSD, Linux, OSX yes yes yes yes yes yes	BSD, Linux, OSX AIX yes yes yes yes yes yes yes yes	Clang/LLVM GNU GCC/G++ BSD, Linux, OSX AIX FreeBSD yes yes yes yes	Clang/LLVM GNU GCC/G++ BSD, Linux, OSX AIX FreeBSD Linux yes yes yes yes yes	BSD, Linux, OSX AIX FreeBSD Linux NetBSD yes yes yes yes yes yes yes yes yes ye	Clang/LLVM GNU GCC/G++ BSD, Linux, OSX AIX FreeBSD Linux NetBSD OpenBSD yes yes </td <td>Clang/LLVM GNU GCC/G++ BSD, Linux, OSX AIX FreeBSD Linux NetBSD OpenBSD OSX yes yes</td> <td>Clang/LLVM GNU GCC/G++ IBM XL BSD, Linux, OSX AIX FreeBSD Linux NetBSD OpenBSD OSX AIX yes yes yes yes yes yes yes yes</td>	Clang/LLVM GNU GCC/G++ BSD, Linux, OSX AIX FreeBSD Linux NetBSD OpenBSD OSX yes yes	Clang/LLVM GNU GCC/G++ IBM XL BSD, Linux, OSX AIX FreeBSD Linux NetBSD OpenBSD OSX AIX yes yes yes yes yes yes yes yes

yes

yes

yes

Notes:

__ppc64__

PPC__

PPC64

PPC

• There is no single POWER processor macro defined by all compilers on all OSes. An #if/#endif that checks multiple macros is required.

yes

yes

yes

yes

yes

yes

yes

yes

yes

- GCC for AIX, NetBSD, and OpenBSD defines the same macros for 32-bit and 64-bit POWER processors. For AIX,
 __64BIT__ is defined for 64-bit POWER. For OpenBSD, _LP64 and __LP64__ are defined for 64-bit POWER. For NetBSD,
 GCC doesn't provide a macro to check for 64-bit use.
- Apple's OSX support for POWER processors ended after OSX 10.5 Leopard in 2007. The open source Darwin distribution, on which OSX is based, is still available for POWER processors.

SPARC

Developer: Oracle, Fujitsu, Sun

yes

Processors: UltraSPARC I/II/III/IV/T1/T2, SPARC T3/T4, etc.

yes

yes

yes

SPARC

Oracle Clang/LLVM **GNU GCC/G++ Solaris Studio** BSD, Linux, Linux, Solaris **BSD** Solaris Solaris Macro sparc yes yes yes yes yes yes yes yes _sparc _sparc__ yes yes yes

__sparc64__ yes

Notes:

• GCC defines processor name macros depending upon the value of the -march command-line option. These include_sparclite_, _sparclet_, _sparc_v8_, _sparc_v9_, _supersparc_, _hypersparc_, and so forth. However, other compilers don't provide this level of detail and writing code that depends upon these macros is probably a bad idea.

x86 and x86-64

A.K.A. (32-bit): IA-32, i386, x86, x86-32

A.K.A. (64-bit): AMD64, EM64T, IA-32e, Intel64, x64, x86-64

Developers: AMD, Intel

Processors: Athlon, Atom, Core, Core 2, Core i3/i5/i7, Opteron, Pentium, Phenom, Sempron, Turion, etc.

x86 32-bit

	Clang/LLVM			GNU GCC/G++ Intel ICC/ICPC			Portland PGCC/PGCPP	Oracle Solaris Studio	Microsoft Visual Studio	
Macro	BSD, Cygwin, Linux, OSX, Solaris	MinGW	Windows	OSX,	Cygwin, MinGW, Windows	Linux, OSX	Windows	Linux, OSX, Windows	Linux, Solaris	Windows
i 386	yes	yes	yes	yes	yes	yes		yes	yes	
i386	yes	yes	yes	yes	yes	yes		yes	yes	
i386	yes	yes	yes	yes	yes	yes		yes		
_M_IX86			yes				yes			yes
X86		yes			yes					

x86 64-bit

				X00 04-DI	L			
	Clang/Ll	_VM	GNU GCC/G++	Intel ICC	/ICPC	Portland PGCC/PGCPP	Oracle Solaris Studio	Microsoft Visual Studio
Macro	BSD, Linux, MinGW, OSX, Solaris	Windows	BSD, Linux, MinGW, OSX, Solaris, Windows	Linux, OSX	Windows	Linux, OSX, Windows	Linux, Solaris	Windows
x86_64	yes	yes	yes	yes			yes	
x86_64	yes	yes	yes	yes		yes	yes	
amd64	yes	yes	yes				yes	
amd64	yes	yes	yes			yes	yes	
_M_AMD64		yes			yes			yes
_M_X64		yes			yes			yes

Notes:

- There is no single x86 processor macro defined by all compilers on all OSes. An #if/#endif that checks multiple macros is required.
- 64-bit instructions for the x86 architecture originated with AMD and were later adopted by Intel. The _amd64, _amd64__, and _M_AMD64 macros are for legacy support, while the newer _x86_64, _x86_64__, and _M_X64 are vendorgeneric.
- With appropriate command-line options, Clang/LLVM and GCC can build 32-bit and 64-bit binaries for Windows instead of POSIX. The macros they define differ between POSIX and Windows.
- GCC and Clang/LLVM define a variety of processor name macros, depending upon the value of the -march command-line option. These include __i486__, __i586__, __pentium__, __pentiumpro__, __athlon__, __atom__, __core2__, __corei7__, __k8__, and so forth. However, other compilers do not provide this level of detail and writing code that depends upon these macros is probably a bad idea.

Further reading

Related articles at NadeauSoftware.com

C/C++ tip: How to list compiler predefined macros explains how to get a compiler's macros by using command-line options and other methods.

C/C++ tip: How to detect the compiler name and version using compiler predefined macros provides **#if/#endif** sets for detecting common compilers.

C/C++ tip: How to detect the operating system type using compiler predefined macros provides **#if/#endif** sets for detecting desktop and server operating systems using compiler macros.

Web articles

Architectures at Sourceforge.net provides a list of current and obsolete processors and some of the macros used to detect them. Unfortunately, the list is out of date, occassionally wrong, doesn't include several compilers, and doesn't show differences between compilers and OSes.

List of CPU architectures at Wikipedia.org provides categories of processor architectures and links to further information about each. No information is provided on predefined macros.

Pre-defined C/C++ Compiler Macros at beefchunk.com has a brief list of processors and predefined macros. However, the list is incomplete and doesn't show differences among compilers and operating systems.

Comments

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