

Per SMX:	GF100 (Fermi)	GF104 (Fermi)	GK110 (Kepler)	GK104 (Kepler)
CUDA Compute Capability	2.0	2.0	3.5	3.0
Threads/Warp	32	32	32	32
Maximum Warps/SMX	48	48	64	64
Maximum Threads/SMX	1,536	1,536	2,048	2,048
Maximum Thread Blocks/SMX	8	8	16	16
32-bit Registers/SMX	32,768	32,768	65,536	65,536
Maximum Registers/Thread	63	63	255	63
Maximum Threads/Thread Block	1,024	1,024	1,024	1,024