

INSTRUCTION LIST :

ADDD F2 F0 F4
ADDD F8 F0 F2

Clock cycle : 0

TIMING TABLE

PC	INSTRUCTION	ISSUED	STARTED	FINISHED	Write CDB
0	ADDD F2 F0 F4	-	-	-	-
1	ADDD F8 F0 F2	-	-	-	-

Reservation Station

Time left	Tag	OP	Busy	valueJ	valueK	Qj	Qk
	Add0		False	0	0		
	Add1		False	0	0		
	Add2		False	0	0		
	Mult0		False	0	0		
	Mult1		False	0	0		

Load Station

Time left	Tag	Busy	Address
	Load0	False	
	Load1	False	
	Load2	False	
	Load3	False	
	Load4	False	
	Load5	False	

Register

Name	F0	F1	F2	F3	F4	F5	F6	F7	F8	F9
Qi										
Value	6.0	0.0	3.5	0.0	10.0	0.0	0.0	0.0	7.8	0.0

Clock cycle : 1
PC : 0

NEXT FP INSTRUCTION TO BE ISSUED: 'ADDD F2 F0 F4'

Instruction 'ADDD F2 F0 F4' ISSUED at clock 1 in 'Add0'

TIMING TABLE

PC	INSTRUCTION	ISSUED	STARTED	FINISHED	Write CDB
0	ADDD F2 F0 F4	1	-	-	-
1	ADDD F8 F0 F2	-	-	-	-

Reservation Station

Time left	Tag	OP	Busy	valueJ	valueK	Qj	Qk
1	Add0	ADDD	True	6.0	10.0		
	Add1		False	0	0		
	Add2		False	0	0		
	Mult0		False	0	0		
	Mult1		False	0	0		

Load Station

Time left	Tag	Busy	Address
	Load0	False	
	Load1	False	
	Load2	False	
	Load3	False	
	Load4	False	
	Load5	False	

Register										
Name	F0	F1	F2	F3	F4	F5	F6	F7	F8	F9
Qi			Add0							
Value	6.0	0.0	3.5	0.0	10.0	0.0	0.0	0.0	7.8	0.0

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Clock cycle : 2

PC : 1

NEXT FP INSTRUCTION TO BE ISSUED: 'ADDD F8 F0 F2'

Instruction 'ADDD F8 F0 F2' ISSUED at clock 2 in 'Add1'

Instruction 'ADDD F2 F0 F4' STARTED at clock 2 in 'Add0'

TIMING TABLE						
PC	INSTRUCTION	ISSUED	STARTED	FINISHED	Write CDB	
0	ADDD F2 F0 F4	1	2	-	-	
1	ADDD F8 F0 F2	2	-	-	-	

Reservation Station							
Time left	Tag	OP	Busy	valueJ	valueK	Qj	Qk
0	Add0	ADDD	True	6.0	10.0		
2	Add1	ADDD	True	6.0	3.5		Add0
	Add2		False	0	0		
	Mult0		False	0	0		
	Mult1		False	0	0		

Load Station			
Time left	Tag	Busy	Address
	Load0	False	
	Load1	False	
	Load2	False	
	Load3	False	
	Load4	False	
	Load5	False	

Register										
Name	F0	F1	F2	F3	F4	F5	F6	F7	F8	F9
Qi			Add0						Add1	
Value	6.0	0.0	3.5	0.0	10.0	0.0	0.0	0.0	7.8	0.0

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Clock cycle : 3

PC : 2

Instruction 'ADDD F2 F0 F4' FINISHED at clock 3 in 'Add0'

TIMING TABLE						
PC	INSTRUCTION	ISSUED	STARTED	FINISHED	Write CDB	
0	ADDD F2 F0 F4	1	2	3	-	
1	ADDD F8 F0 F2	2	-	-	-	

Reservation Station							
Time left	Tag	OP	Busy	valueJ	valueK	Qj	Qk
0	Add0	ADDD	True	6.0	10.0		
2	Add1	ADDD	True	6.0	3.5		Add0
	Add2		False	0	0		
	Mult0		False	0	0		
	Mult1		False	0	0		

Load Station			
Time left	Tag	Busy	Address

Load0	False
Load1	False
Load2	False
Load3	False
Load4	False
Load5	False

Register

Name	F0	F1	F2	F3	F4	F5	F6	F7	F8	F9
Qi			Add0						Add1	
Value	6.0	0.0	3.5	0.0	10.0	0.0	0.0	0.0	7.8	0.0

Clock cycle : 4
PC : 2

Instruction 'ADDD F2 F0 F4' BROADCASTED in CDB at clock 4 with value '16.0'

TIMING TABLE

PC	INSTRUCTION	ISSUED	STARTED	FINISHED	Write CDB
0	ADDD F2 F0 F4	1	2	3	4
1	ADDD F8 F0 F2	2	-	-	-

Reservation Station

Time left	Tag	OP	Busy	valueJ	valueK	Qj	Qk
1	Add0		False	0	0		
	Add1	ADDD	True	6.0	16.0		
	Add2		False	0	0		
	Mult0		False	0	0		
	Mult1		False	0	0		

Load Station

Time left	Tag	Busy	Address
	Load0	False	
	Load1	False	
	Load2	False	
	Load3	False	
	Load4	False	
	Load5	False	

Register

Name	F0	F1	F2	F3	F4	F5	F6	F7	F8	F9
Qi									Add1	
Value	6.0	0.0	16.0	0.0	10.0	0.0	0.0	0.0	7.8	0.0

Clock cycle : 5
PC : 2

Instruction 'ADDD F8 F0 F2' STARTED at clock 5 in 'Add1'

TIMING TABLE

PC	INSTRUCTION	ISSUED	STARTED	FINISHED	Write CDB
0	ADDD F2 F0 F4	1	2	3	4
1	ADDD F8 F0 F2	2	5	-	-

Reservation Station

Time left	Tag	OP	Busy	valueJ	valueK	Qj	Qk
0	Add0		False	0	0		
	Add1	ADDD	True	6.0	16.0		
	Add2		False	0	0		
	Mult0		False	0	0		
	Mult1		False	0	0		

Load Station

Time left	Tag	Busy	Address
	Load0	False	
	Load1	False	
	Load2	False	
	Load3	False	
	Load4	False	
	Load5	False	

Register

Name	F0	F1	F2	F3	F4	F5	F6	F7	F8	F9
Qi									Add1	
Value	6.0	0.0	16.0	0.0	10.0	0.0	0.0	0.0	7.8	0.0

Clock cycle : 6
PC : 2

Instruction 'ADD F8 F0 F2' FINISHED at clock 6 in 'Add1'

TIMING TABLE

PC	INSTRUCTION	ISSUED	STARTED	FINISHED	Write CDB
0	ADD F2 F0 F4	1	2	3	4
1	ADD F8 F0 F2	2	5	6	-

Reservation Station

Time left	Tag	OP	Busy	valueJ	valueK	Qj	Qk
	Add0		False	0	0		
0	Add1	ADD	True	6.0	16.0		
	Add2		False	0	0		
	Mult0		False	0	0		
	Mult1		False	0	0		

Load Station

Time left	Tag	Busy	Address
	Load0	False	
	Load1	False	
	Load2	False	
	Load3	False	
	Load4	False	
	Load5	False	

Register

Name	F0	F1	F2	F3	F4	F5	F6	F7	F8	F9
Qi									Add1	
Value	6.0	0.0	16.0	0.0	10.0	0.0	0.0	0.0	7.8	0.0

Clock cycle : 7
PC : 2

Instruction 'ADD F8 F0 F2' BROADCASTED in CDB at clock 7 with value '22.0'

TIMING TABLE

PC	INSTRUCTION	ISSUED	STARTED	FINISHED	Write CDB
0	ADD F2 F0 F4	1	2	3	4
1	ADD F8 F0 F2	2	5	6	7

Reservation Station

Time left	Tag	OP	Busy	valueJ	valueK	Qj	Qk
	Add0		False	0	0		
	Add1		False	0	0		
	Add2		False	0	0		
	Mult0		False	0	0		
	Mult1		False	0	0		

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Load Station  
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Time left	Tag	Busy	Address
	Load0	False	
	Load1	False	
	Load2	False	
	Load3	False	
	Load4	False	
	Load5	False	

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Register  
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Name	F0	F1	F2	F3	F4	F5	F6	F7	F8	F9
Qi										
Value	6.0	0.0	16.0	0.0	10.0	0.0	0.0	0.0	22.0	0.0