-	d	+		
	H	Low	na	N
1				
- 3		la	nt	vh

Analog IC Design - Homework#4

TP24222



Roblem 1:

- a) For proper current mirror operation, the transistors must be in saturation. So Vos > Vos VT.

Vina = Vov+VT

Rol3 =
$$ro_3$$
 (1 + $gma+gmb3$) 0) Rol3 = ro_3

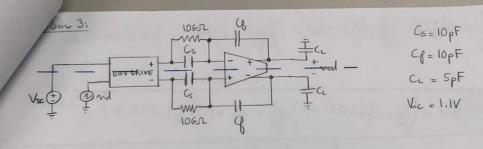
c) Rout =
$$\frac{1}{1 \text{ Iout}} \Rightarrow \lambda = \frac{1}{\text{Ruly Iout}}$$

$$\lambda_{\text{WSCCM}} = \frac{g_{\text{dSH}}}{\frac{g_{\text{mH}}}{I_{\text{D}}} + \frac{g_{\text{mbH}}}{I_{\text{D}}} + \frac{g_{\text{dSH}}}{I_{\text{D}}} \lambda} \lambda \qquad \lambda = \frac{1}{I_{\text{D}} I_{\text{D}}}$$

$$\lambda_{\text{WSCCM}} = \frac{1}{R_{\text{D}} I_{\text{D}}} = \frac{1}{\left(r_{\text{OH}}\left(1 + \left(g_{\text{mH}} + g_{\text{mbH}}\right) r_{\text{O3}}\right) + r_{\text{O3}}\right) I_{\text{D}}}$$

$$= \frac{1}{\left(r_{\text{OH}}\left(1 + g_{\text{mH}} r_{\text{O3}} + g_{\text{mbH}} r_{\text{O3}}\right) + r_{\text{O3}}\right) I_{\text{D}}}$$

$$= \frac{gd_{SH}/I_{D}}{\frac{1}{I_{D}r_{03}} + \frac{gm_{H}}{I_{D}} + \frac{1}{I_{D}r_{04}}} \frac{1}{I_{D}r_{03}}$$



$$(vid-nx)$$
 s(s + $(vid-vx)$ = $(vx - vod)$ s(f

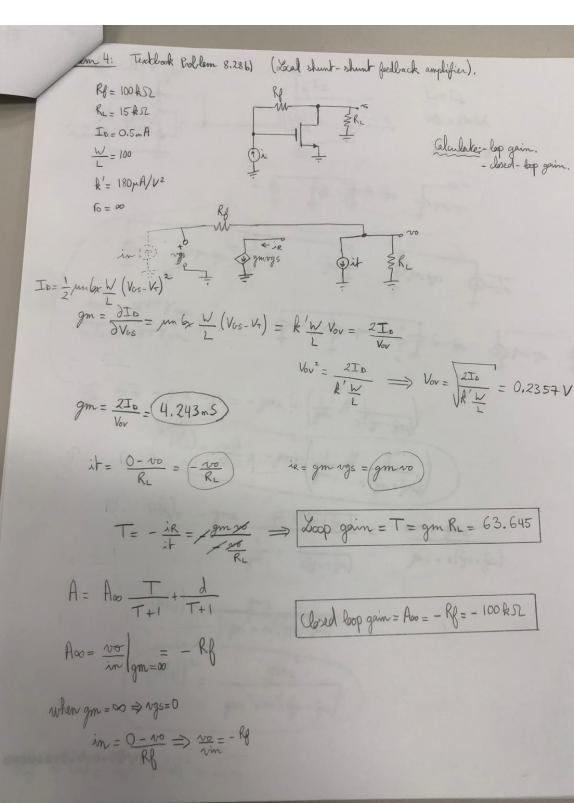
$$vx\left(scs + \frac{1}{10GR} + scg\right) = vid\left(scs + \frac{1}{10GR}\right) + vodscg$$

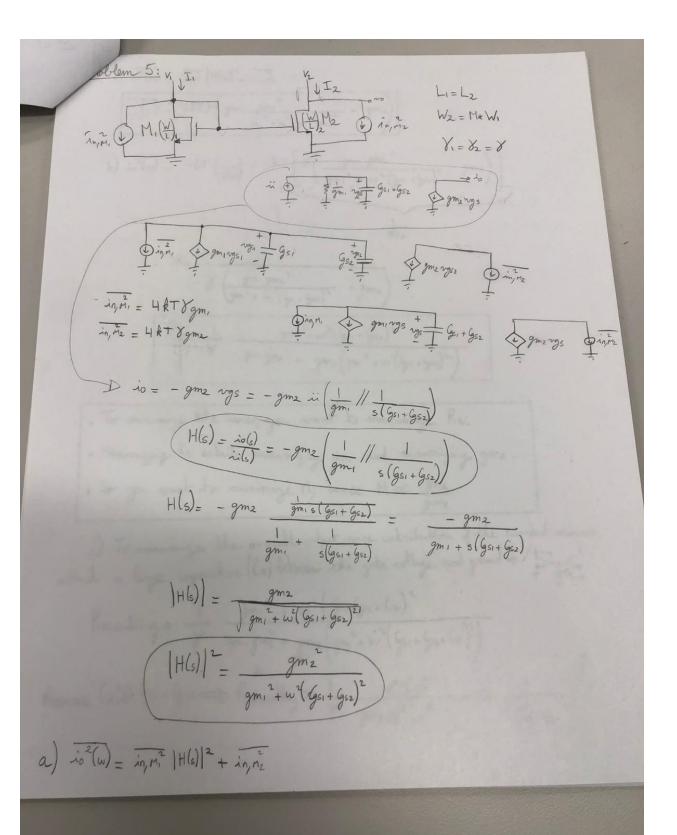
$$N\pi = \frac{vid(s(s + \frac{1}{106\pi}) + vods(f)}{s(s + \frac{1}{106\pi}) + s(f)} = \frac{vid((06\pi)s(s + 1) + vods(f)(106\pi))}{(106\pi)(s(s + s(f) + 1)}$$

$$v_{x} = vod \left(1 + \frac{s(s)}{s(f)}\right)$$

$$\frac{vod}{vod} = \frac{(106\pi) s(s+1)}{1 + 106\pi s(G+C_s) + C_s} + \frac{C_s}{C_s} + \frac{C_s}{C_s}$$

$$\frac{vod}{vod} = \frac{(106\pi) s(s+1)}{1 + 106\pi s(G+C_s) + C_s} + \frac{C_s}{C_s} + \frac{C_s}{C_s}$$





a)
$$io^{2}(\omega) = in_{1}m_{1} |H(s)|^{2} + in_{1}m_{2}$$

$$\int_{so^{2}(\omega)}^{2} = 4kTY \left(gm_{1} \frac{gm_{2}^{2}}{gm_{1}^{2} + \omega^{2}(gs_{1} + gs_{2})^{2}} + gm_{2}\right)$$
b) $io^{2}(\omega) = 4kT \left(\frac{1}{R_{N}}\right) = 4kT \left(Y \left(\frac{gm_{1} gm_{2}^{2}}{gm_{1}^{2} + \omega^{2}(gs_{1} + gs_{2})^{2}} + gm_{2}\right)\right)$

$$R_{N} = \frac{1}{Y \left(\frac{gm_{1} gm_{2}^{2}}{gm_{1}^{2} + \omega^{2}(gs_{1} + gs_{2})^{2}} + gm_{2}\right)}$$

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- . To minimize the noise, you want to maximize RN.
- · Maximizing RN entails maximizing gm, and minimizing gmz.
- . So you want to minimize M, since M or gmz gm;
- c) To minimize the overall output noise contribution of the current minor, attach a large capacitor ((x) between the gate vollage and ground > First

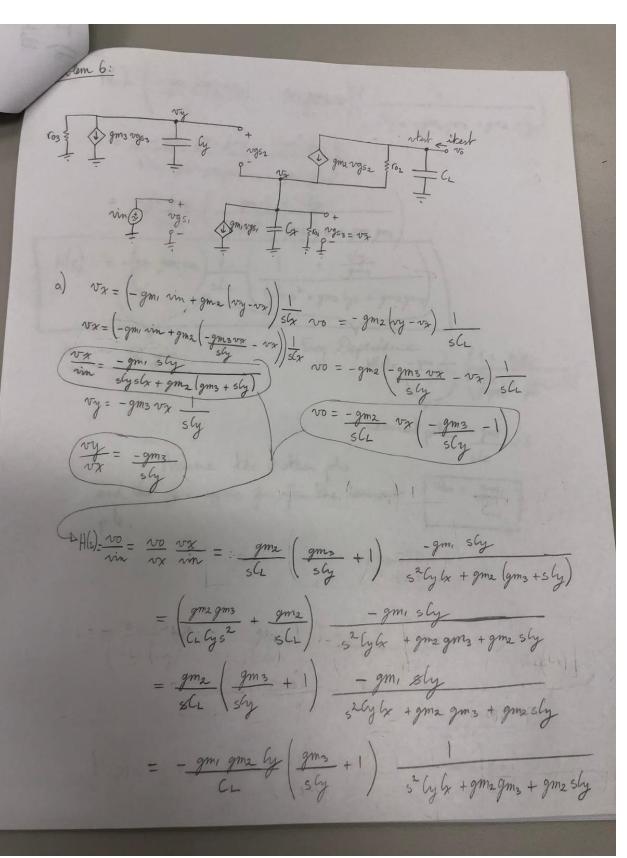
River ling =
$$\frac{1}{\gamma} \frac{gm_1^2 + \omega^2 (g_{S1} + g_{S2} + C_x)^2}{gm_1 gm_2^2 + gm_2 (gm_1^2 + \omega^2 (g_{S1} + g_{S2} + C_x)^2)}$$

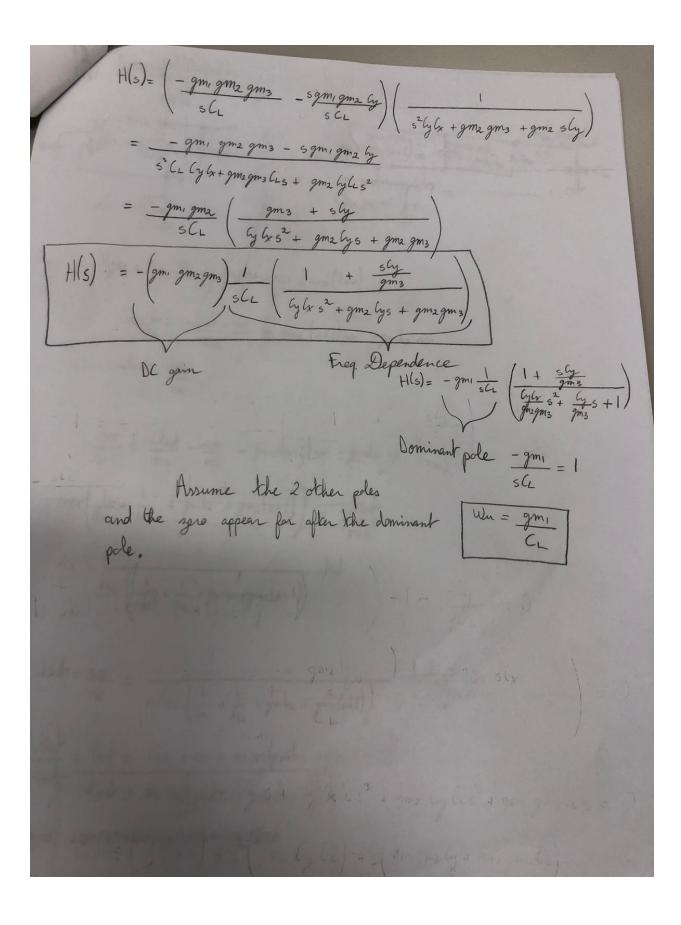
d) The noise of this mirror would be much greater than that of a resistor that provides the same output resistance as the mirror.

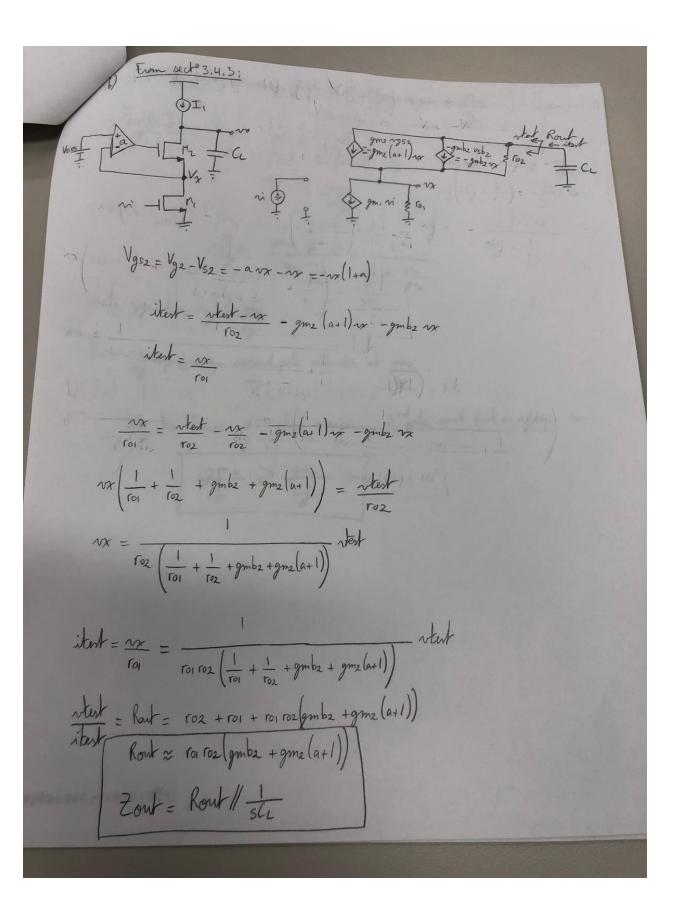
$$\Rightarrow \frac{1}{1000} = \frac{4kT}{RN!}$$

$$\frac{RN \ll r_0, so injré \ll injré niver}{r_0}$$

However, replacing the mirror with such a resistor would completely annihilate the headroom at the output of the circuit, since the value of ro is usually on the order of hundreds of kr. So the voltage drop accross ro would probably be the same as the supplied voltage.







Unity gain frequency of the feedback loop is we = 3m3

Teedback circuit => my = - gm3 1

1 + gm3(0) 5 hy

my = - gm3

s Gy

 $\frac{vx}{vy} = \frac{gm2\frac{1}{56x}}{1 + gm2\frac{1}{56x}} = \frac{1}{1 + \frac{56x}{gm2}}$

Unity gain frequency = gm3 Cy

From ix expression, second pole activates at gmz,

So to have 45° place margin, 2 rdple must kick in after un.

gm2 > gm3
Cy
Cy