

Figure 1: NMOS schematic

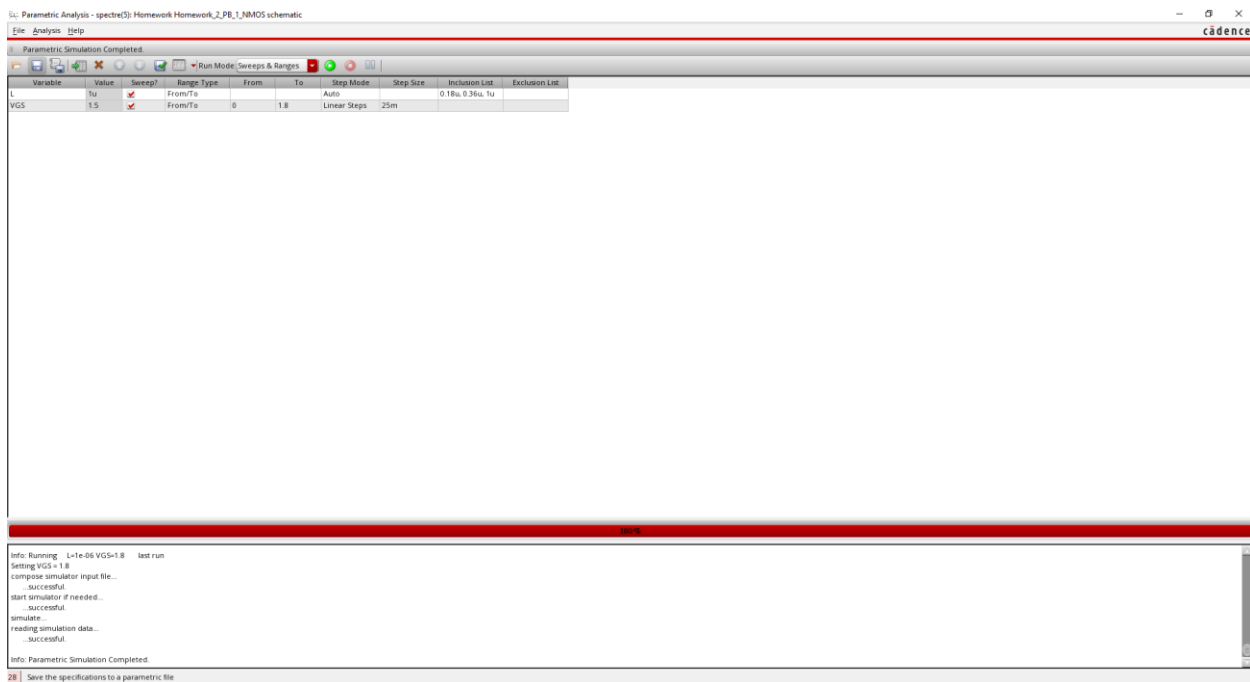


Figure 2: NMOS simulation window

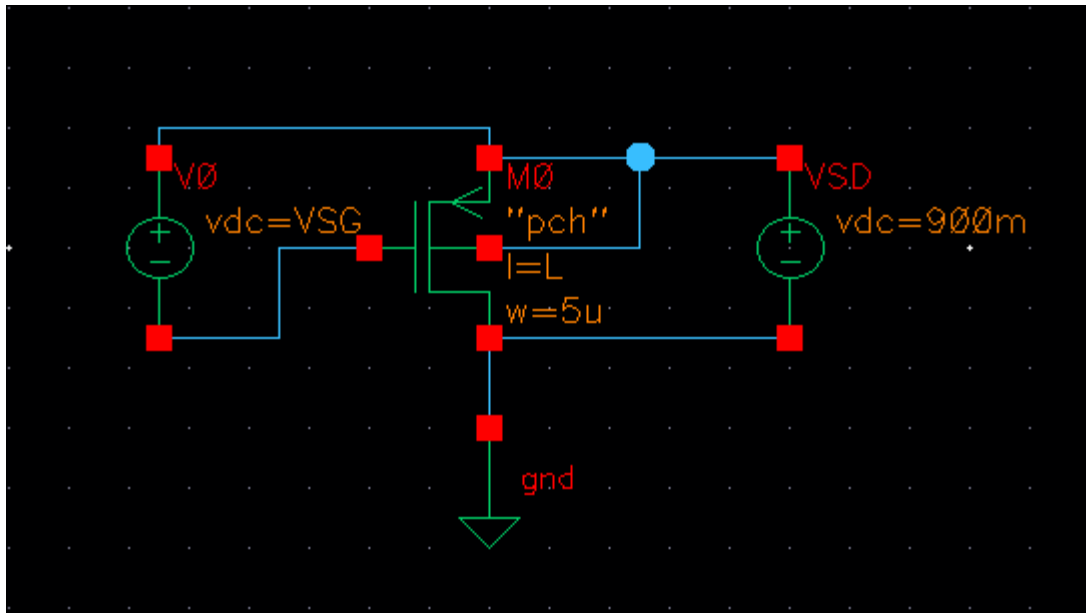


Figure 3: PMOS schematic

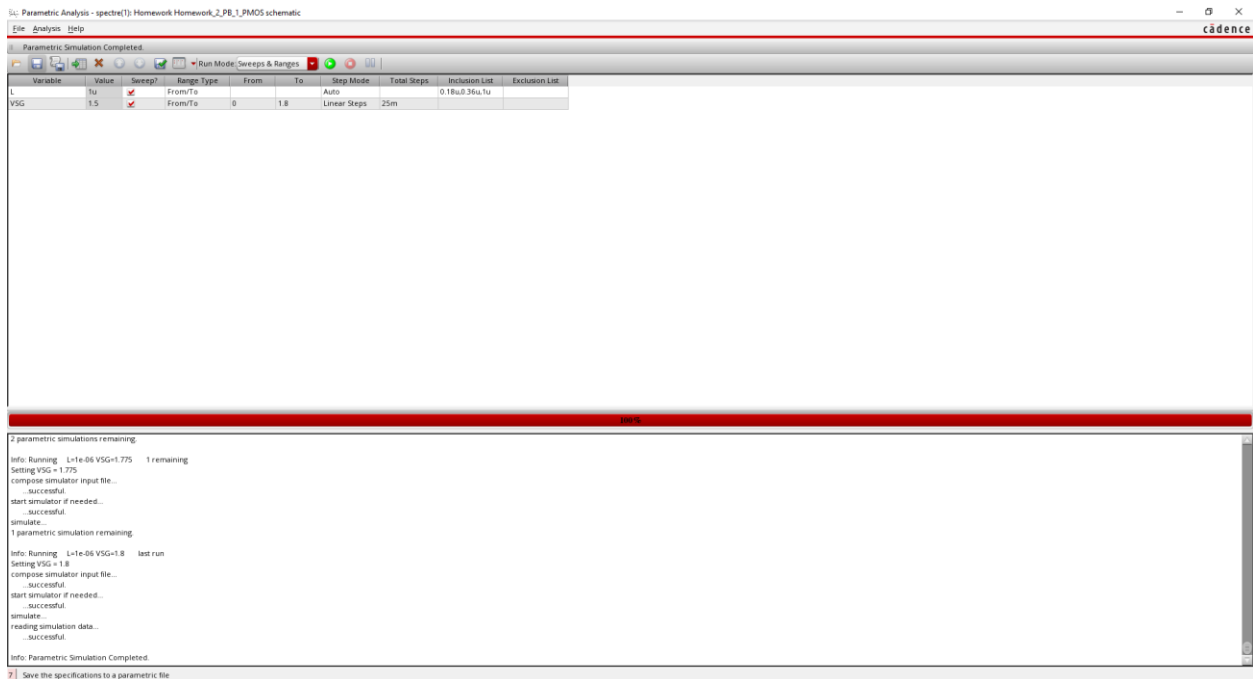


Figure 4: PMOS simulation window