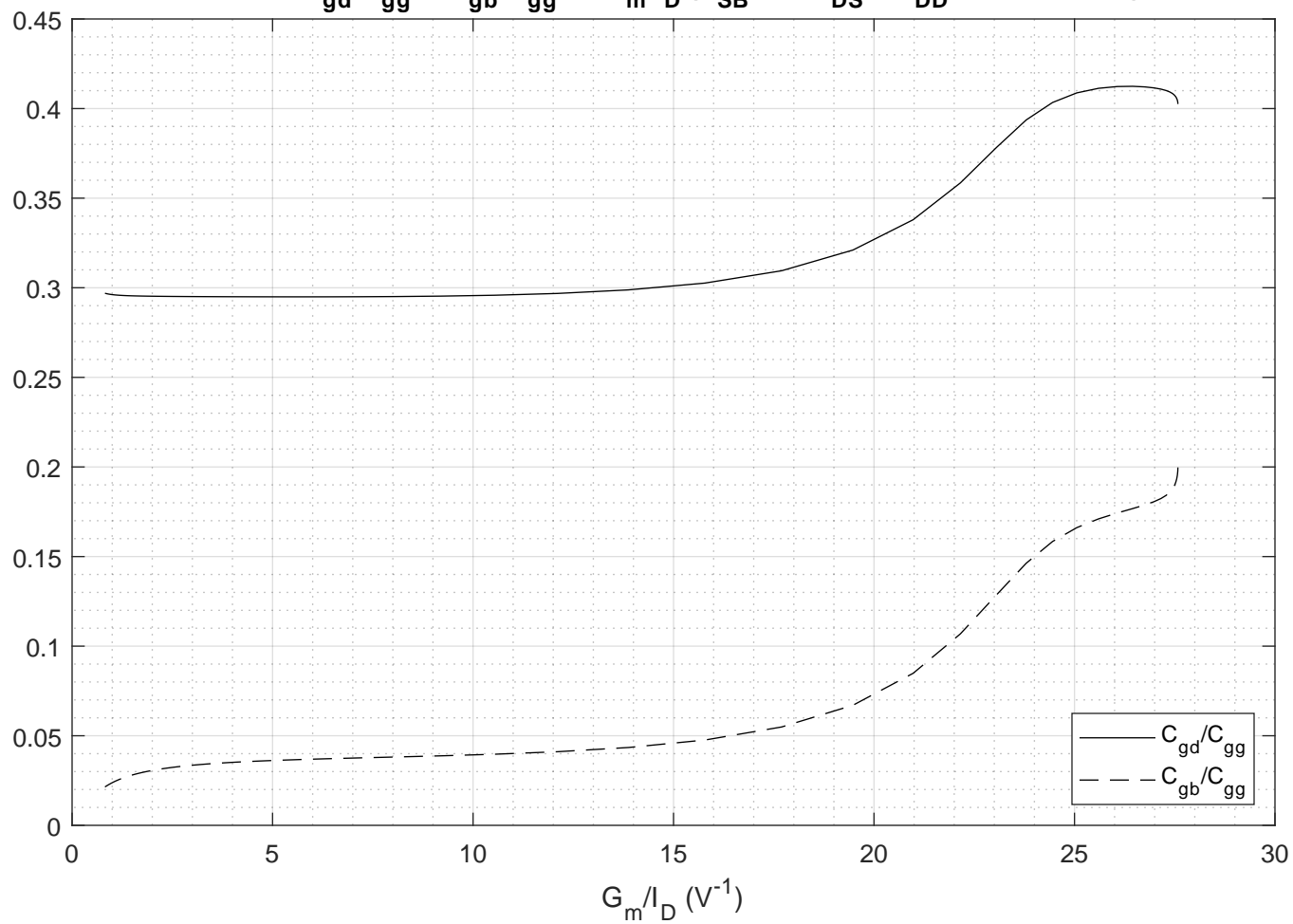
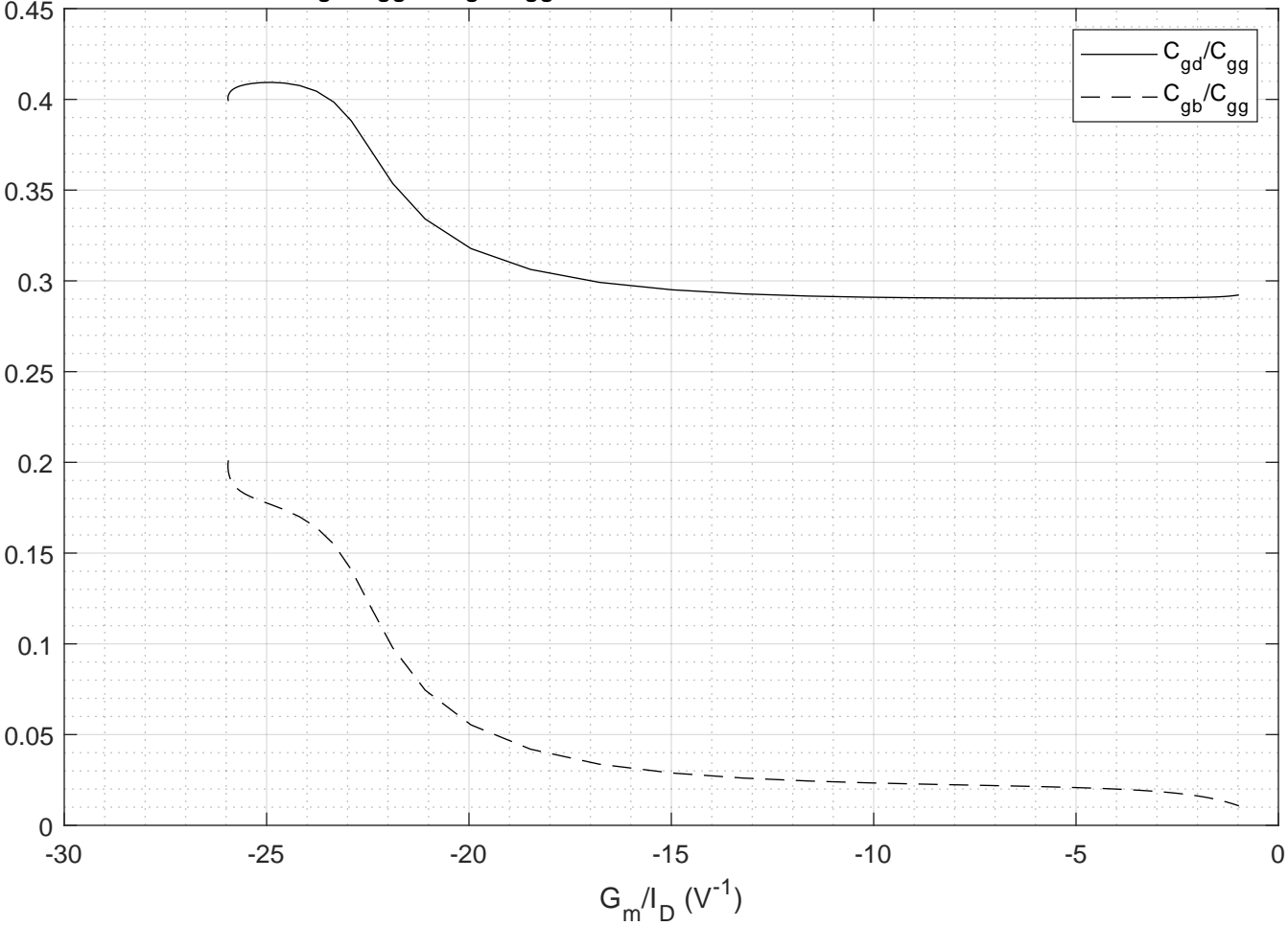


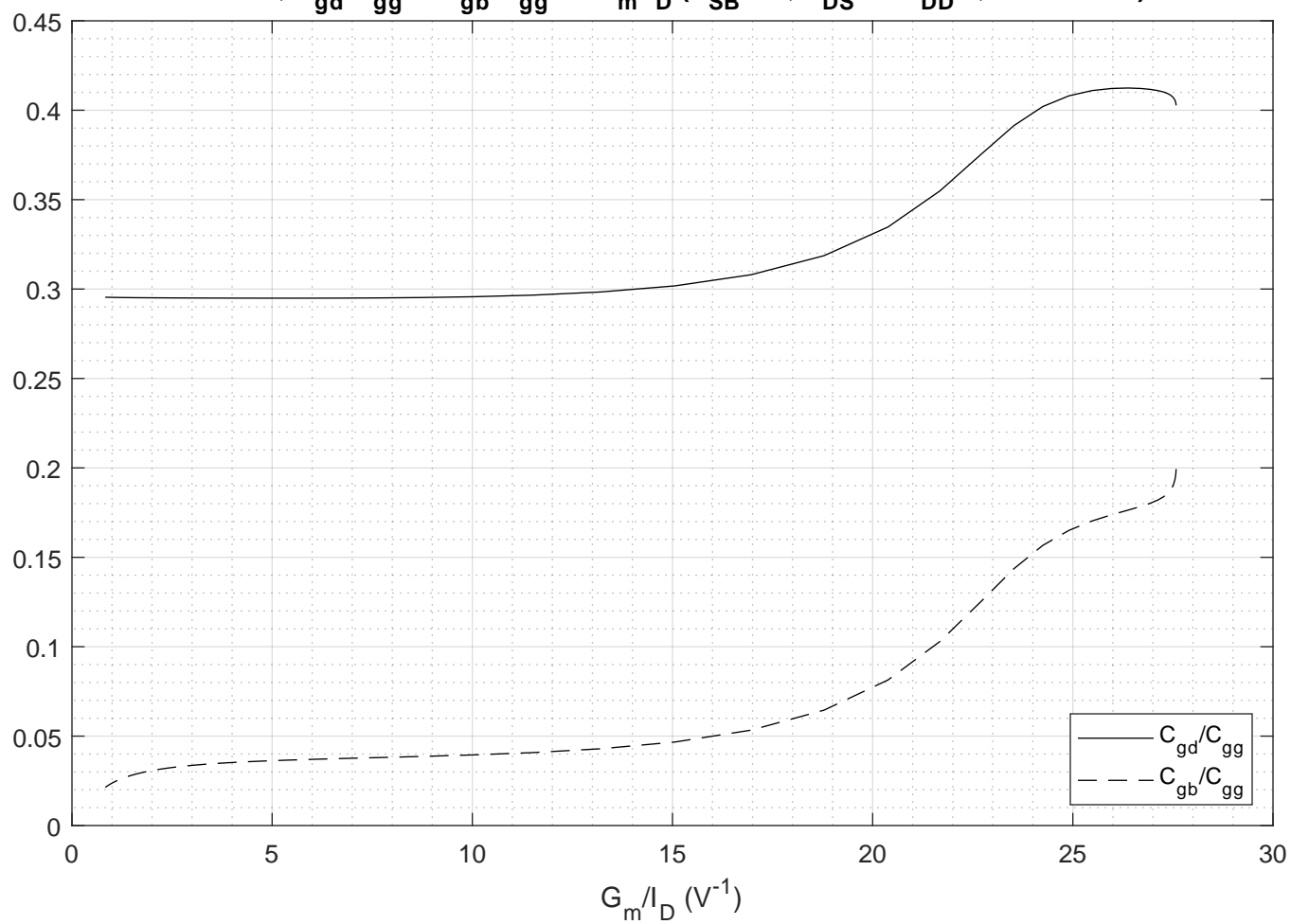
NMOS, C_{gd}/C_{gg} & C_{gb}/C_{gg} vs G_m/I_D ($V_{SB} = 0$, $V_{DS} = V_{DD}/2$, $L = 0.18\mu m$)



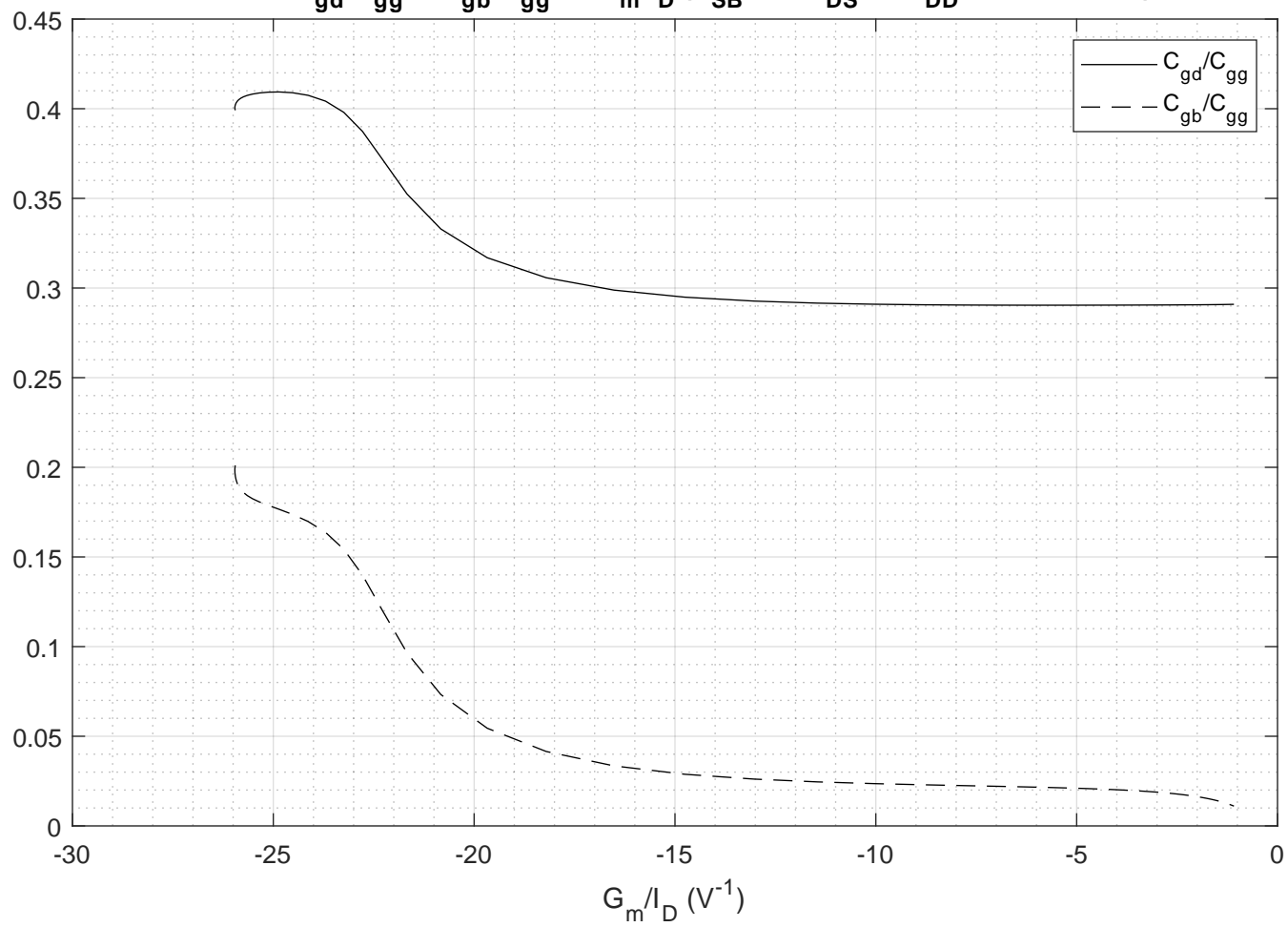
PMOS, C_{gd}/C_{gg} & C_{gb}/C_{gg} vs G_m/I_D ($V_{SB} = 0$, $V_{DS} = V_{DD}/2$, $L = 0.18\mu m$)

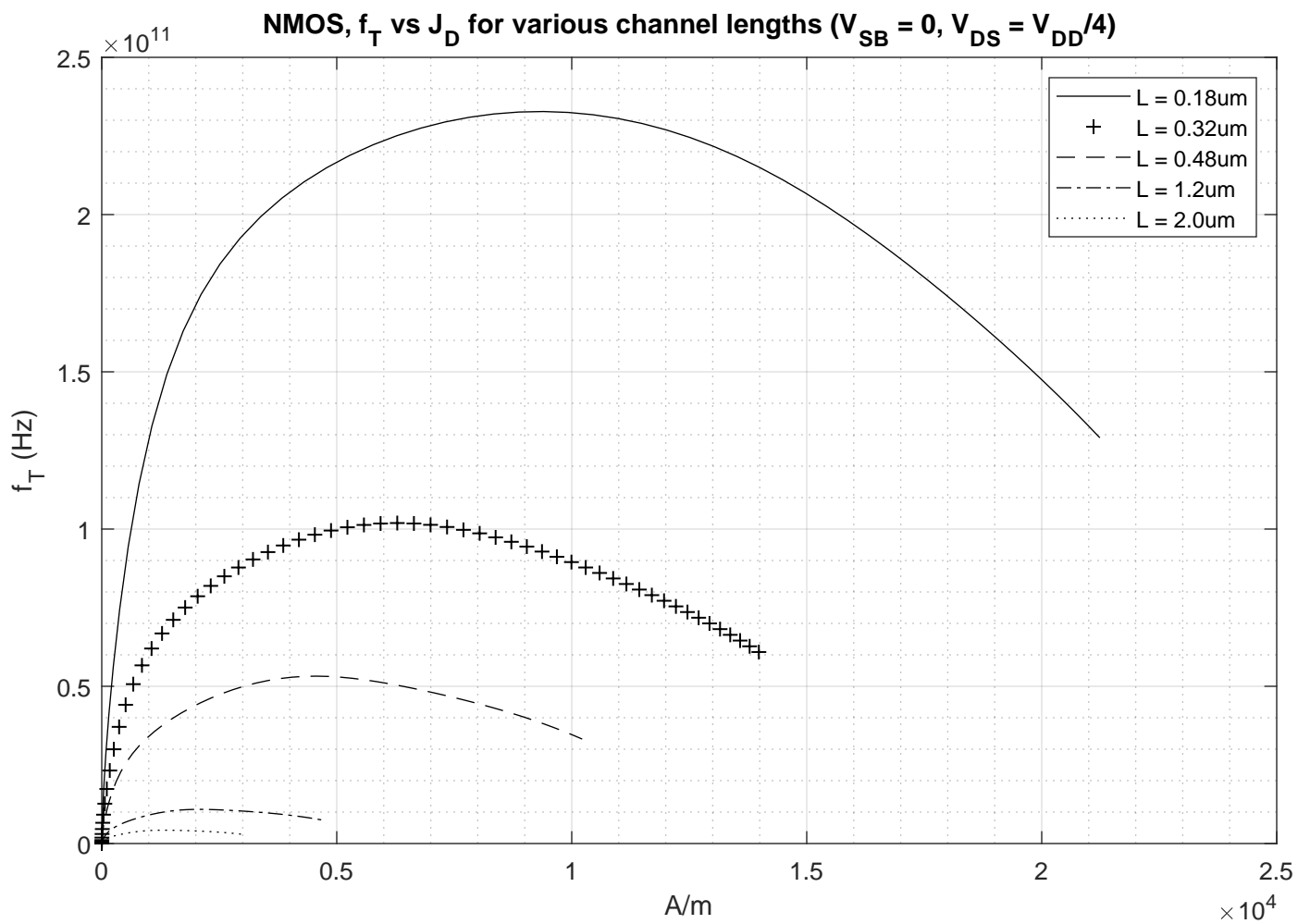


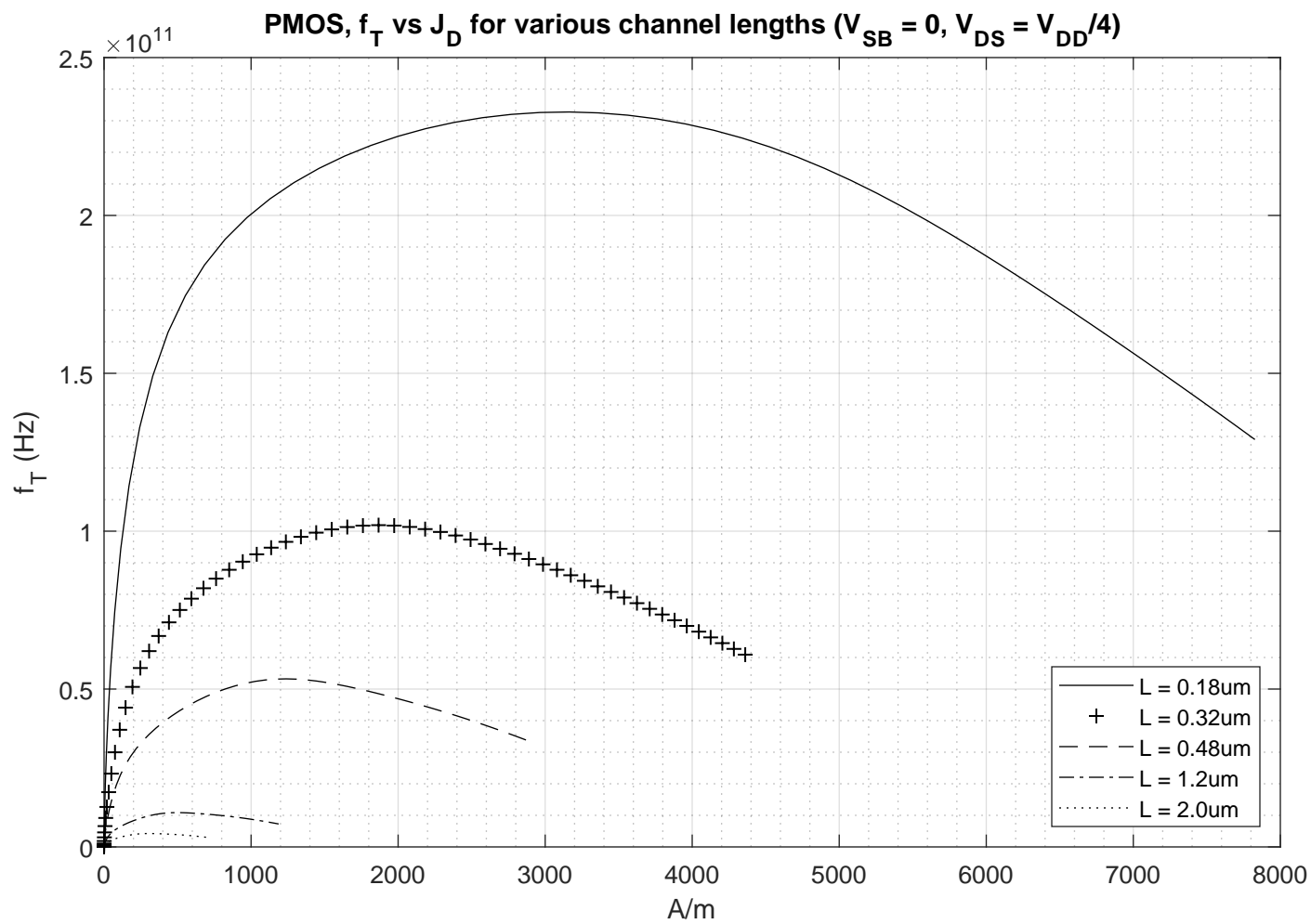
NMOS, C_{gd}/C_{gg} & C_{gb}/C_{gg} vs G_m/I_D ($V_{SB} = 0$, $V_{DS} = 3V_{DD}/4$, $L = 0.18\mu m$)



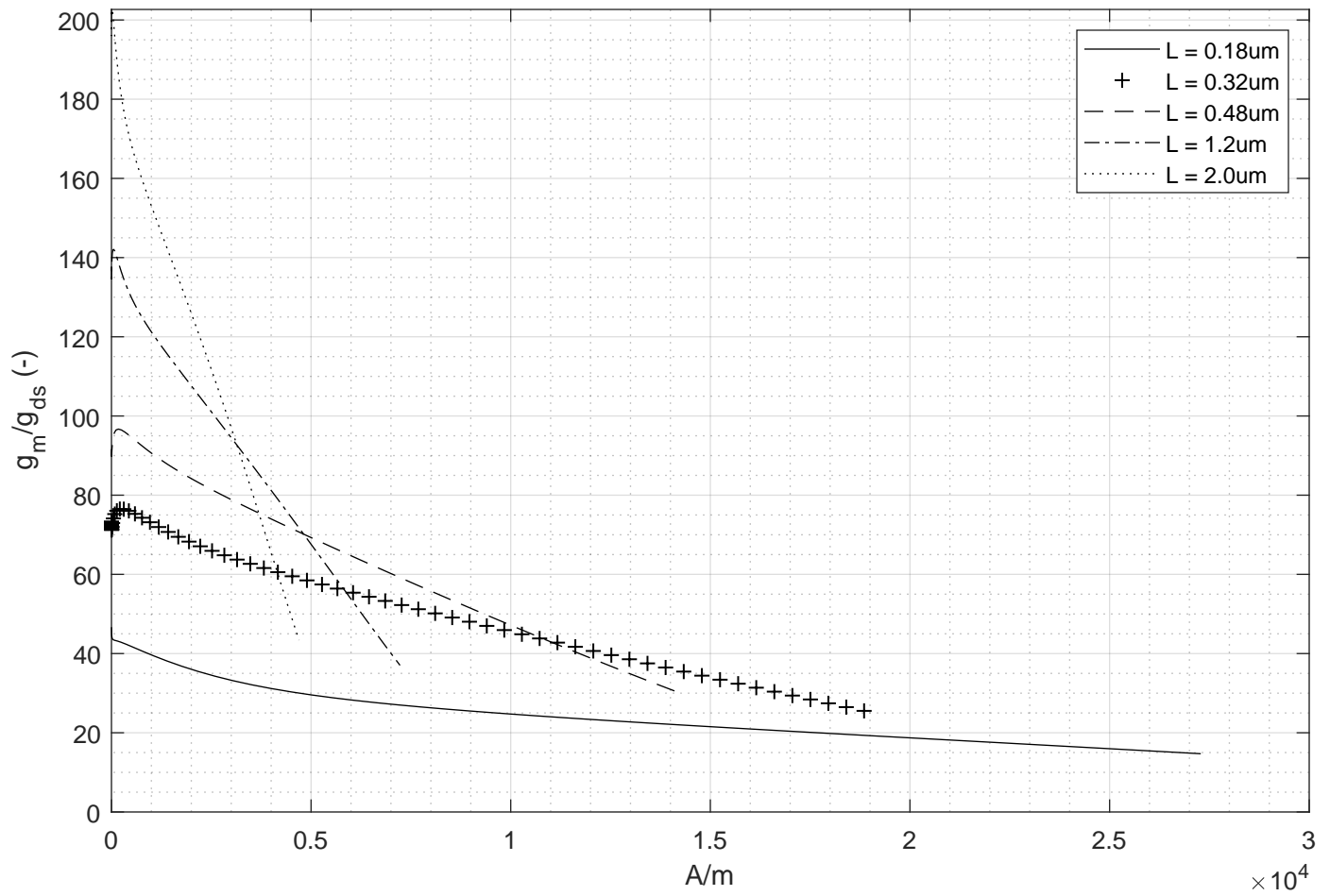
PMOS, C_{gd}/C_{gg} & C_{gb}/C_{gg} vs G_m/I_D ($V_{SB} = 0$, $V_{DS} = 3V_{DD}/4$, $L = 0.18\mu m$)



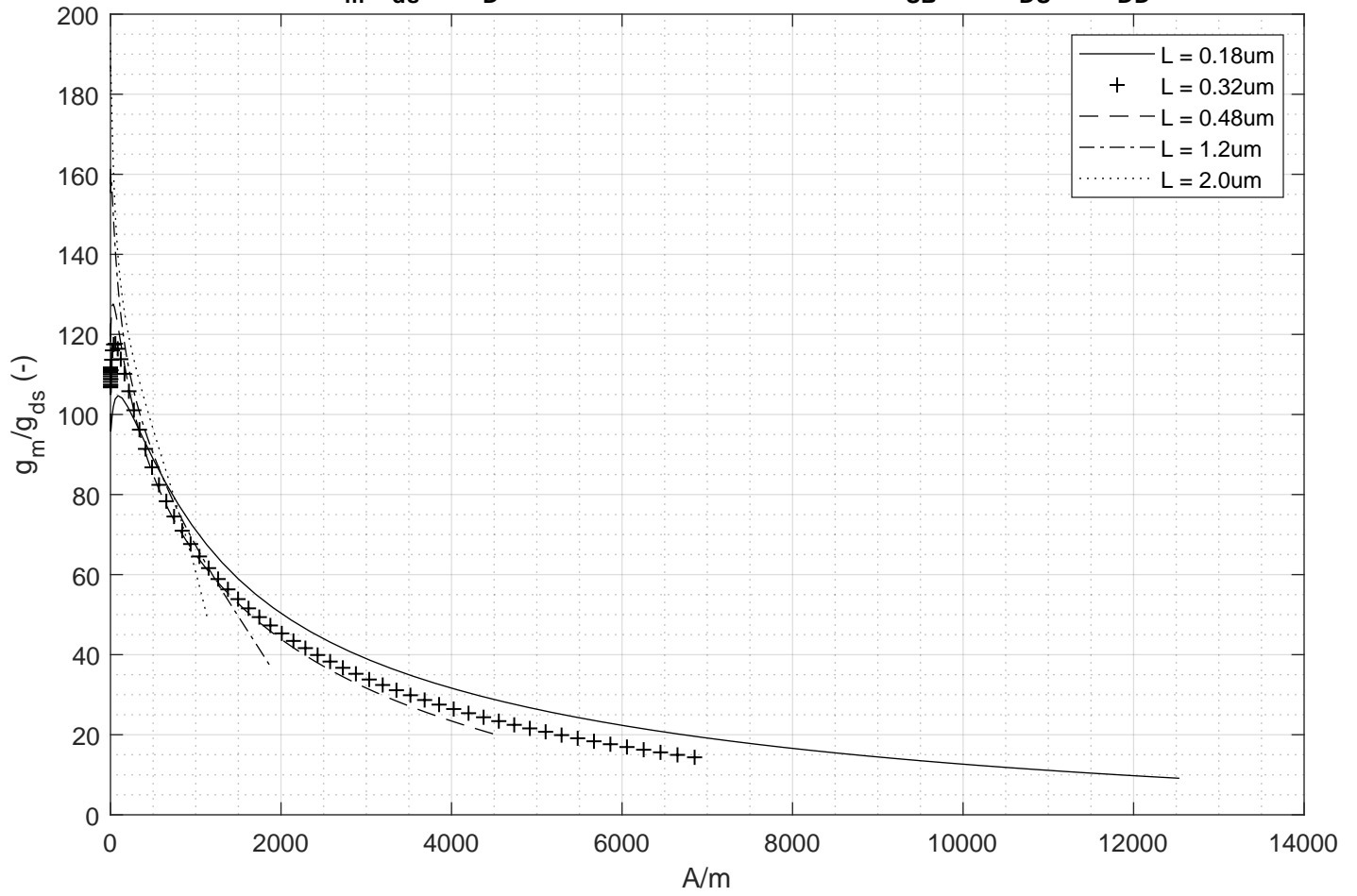




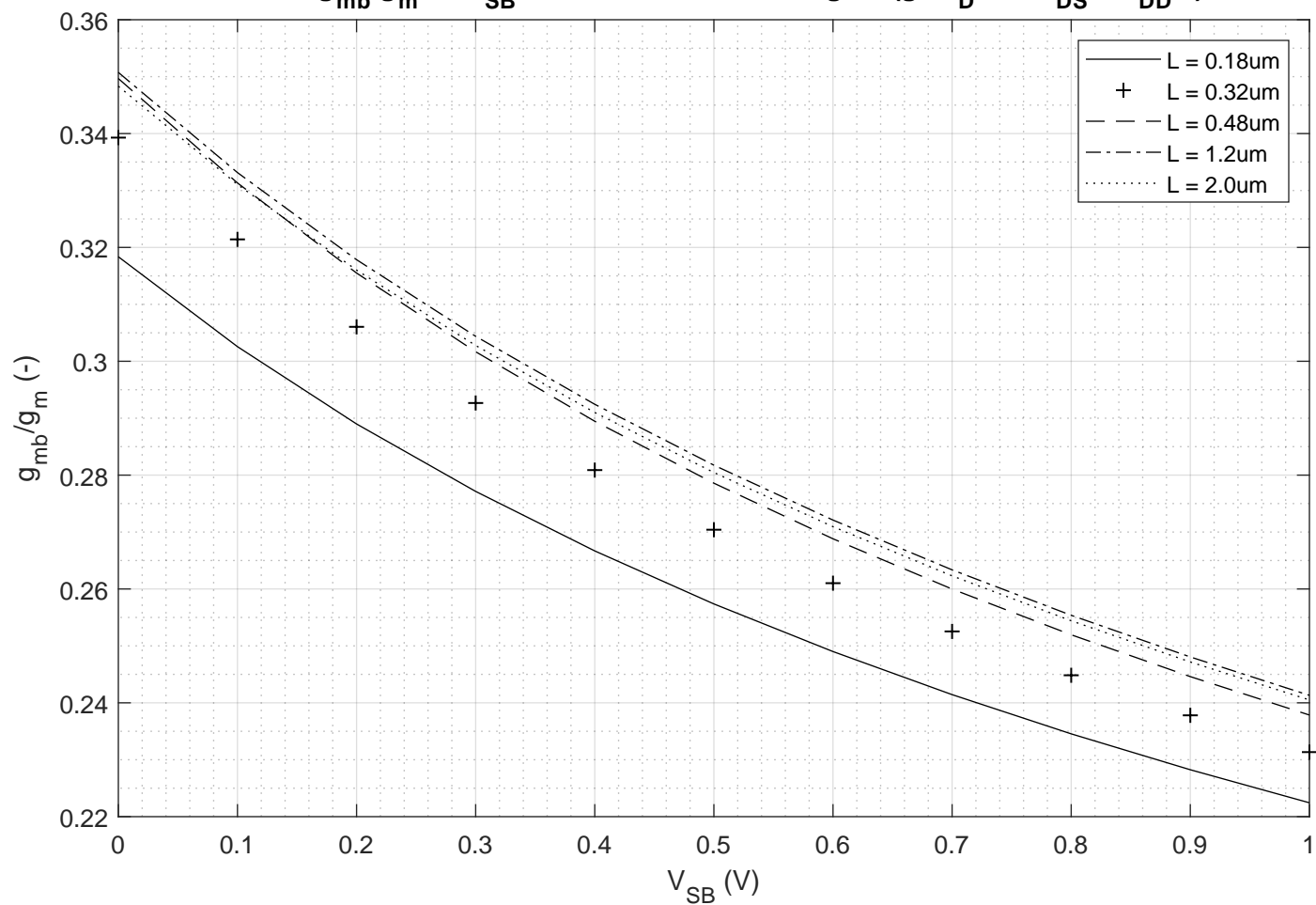
NMOS, g_m/g_{ds} vs J_D for various channel lengths ($V_{SB} = 0$, $V_{DS} = 3V_{DD}/4$)



PMOS, g_m/g_{ds} vs J_D for various channel lengths ($V_{SB} = 0$, $V_{DS} = 3V_{DD}/4$)



NMOS, g_{mb}/g_m vs V_{SB} for various channel lengths ($gm/I_D = 5$, $V_{DS} = V_{DD}/2$)



PMOS, g_{mb}/g_m vs V_{SB} for various channel lengths ($g_m/I_D = 5$, $V_{DS} = V_{DD}/2$)

