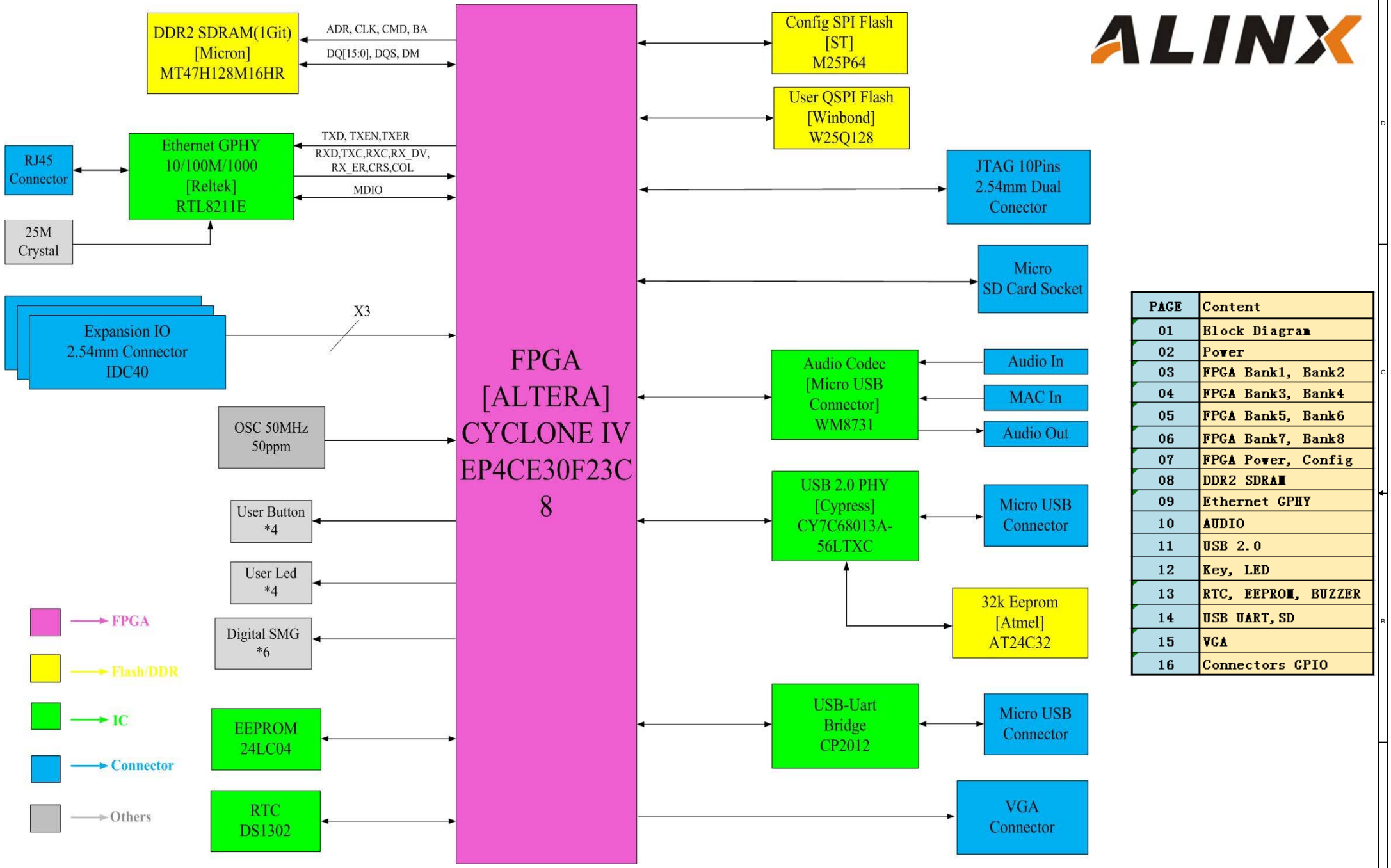

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15	VGA
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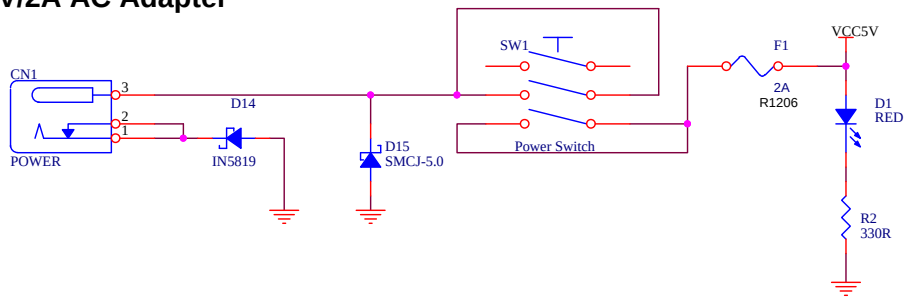
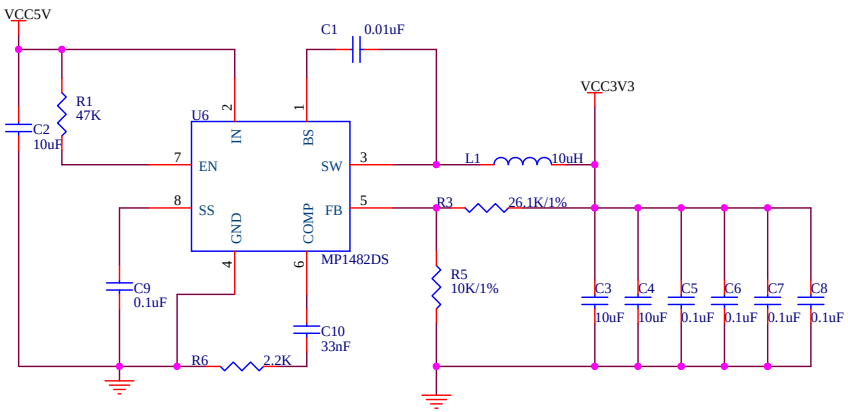
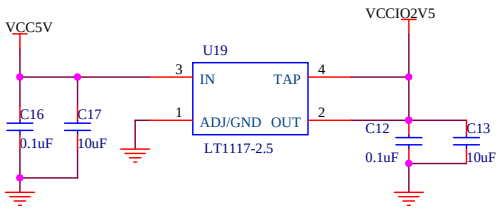
5V/2A AC Adapter



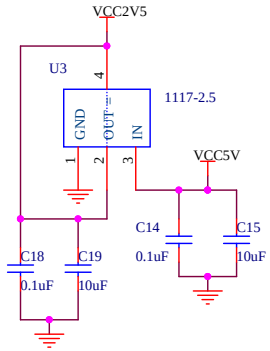
+3.3V POWER



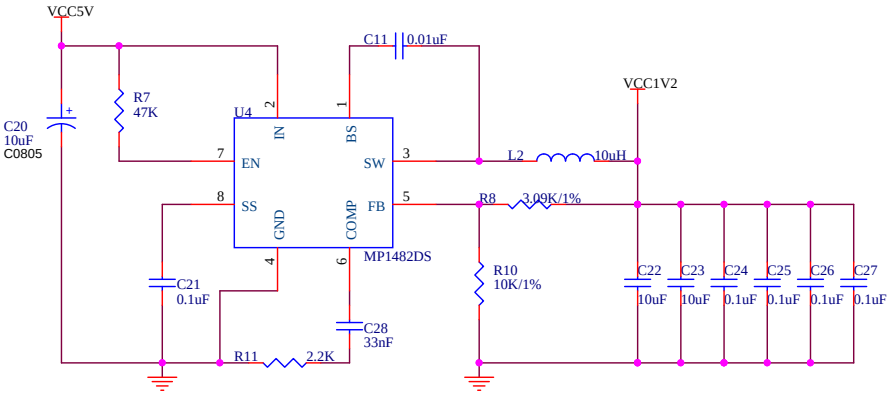
VCCIO POWER



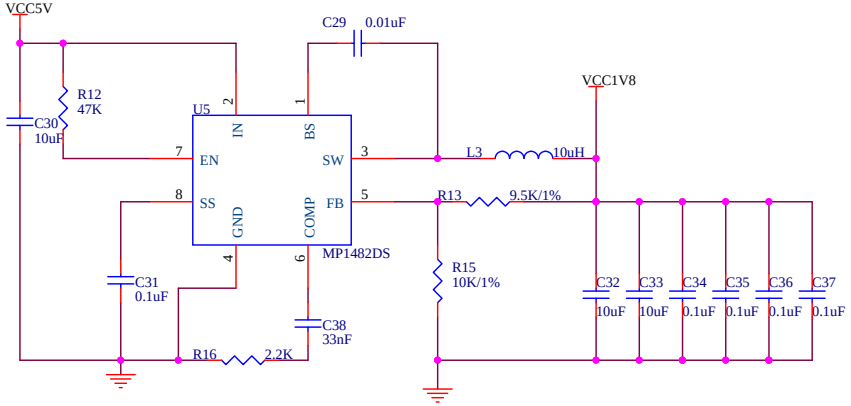
+2.5V POWER

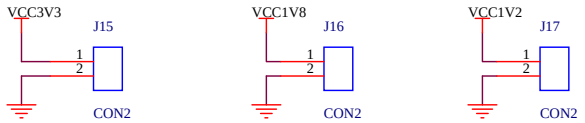


+1.2V POWER



+1.8V POWER





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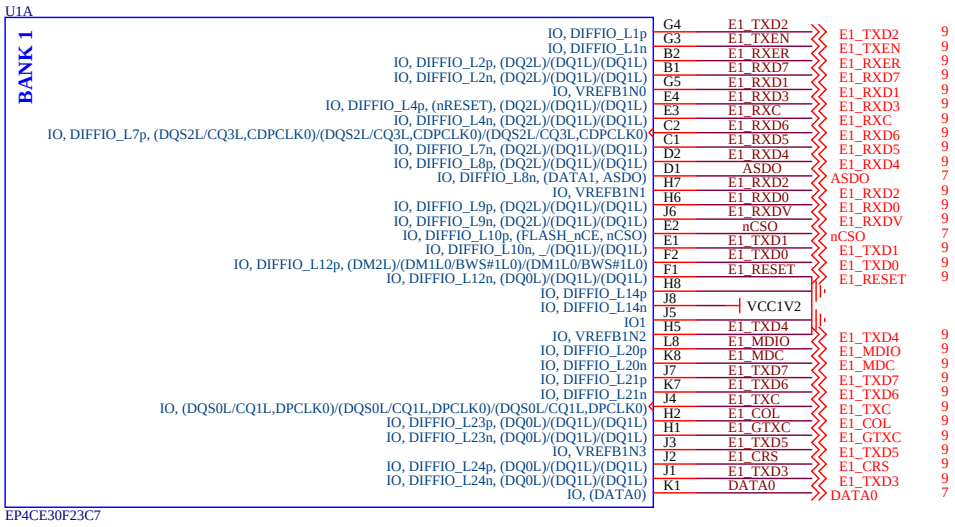

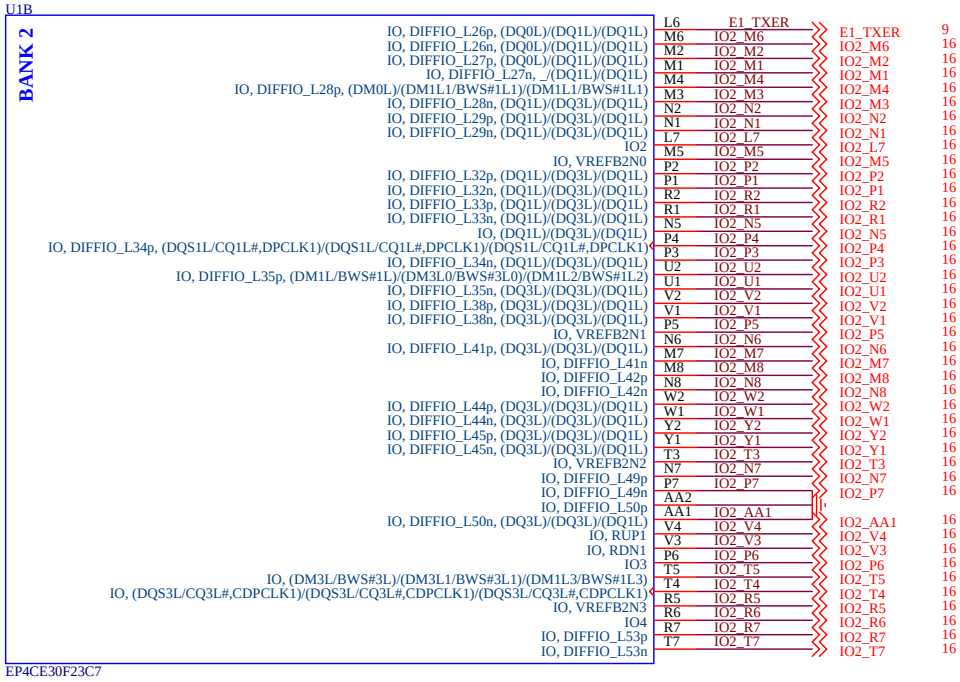

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Title: 03_FPGA

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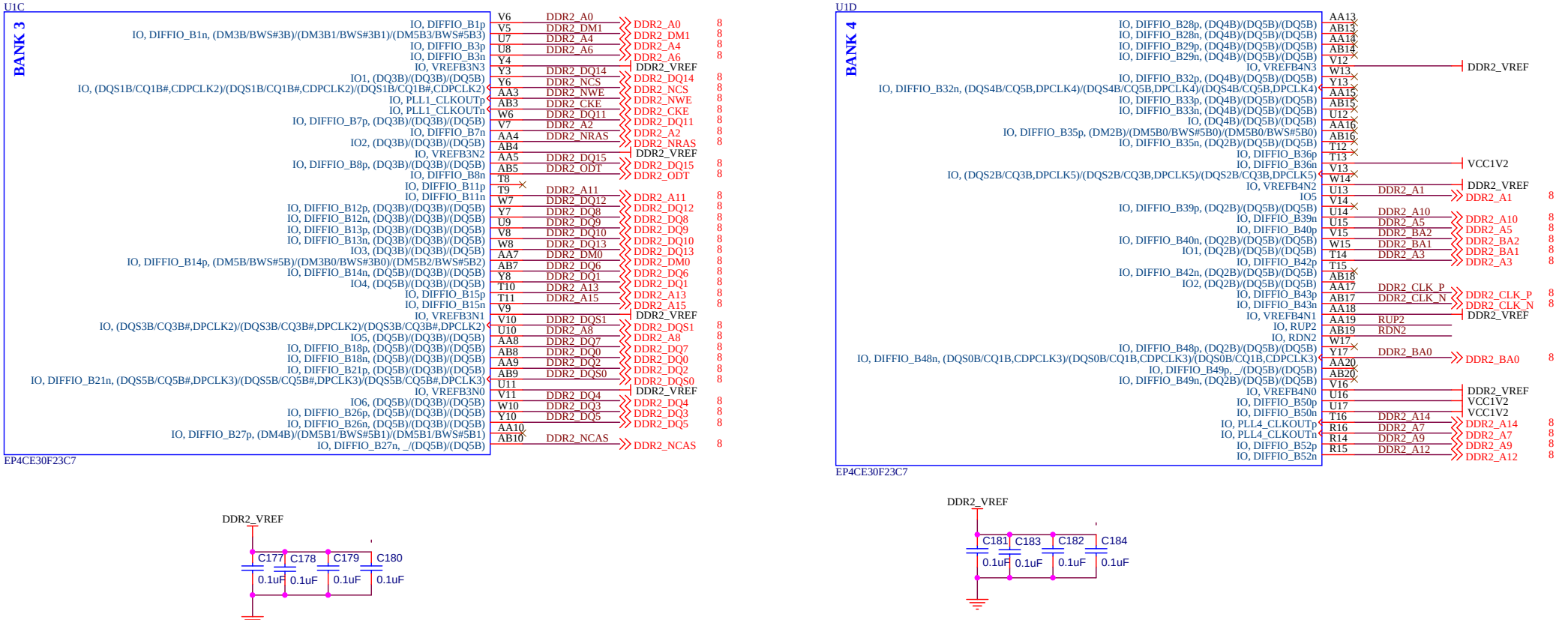

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Title	04_FPGA	
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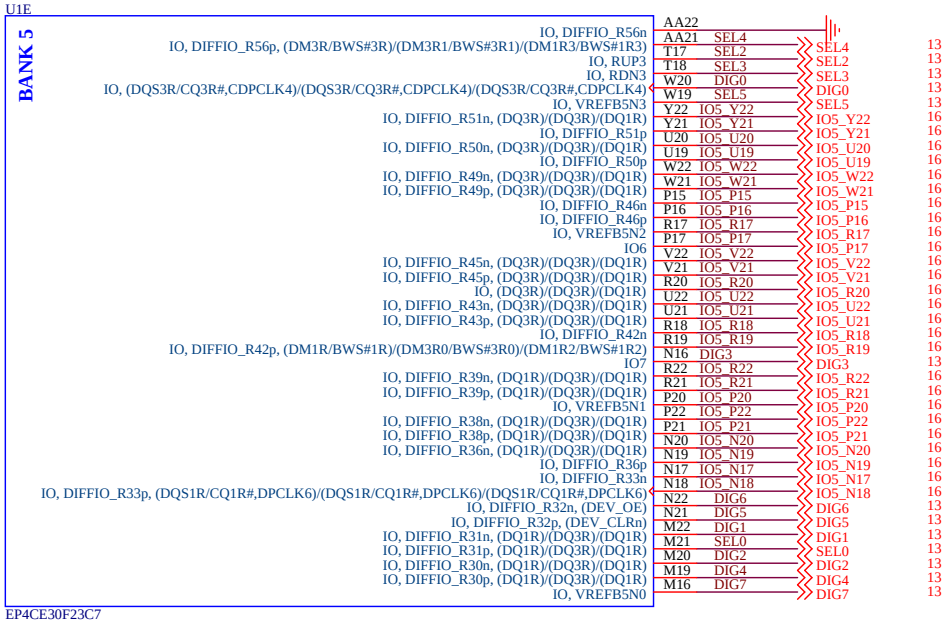

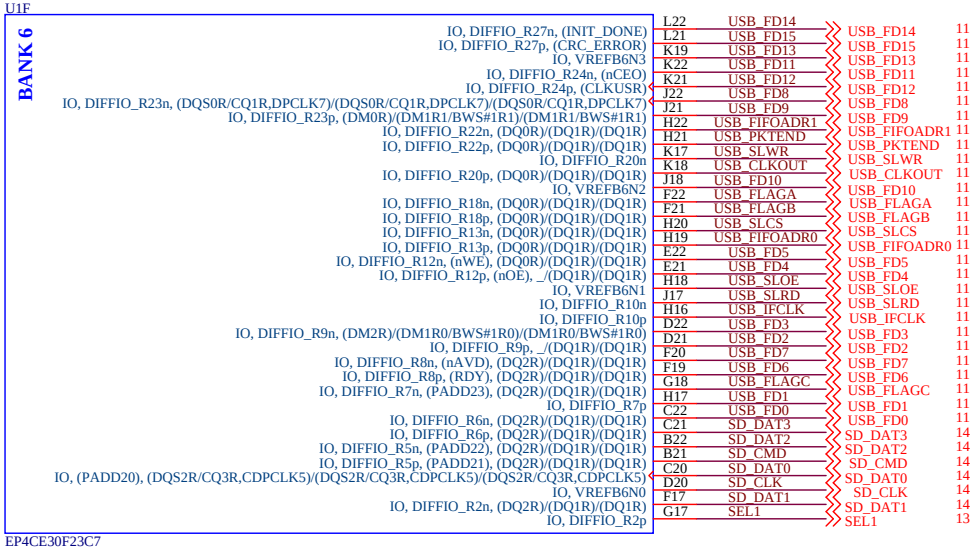

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Title: 05_FPGA

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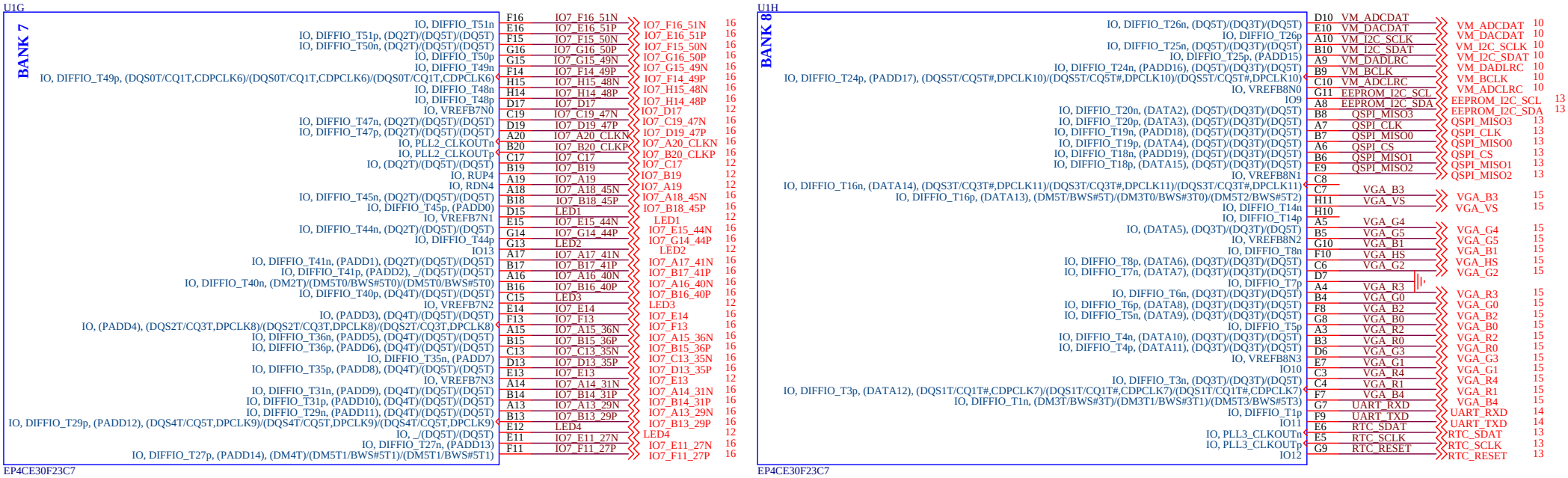

D7 change to GND, H10 change to VGA_B4

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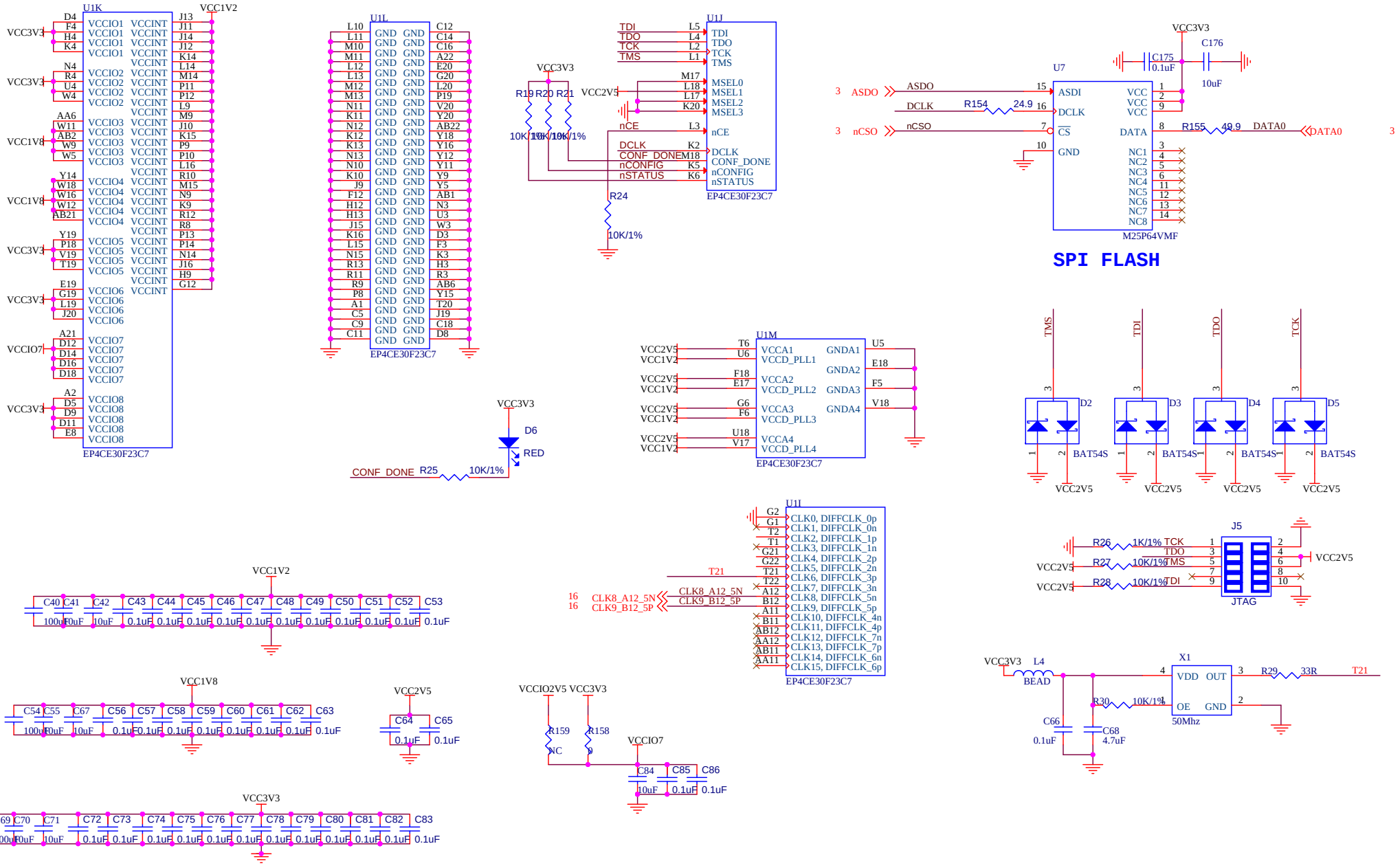
U1K
EP4CE30F23C7
U1L
EP4CE30F23C7
U1J
EP4CE30F23C7
TDI
TDO
TCK
TMS
nCE
nCONFIG
nSTATUS
CONF_DONE
MSEL0
MSEL1
MSEL2
MSEL3
DCLK
U7
M25P64VMF
ASDO
nCSO
DATA0
SPI FLASH
U1M
EP4CE30F23C7
U1I
EP4CE30F23C7
D2
D3
D4
D5
BAT54S
J5
JTAG
X1
50Mhz
BEAD
L4
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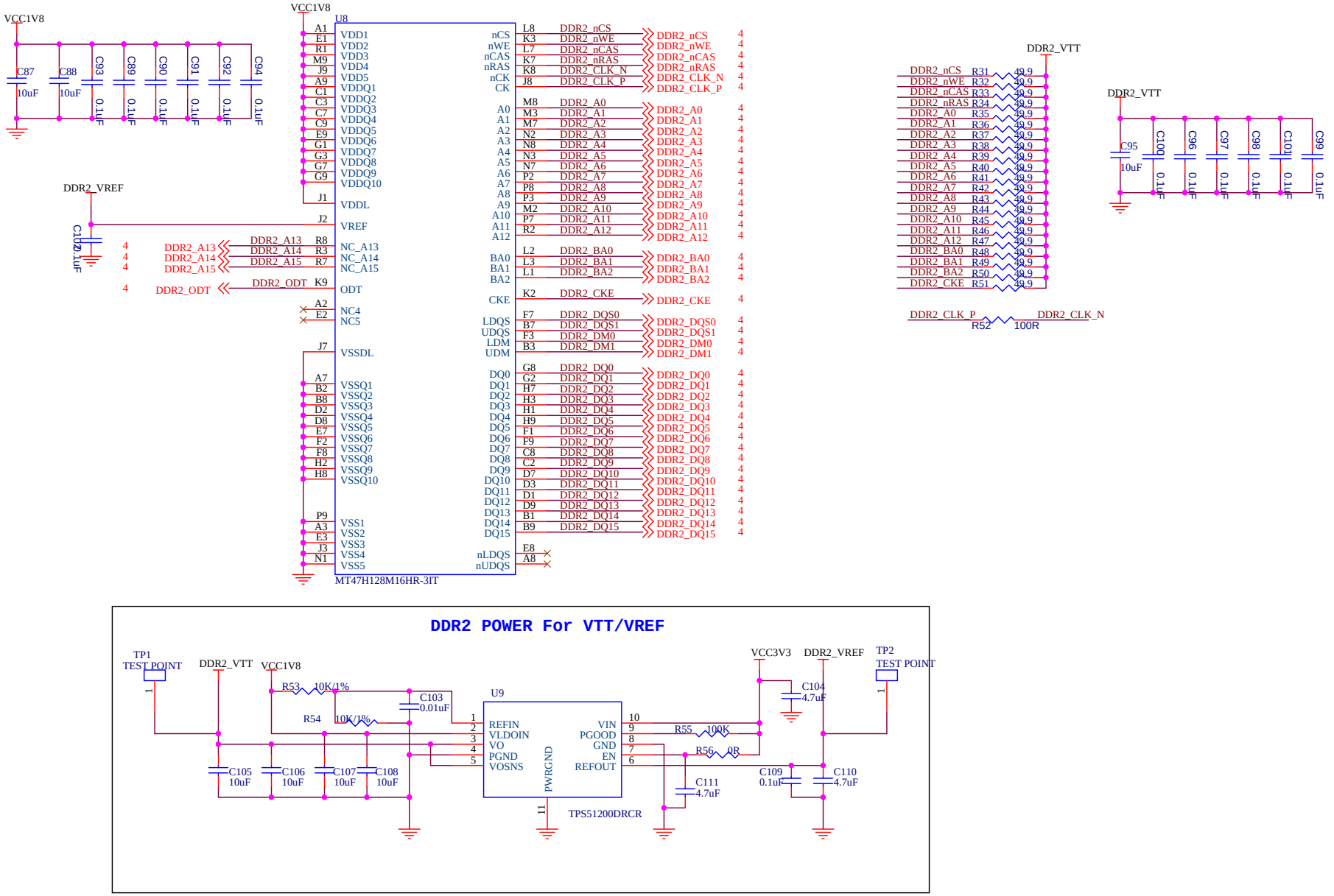

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Title: 08_DDR2		
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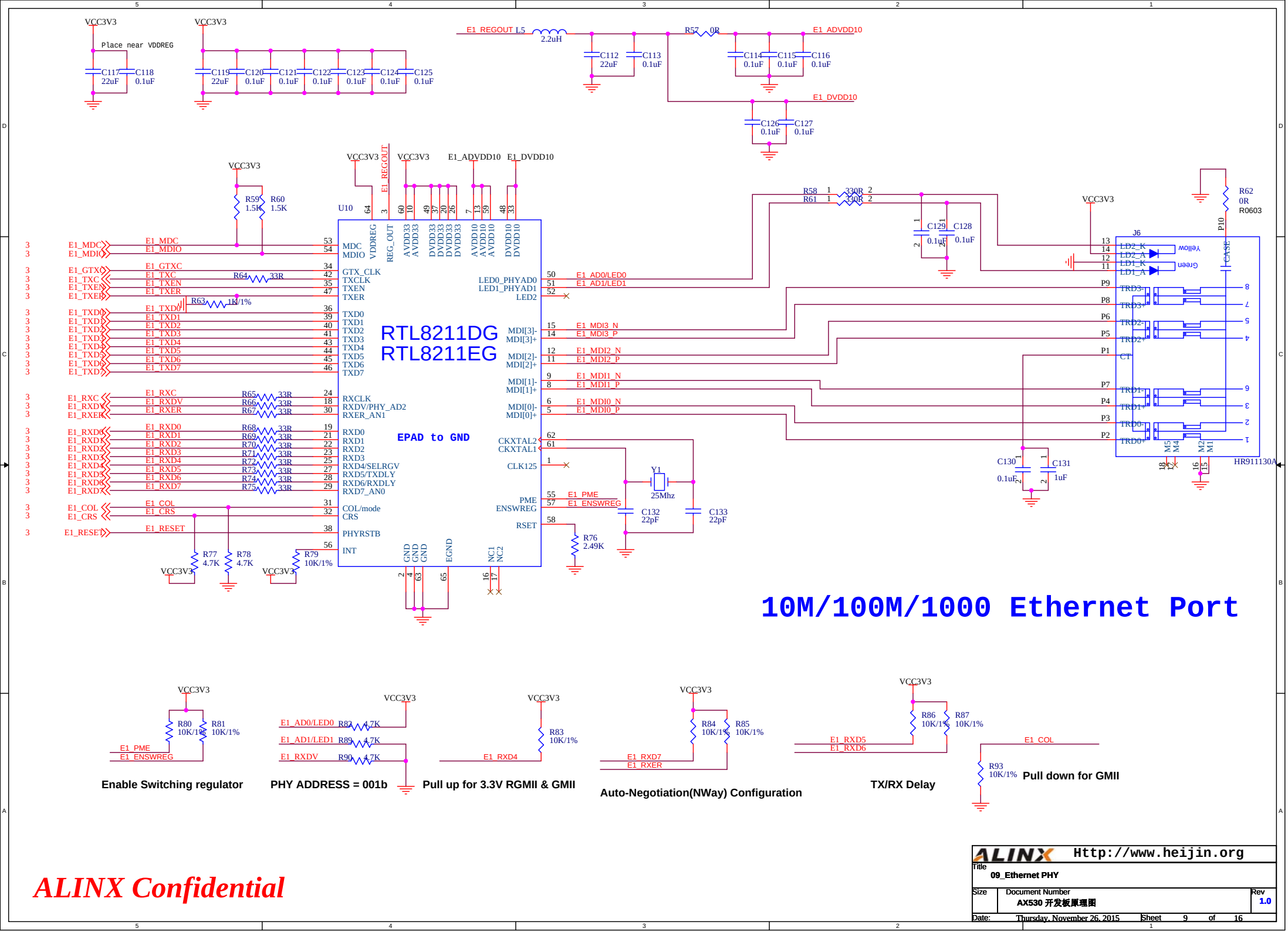
RTL8211DG
RTL8211EG
EPAD to GND
10M/100M/1000 Ethernet Port
Enable Switching regulator
PHY ADDRESS = 001b
Pull up for 3.3V RGMII & GMII
Auto-Negotiation(NWay) Configuration
TX/RX Delay
Pull down for GMII
HR911130A
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Title 09_Ethernet PHY
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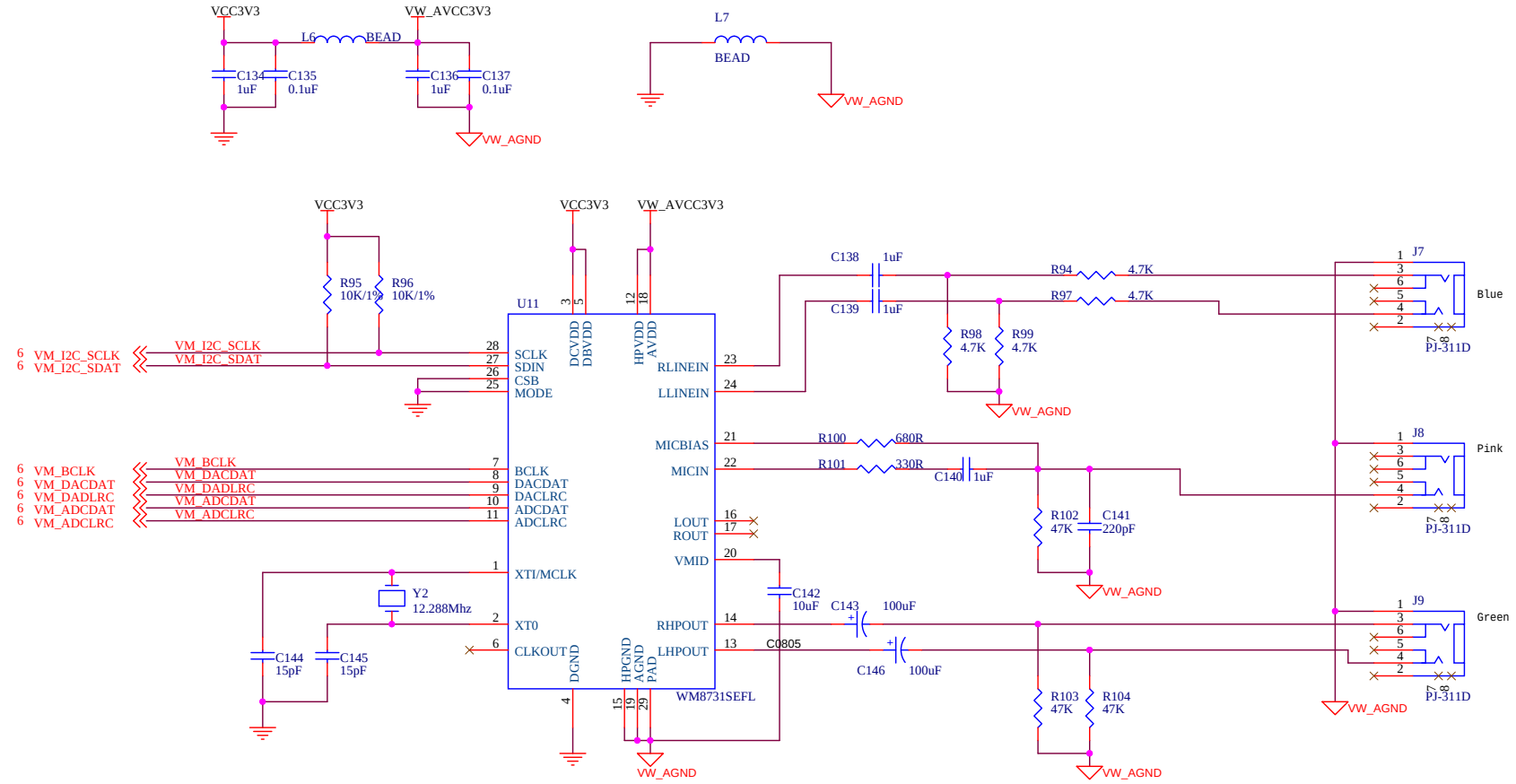

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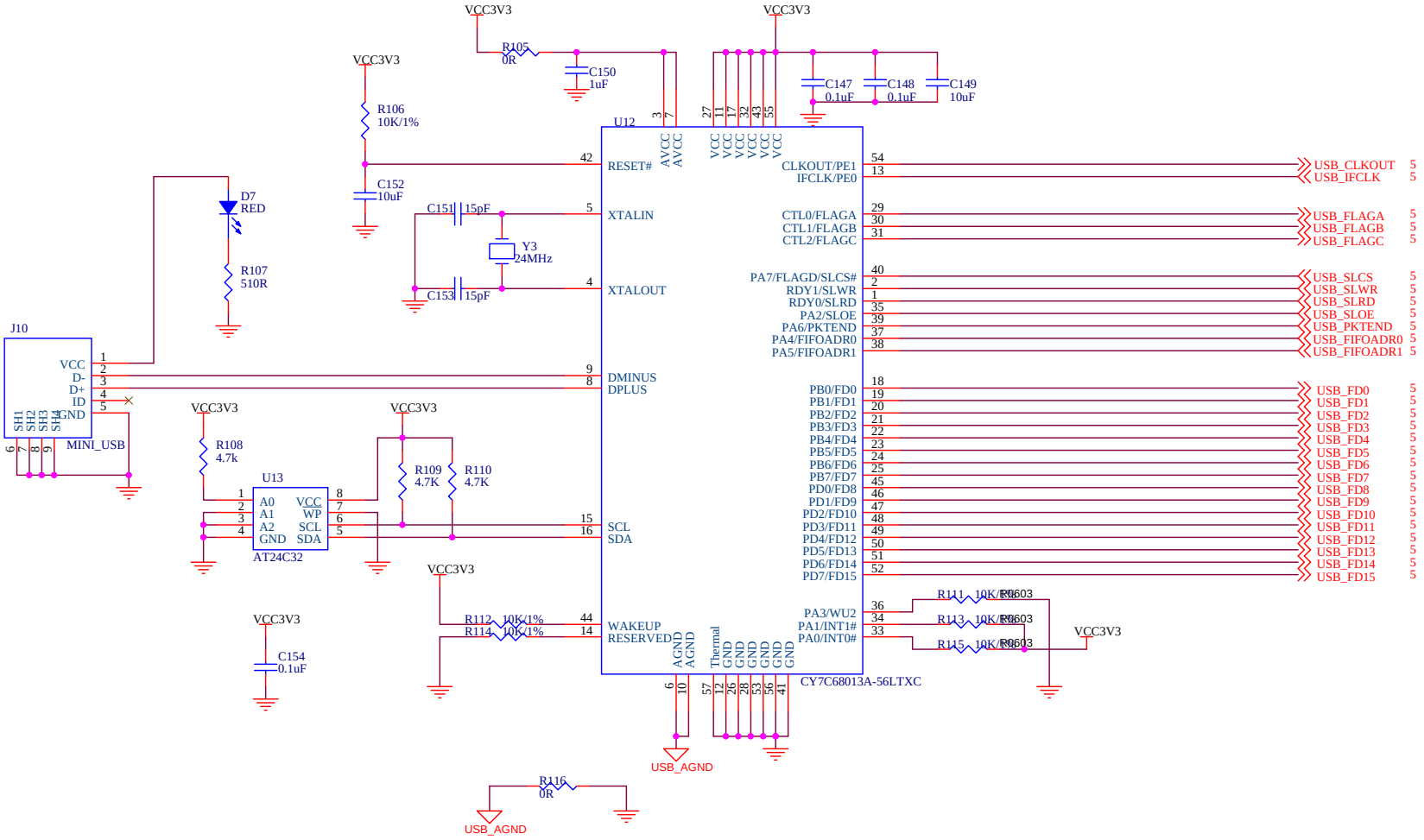

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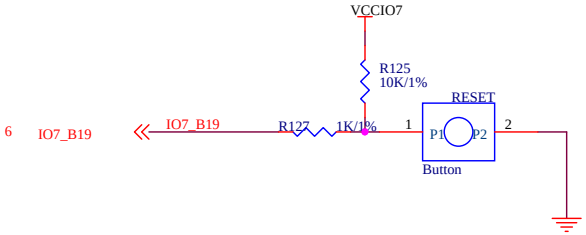
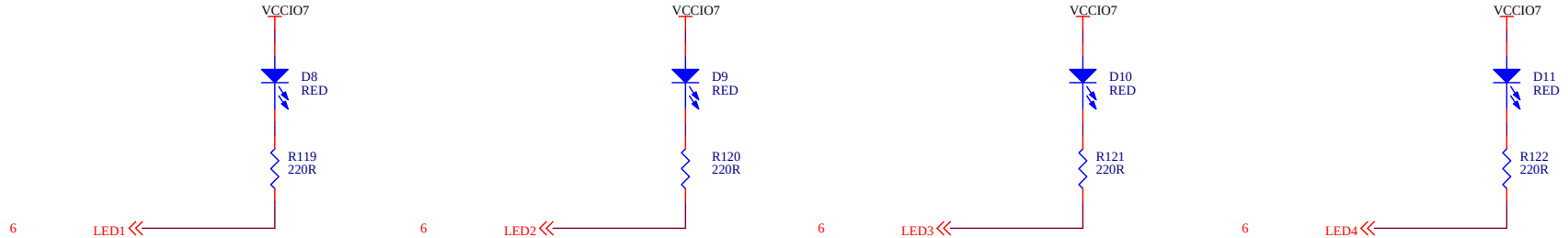
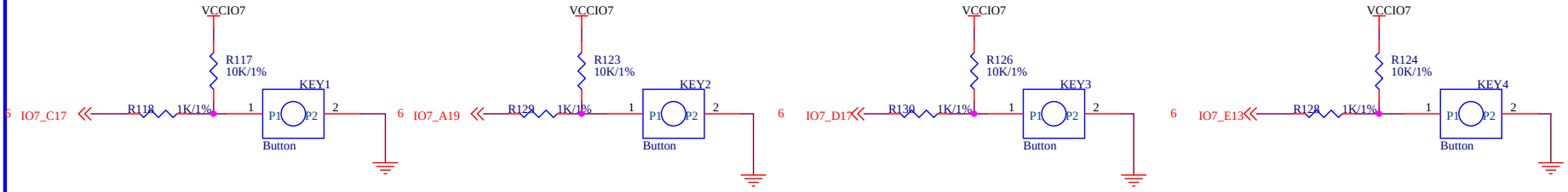
RESET KEY



USER LED



USER KEY



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Title: 12_KEY_LED		
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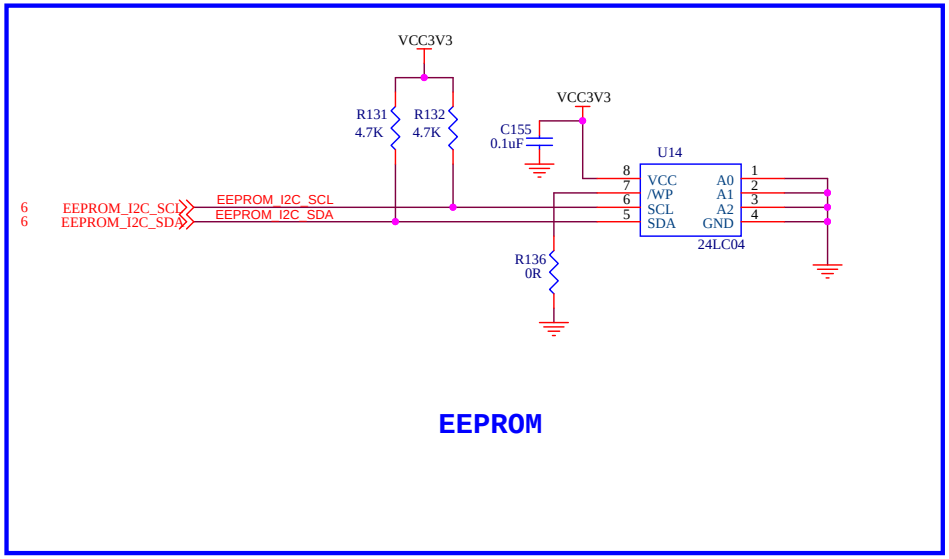
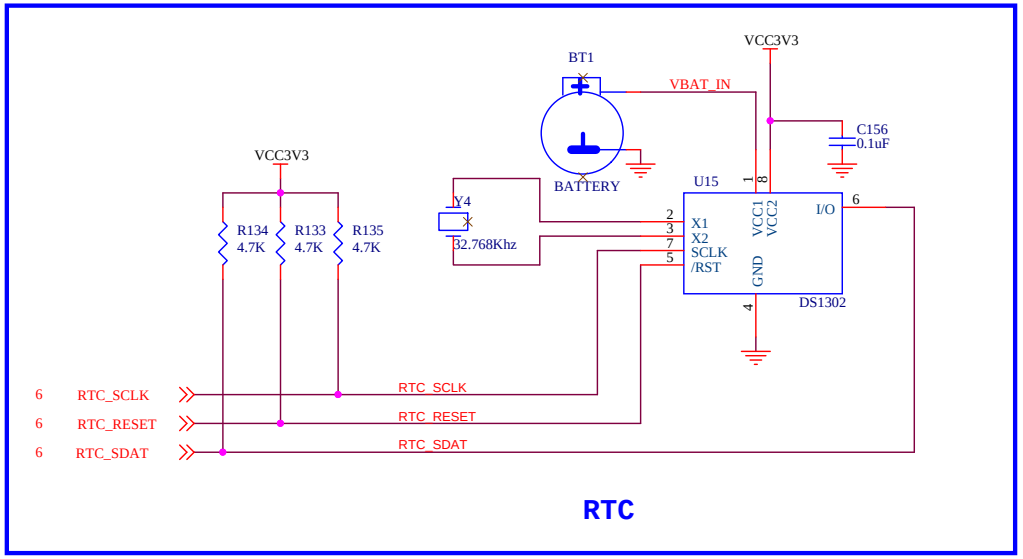
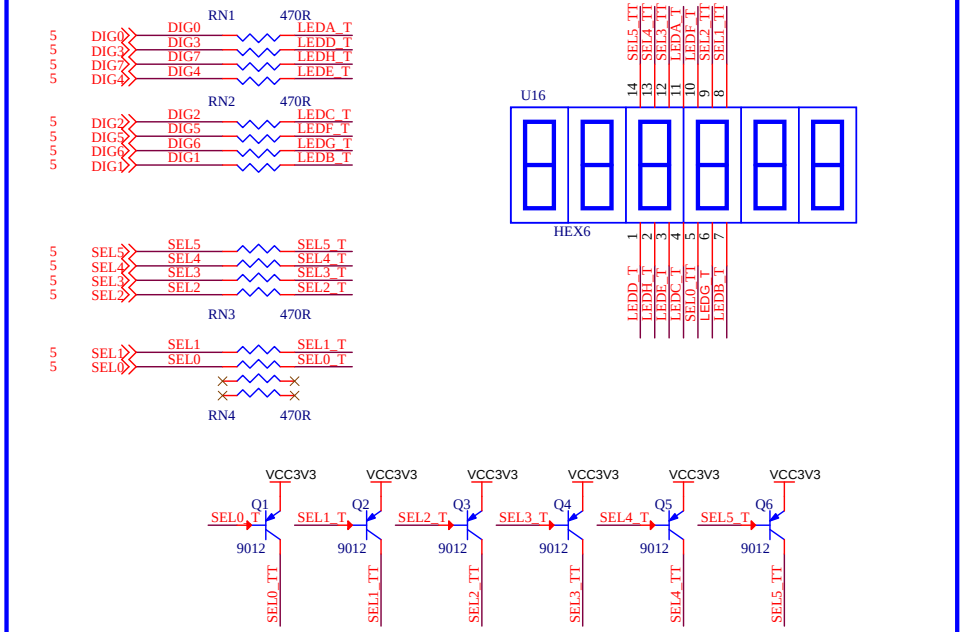


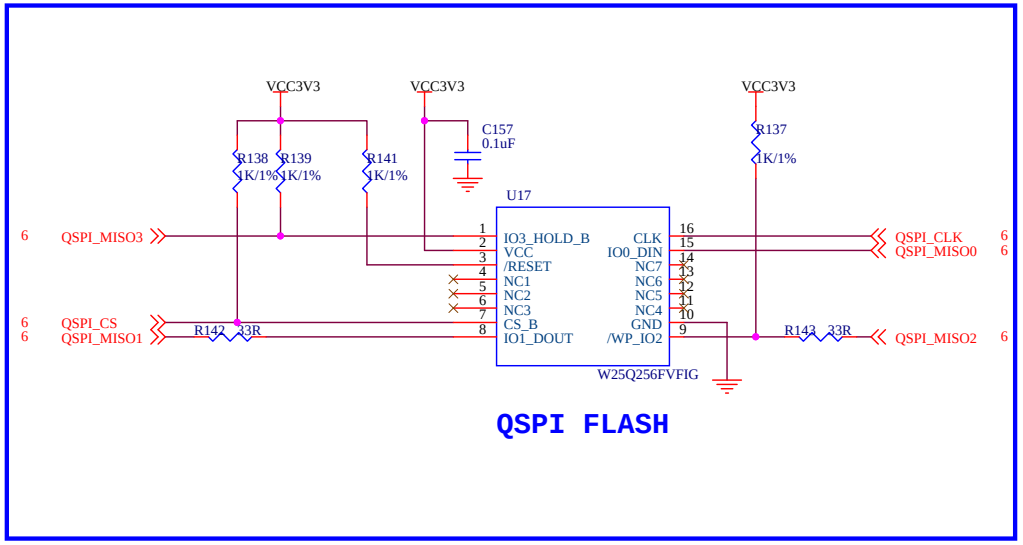
EEPROM



RTC

Digital tube





QSPI FLASH

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Title	13_RTC_ EEPROM_FLASH	
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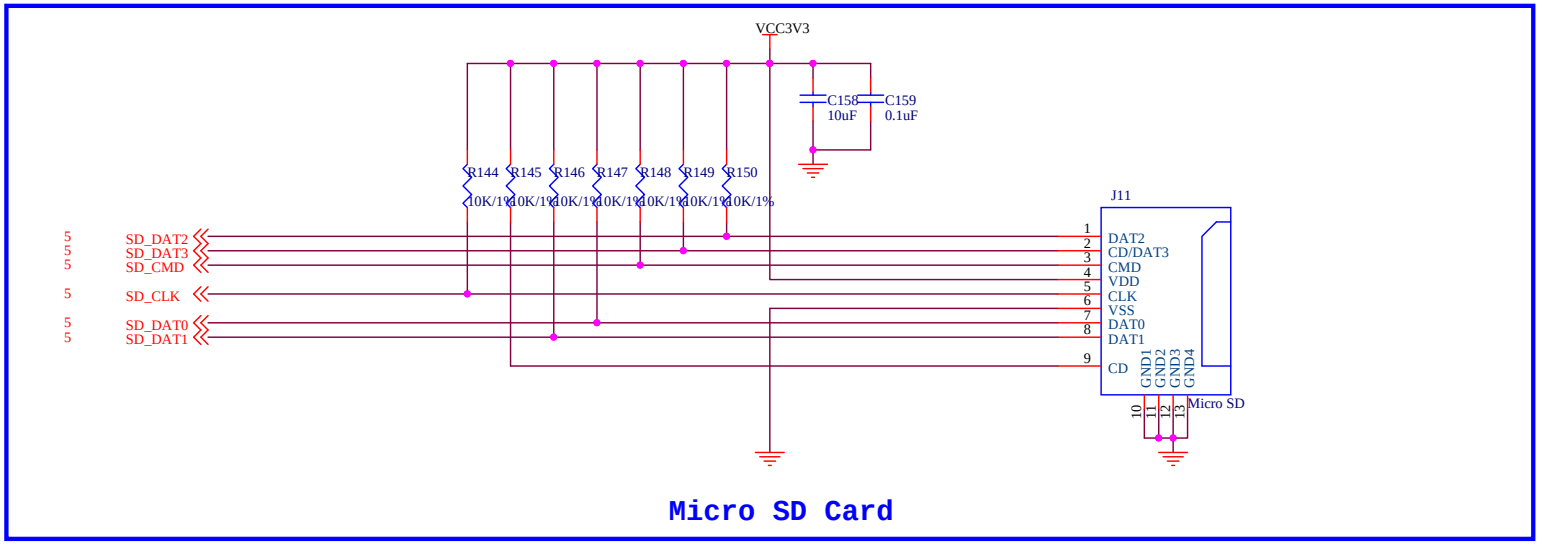
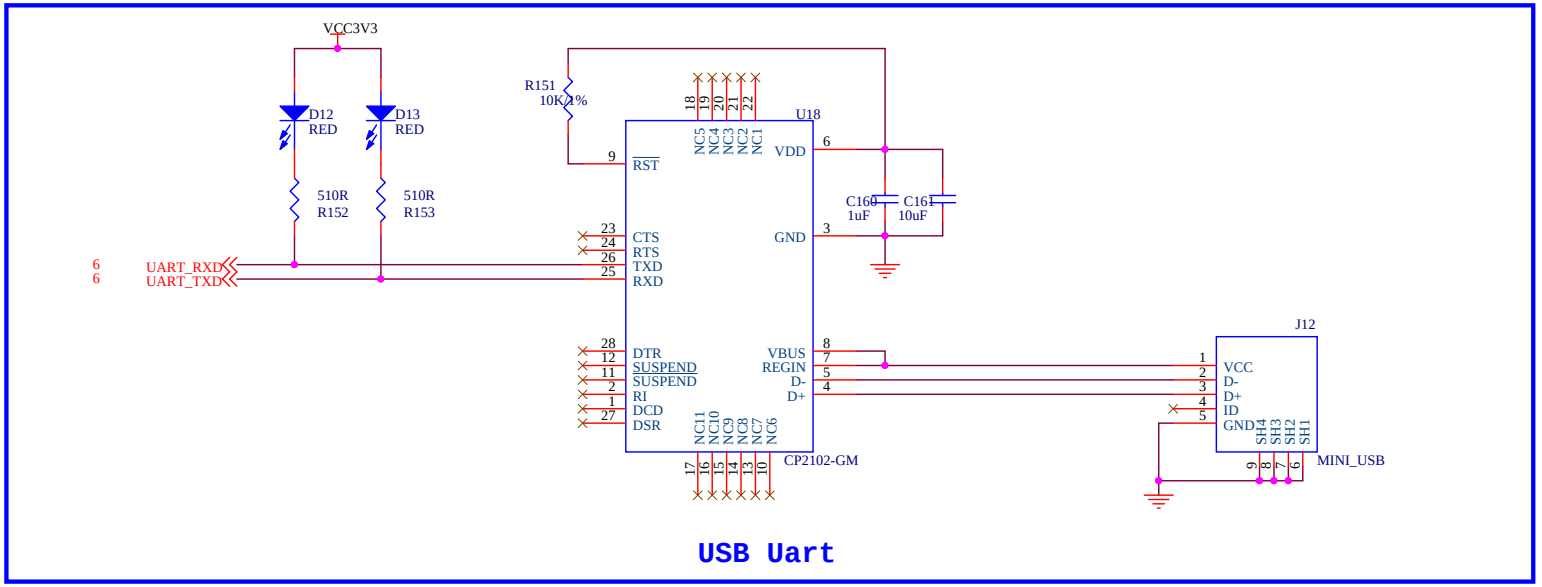


Micro SD Card



USB Uart

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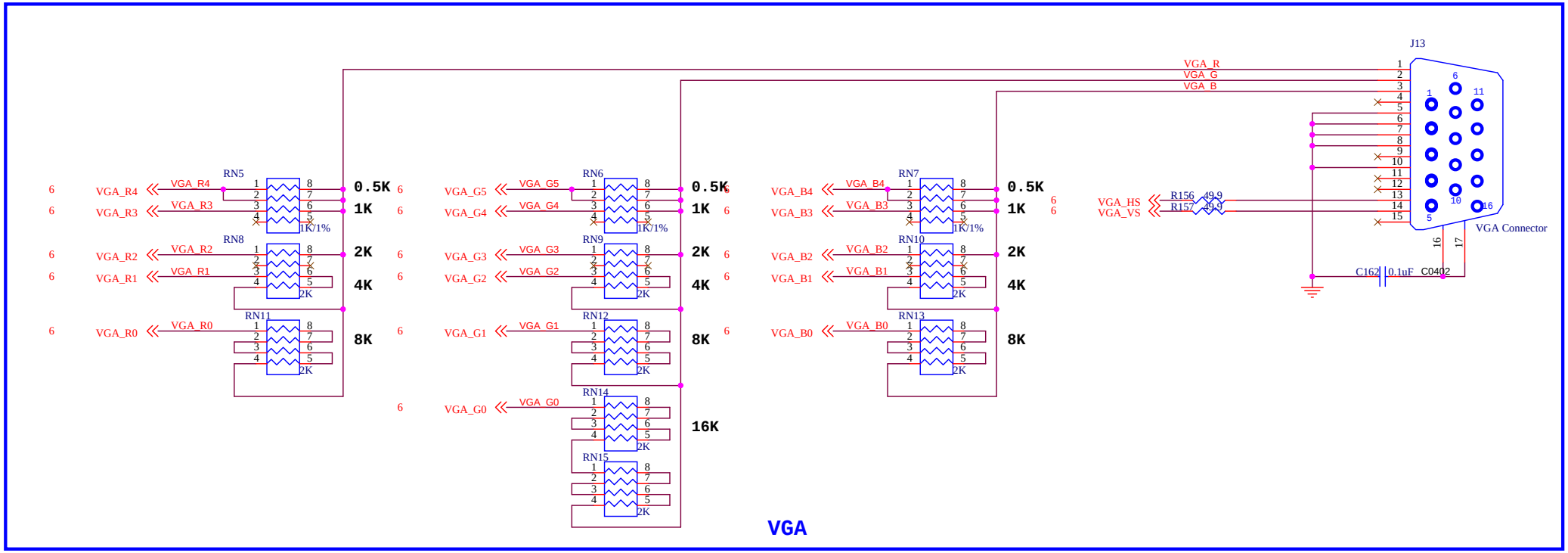

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Title	15_VGA_CAMERA	
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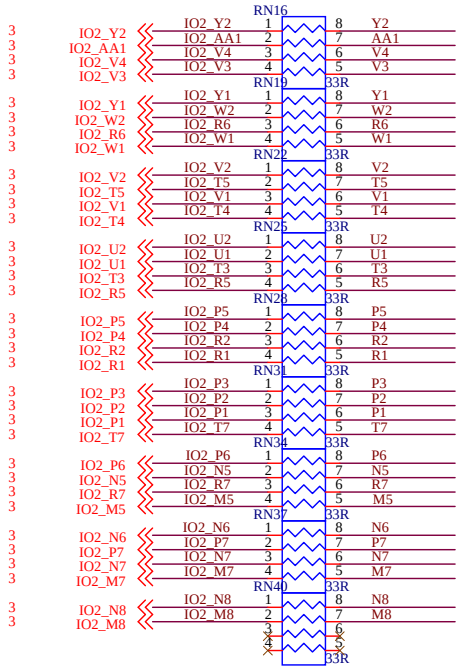

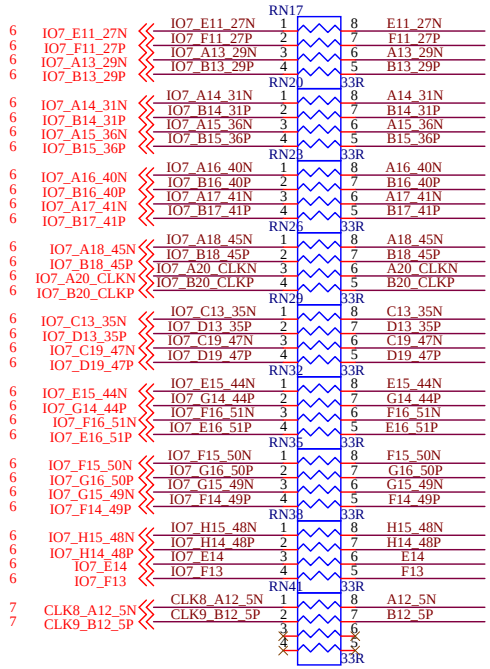

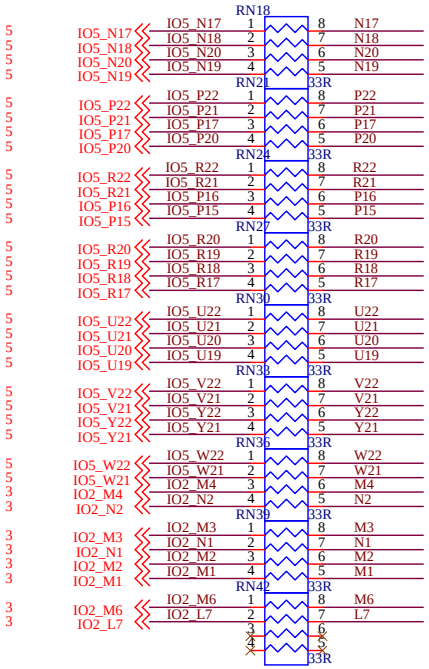

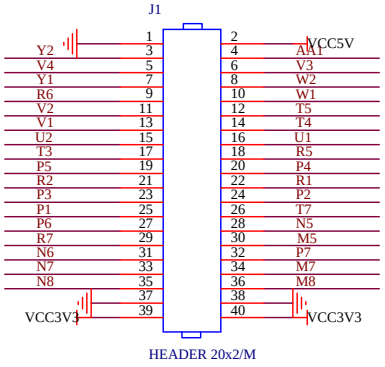

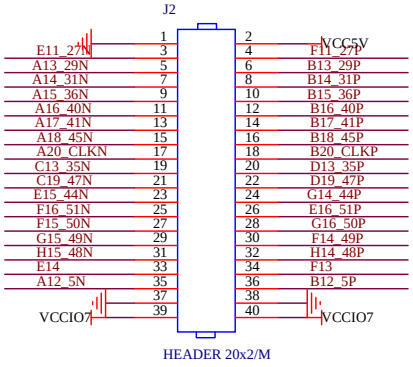

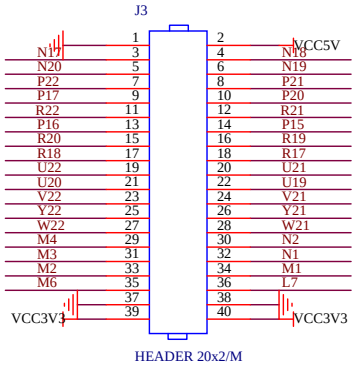

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