A2 A3	<pre>if (inet->hdrincl == 0) initialize_rfv(&rfv);</pre>	Basicblock record	
(a) Source code		A3	
A2	<pre>trace_access (&inet->hdrincl,</pre>	Memory access record (&inet->hdrincl, A2, 1, read)	
(b) Instrumented source code		(c) Per-thread memory region	n