A2 A3	if (inet->hdrincl == 0) initialize_rfv(&rfv);	Basicblock record
(a) Source code		A3
	trace_access (&inet->hdrincl, A2, 1, read, ts);	Memory access record
A2	if (inet->hdrincl == 0)	(&inet->hdrincl, A2, 1, read, ts)
	trace_basicblock(A3);	
A3	initialize_rfv(&rfv);	
(b) Instrumented source code		(c) Per-thread memory region

т