



Thread A Thread B

A2 

A4 


A6 


 B1

 B2

(a) Thread interleaving


Pair #1

A2 

 B1

Pair #2

A4 

 B1


Pair #3

A6 


 B2

(b) Conflicting instruction pairs

Segment #1
(Pair #1, #2)


A2 

A4 

 B1

Segment #2
(Pair #1, #3)

A2 


 B1

A6 

 B2

Segment #3
(Pair #2, #3)

A4 

 B1

A6 

 B2

(c) Interleaving segments