

```
A2      if (inet->hdrincl == 0)
A3          initialize_rfv(&rfv);
```

(a) source code

```
0x0:  trace_basicblock(0x0);
0x5:  trace_access (&inet->hdrincl,
                  0xa, 1, read);
0xa:  if (inet->hdrincl == 0) {
0x12:      trace_basicblock(0x12);
0x17:      initialize_rfv(&rfv);
0x1c: }
```

(b) abstracted binary

Basicblock record

0x0

0x12

...

Memory access record

(&inet->hdrincl, 0xa, 1, read)

...

(c) Per-thread memory regions