A2	if (**inet->hdrincl** == 0)
A3	initialize_rfv(**&rfv**);

(a) Source code

	trace_access (&inet->hdrincl, A2, 1, read, ts);
A2	if (**inet->hdrincl** == 0) {
	trace_basicblock(A3);
A3	initialize_rfv(**&rfv**);
	}

(b) Instrumented source code

Basicblock record

> A3
>
> ...

Memory access record

> (&inet->hdrincl, A2, 1, read, ts)
>
> ...

(c) Per-thread memory region