A2 A3	<pre>if (inet->hdrincl == 0) initialize_rfv(&rfv);</pre>	Basicblock record
(a) Source code		A3
	trace_access (&inet->hdrincl, A2, 1, read, ts);	Memory access record
A2	<pre>if (inet->hdrincl == 0) { trace_basicblock(A3);</pre>	(&inet->hdrincl, A2, 1, read, ts)
А3	<pre>initialize_rfv(&rfv); }</pre>	
(b) Instrumented source code		(c) Per-thread memory region