

A2	if (inet->hdrincl == 0)
A3	initialize_rfv(&rfv);

(a) Source code

	trace_basicblock(0x0);
	trace_access (&inet->hdrincl,
	0xa, 1, read);
A2	if (inet->hdrincl == 0) {
	trace_basicblock(0x12);
A3	initialize_rfv(&rfv);
	}

(b) Instrumented source code

Basicblock record

0x0
0x12
...

Memory access record

(&inet->hdrincl, 0xa, 1, read)
...

(c) Per-thread memory regions