

```
A2  if (inet-&gthdrincl == 0)
A3      initialize_rfv(&rfv);
```

(a) source code

```
0x0:  trace_read
      (&inet-&gthdrincl, 0x5, 1);
0x5:  if (inet-&gthdrincl == 0) {
0xb:      trace_basicblock(0xb);
0x12:      initialize_rfv(&rfv);
0x14:  }
```

(b) abstracted binary

**Memor\_access\_records**

```
(&inet-&gthdrincl, 0x5, 1, read)
...
```

**Basicblock\_records**

```
0xb
...
```

(c) memory region