# Cpr E 281 LAB1 ELECTRICAL AND COMPUTER ENGINEERING IOWA STATE UNIVERSITY

# Lab 1 Answer Sheet

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#### PRELAB:

**Q1.** Fill in the Truth Table below for an AND gate:

Α	В	С
0	0	0/False
0	1	0/False
1	0	0/false
1	1	1/true

**Q2.** What does the .bdf file extension stand for?

Bit Distribution Format

# LAB: 2.0 Fill in the Truth Table for *lab1step1*:

Α	В	С
0	0	0
0	1	0
1	0	0
1	1	1

Logic Expression: A+B = C

## Lab 1 Answer Sheet

### 4.0 Fill in the Truth Table for *lab1step2*:

W	X	Υ	Z
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	0

Logic Expression: (w'xy')+(wx'y')+(wx'y) = z

### 4.0 Fill in the Truth Table for *lab1step3*:

Α	В	С	F
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	0
1	1	0	1
1	0	1	1
1	1	1	0

Logic Expression: (A'B'C)+(A'BC)+(AB'C)+(ABC')=F