

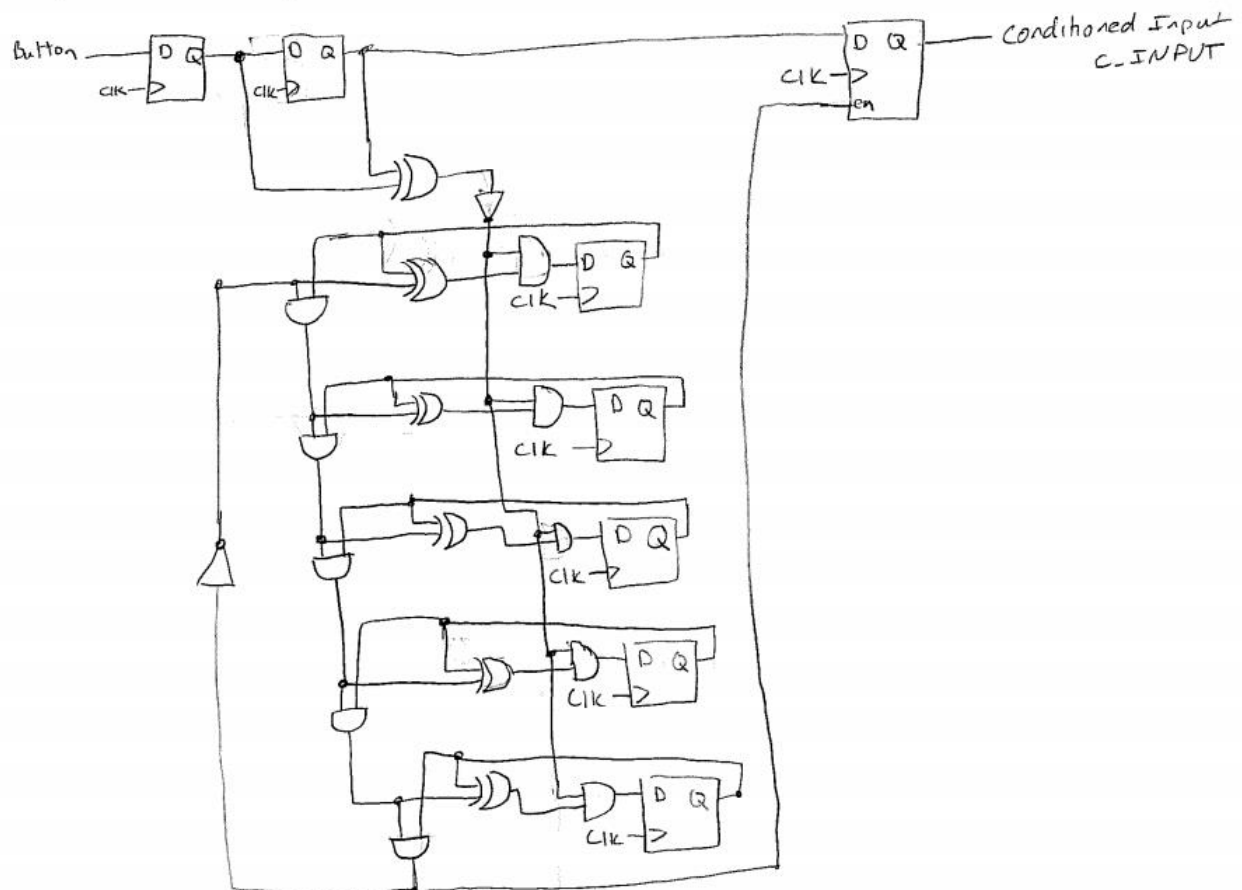
Specification:

Inputs:

Clock: CLK

Conditioned Output: C_INPUT

Input Conditioner



Performance:

Subcomponent	Cost Per	#Used	Total
DFF	13	7	91
DFF w/ E	20	1	20
Inverter	1	2	2
2AND	3	10	30
XOR	8	6	48

Total GIE: 191