BLINKING MODE SIGNAL:

Specification:

This component generates a signal that changes every second, cycling between off and on. This is done by having an active HIGH signal the enable on a 16-bit synchronous counter. The output is tied to the 16th bit, and will toggle between HIGH and LOW every 32768 cycles, or the frequency of the clock.
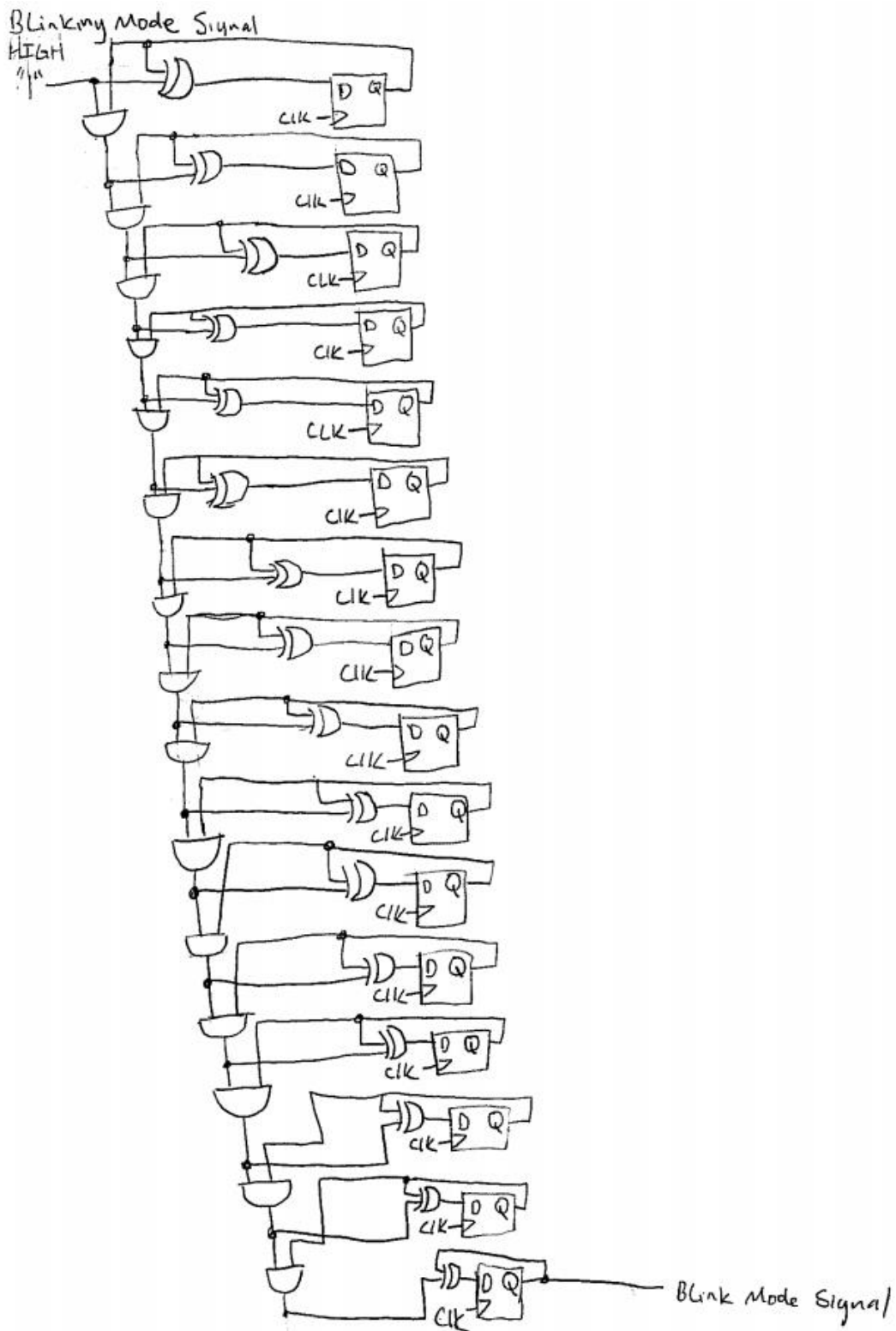
Inputs:

Clock: CLK

Outputs:

Blink Mode Signal: Single wire that outputs the signal. Should blink on and off on a 1 Hz frequency.

(Schematic On Next Page)

Schematic:

Performance:

Subcomponent	Cost Per	#Used	Total
DFF	13	16	208
2AND	3	15	45
XOR	8	16	128

Total GIE: 381