

Lab4 Demo

- (40%) Simulate a specific cache given at demo time(need to modify your source code).
 - MRL(Most recently used)
 - 優先清除”最常使用的數據

	64 32 8 MRU spice.din
Demand fetch	1000001
Cache hit	990004
Cache miss	9997
Miss rate	0.0100
Read data	159699
Write data	66538
Bytes from memory	319904
Bytes to memory	224352