

# HY-TTC 500

## Quick Start Guide - C Programming Programmable ECU for Sensor-Actuator Management

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**Product Version 01.08**

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## Part I

# Quick Start Guide

## 1 Overview

The purpose of this document is to give a short overview of how to setup the HY-TTC 500 Starter Kit for C programming.

In case of errors or bugs in documents or workshop examples, please send a feedback to our support team ([support@ttcontrol.com](mailto:support@ttcontrol.com)).

## 2 Information and latest version of software

Get the latest version of the Quick Start Guide and information about new product features, improvements and bug fixes from our Service Area at <http://www.ttcontrol.com/service-area/>.

Download the latest files as follows:

1. Enter the **Service Area** page.
2. Then click **Controller > HY-TTC 500 Family > IO Driver Release > IO Driver Release <latest version>**.
3. From the page with the latest IO Driver Release, download **Installer - C Programming Environment for HY-TTC 500 <version and build>**.

### 3 Getting Started

TTControl GmbH recommends using the HY-TTC 500 Starter Kit, because it includes all components required for smooth development. There are two variants of the HY-TTC 500 Starter Kit available: one for C programming (with JTAG-Adapter and open housing) and one for CODESYS programming. This Quick Start Guide is for the C programming variant (see Figure 3.2 on the following page). For the CODESYS programming variant, see [5].

Note that the Ethernet USB adapter is only part of the starter kit if the corresponding HY-TTC 500 ECU is equipped with an Ethernet interface.

Nr.	Starter Kit Components
1	HY-TTC 500 ECU open housing
2	JTAG adapter
3	JTAG adapter cable
4	Interface board
5	Cable harness 1.5 m
6	Installation CD C-Programming
7	Ethernet USB adapter with driver and software
8	PCAN USB adapter with driver and software
9	Datacable 2m
10	Quick Start Guide



Figure 3.1: HY-TTC 500 Starter Kit



Figure 3.2: HY-TTC 500 Starter Kit for C programming

## 4 Using the Interface Board

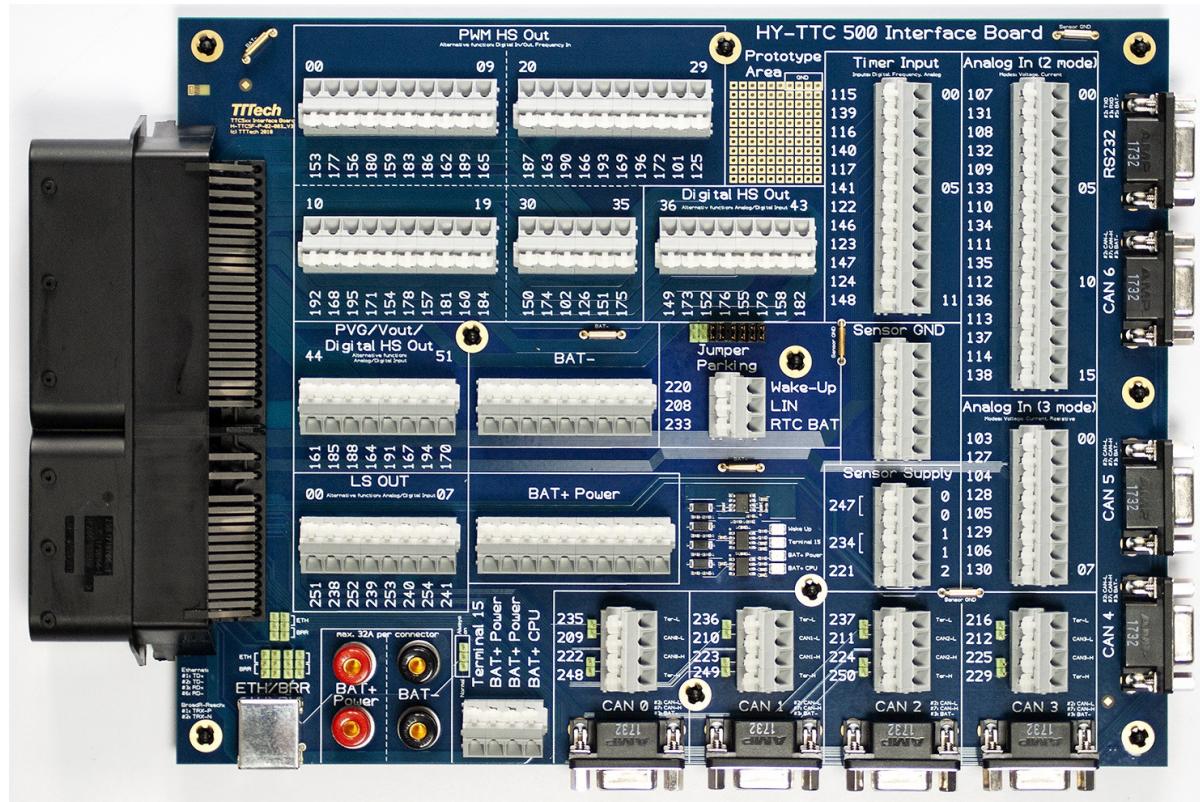


Figure 4.1: HY-TTC 500 Interface Board

### 4.1 HY-TTC 500 Cable Harness

Connect the HY-TTC 500 to the connector interface board with the provided cable harness.

**Note:** Do not use a cable harness from a different HY-TTC 500 variant! The HY-TTC 500 variants use different cable harnesses and the usage of the wrong cable harness may damage the HY-TTC 500 device.

## 4.2 HY-TTC 500 CAN Termination

If termination of a CAN interface ( $2 \times 60 \Omega$  split termination) is required, then connect the jumper pins for the CAN interface as follows:

CAN Interface	Connect Jumper Pins
CAN0	235 with 209 and 222 with 248
CAN1	236 with 210 and 223 with 249
CAN2	237 with 211 and 224 with 250
CAN3	216 with 212 and 225 with 229

Figure 4.3 on the next page shows the jumper setting for CAN0 as example.

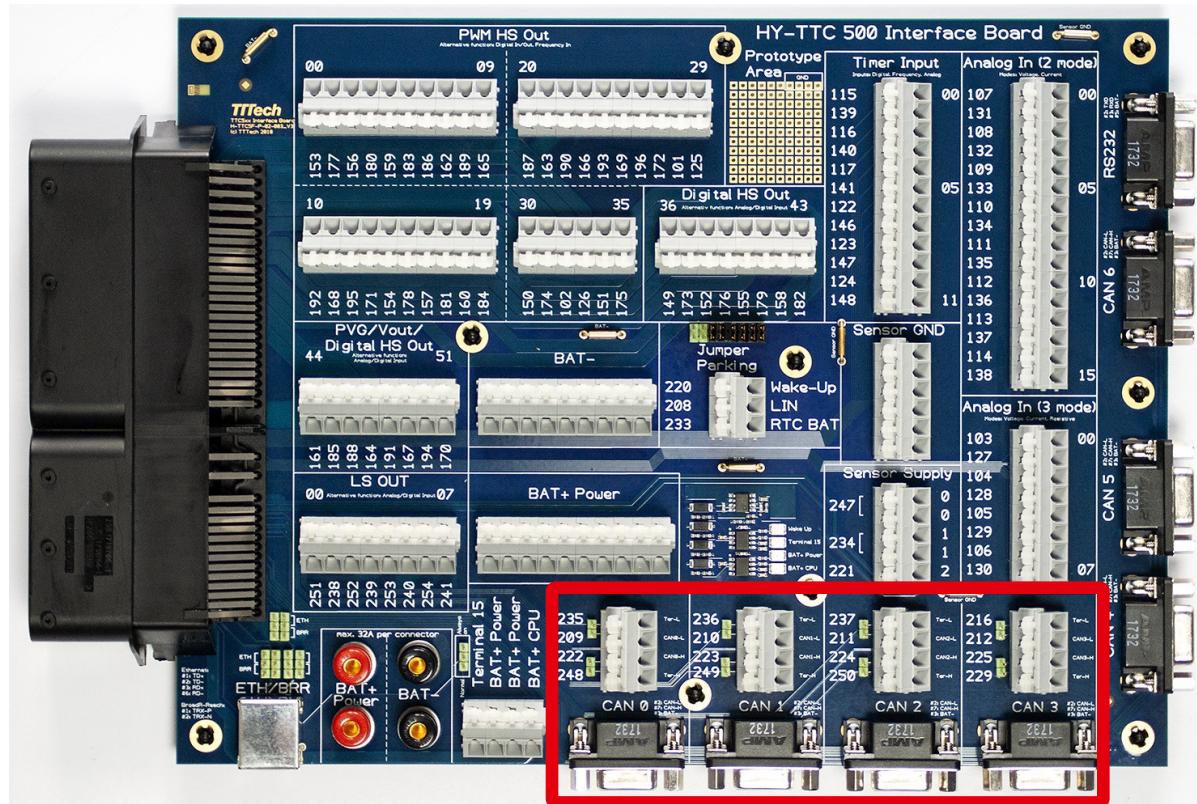


Figure 4.2: HY-TTC 500 Interface Board CAN Termination

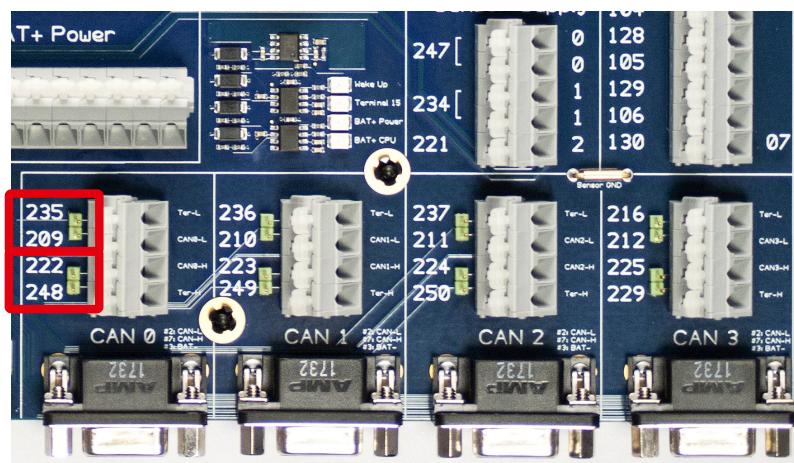


Figure 4.3: HY-TTC 500 Interface Board CAN Termination

### 4.3 HY-TTC 500 Power Supply

- Connect the power supply GND to the black socket named **BAT-**.
- Connect the positive power supply to the red socket named **BAT+ Power** and to **BAT+ CPU** on the connector terminal block, e.g. by short-circuit of **BAT+ CPU** to **BAT+ Power**.

See also Figure 4.4 on the following page and Figure 4.5 on the next page.

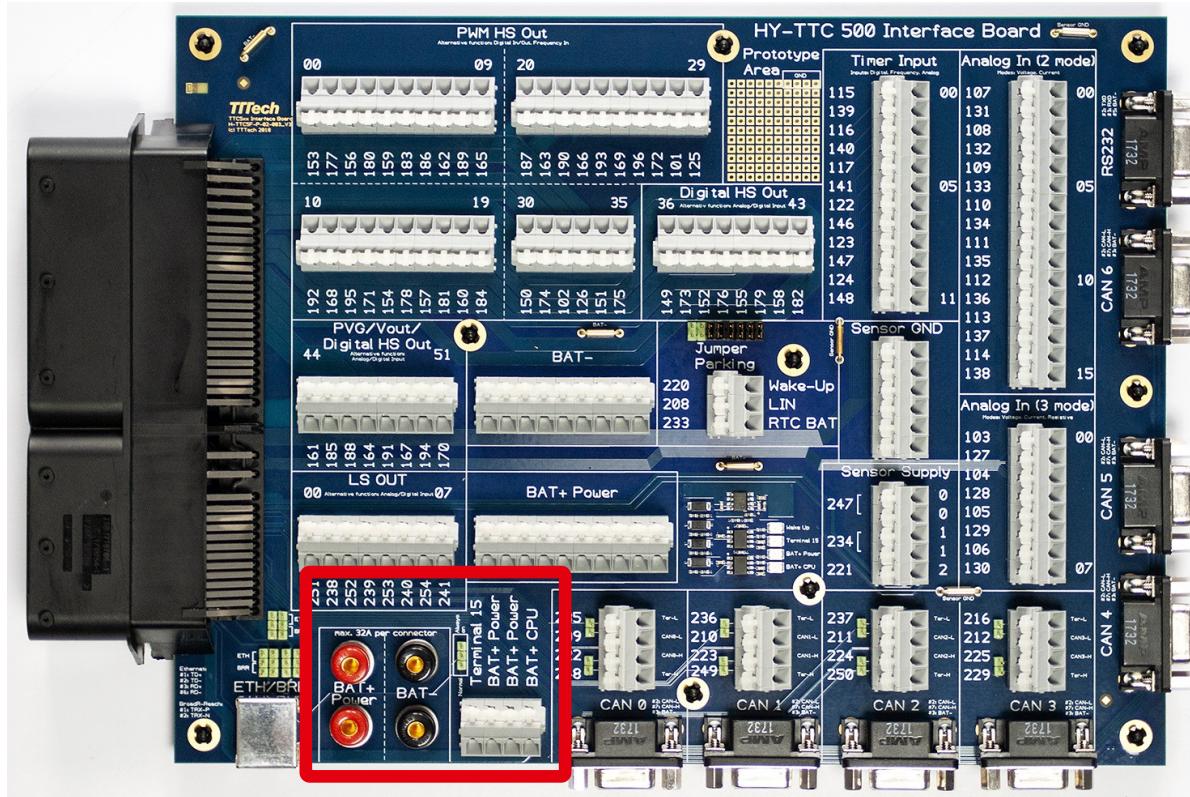


Figure 4.4: HY-TTC 500 Interface Board Power Supply

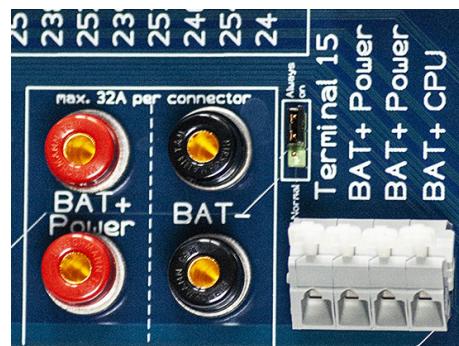


Figure 4.5: HY-TTC 500 Interface Board Power Supply

## 4.4 HY-TTC 500 Terminal 15 (KL15) Modes

- **Always on Mode:**

Put the jumper to **Always on** mode if the HY-TTC 500 shall be in active state independent of **Terminal 15**.

- **Normal Mode:**

Put the jumper to **Normal** mode if the HY-TTC 500 power-up shall depend on **Terminal 15**.

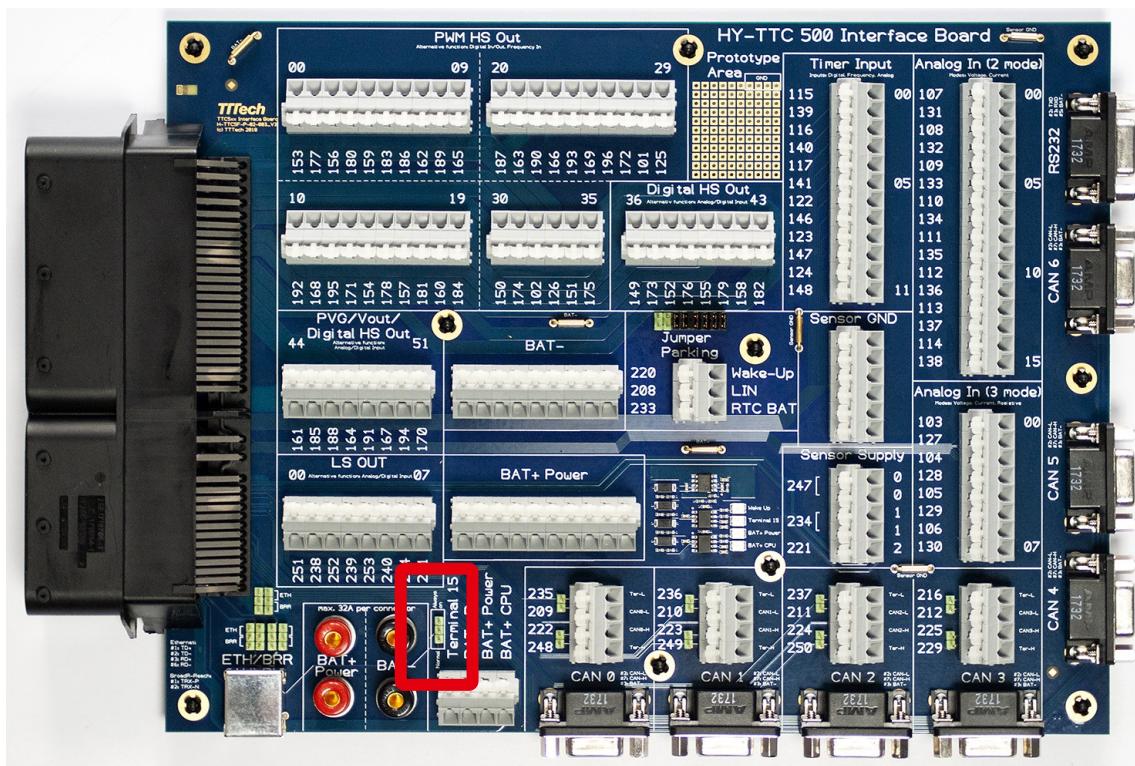


Figure 4.6: HY-TTC 500 Interface Board Terminal 15 (KL15)



Figure 4.7: HY-TTC 500 Interface Board Terminal 15 (KL15)

## 4.5 Status LEDs

There are status LEDs for

- BAT+ Power
- BAT+ CPU
- Terminal 15
- Wake-Up

See Figure 4.8 on this page and Figure 4.9 on the next page.

A status LED is on, when the corresponding input voltage is above the lower voltage limit (8V) for safe operation of the ECU.

The switching threshold of the LEDs is min. 8 V and max. 8.9 V (to guarantee a voltage >8 V over all parts tolerances).

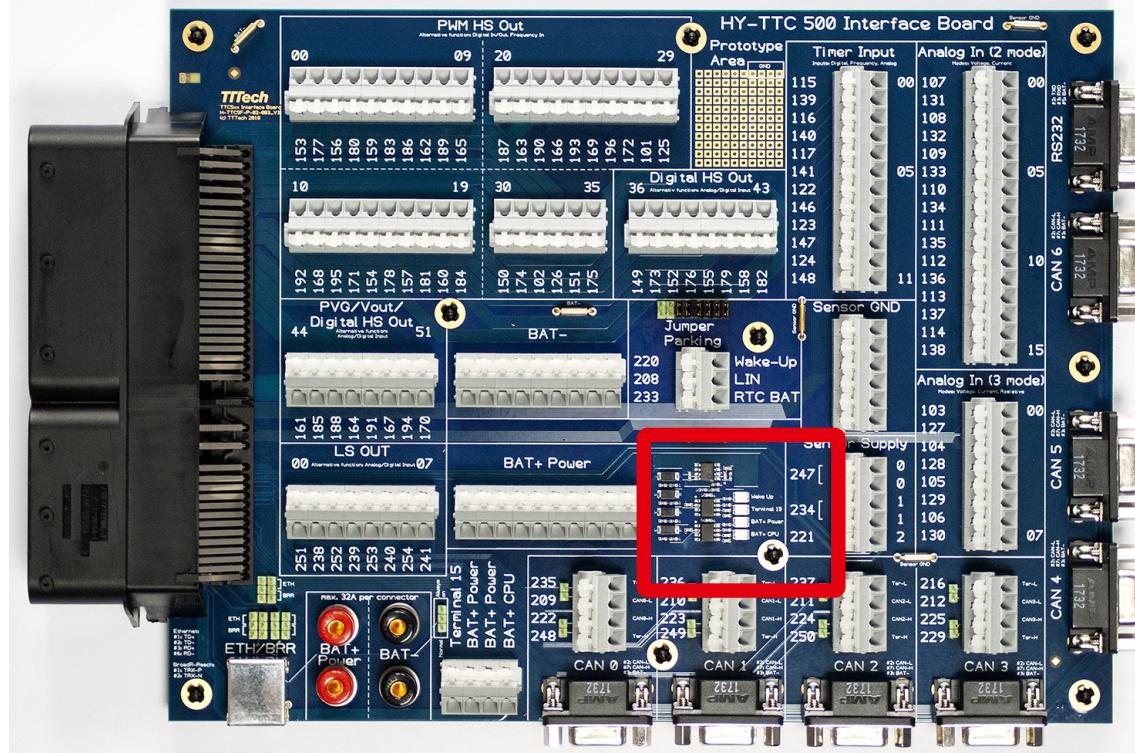


Figure 4.8: Status LEDs

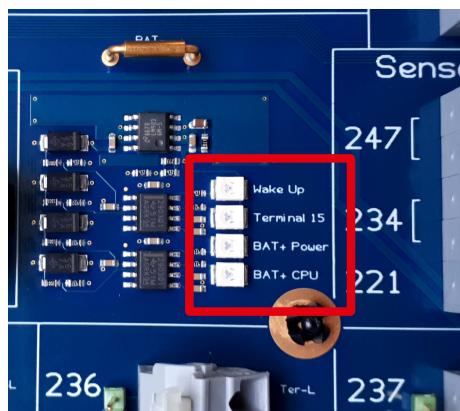


Figure 4.9: Status LEDs

## 4.6 Selecting BroadR-Reach® or Ethernet mode

For Ethernet mode, set all six jumpers as shown in Figure 4.10(a) on this page.

**Note:** The Ethernet mode is only available for HY-TTC 580.

For BroadR-Reach® mode (also known as 100BASE-T1 Ethernet), set all six jumpers as shown in Figure 4.10(b) on the current page.

**Note:** The BroadR-Reach® mode is available on HY-TTC 590E, HY-TTC 590, and HY-TTC 508.



(a) Jumpers for Ethernet mode

(b) Jumpers for BroadR-Reach® mode

Figure 4.10: Jumpers for Ethernet or BroadR-Reach® mode

## 4.7 HY-TTC 500 Download and Debugging

For application download to and debugging of HY-TTC 500 there are the following options:

- For download and debugging via **JTAG** it is possible to use the **Lauterbach Power Debug Module** with the **TTControl JTAG Adapter Board** and Lauterbach Batch files (\*.cmm).
- For download via **CAN** it is possible to use the **TTControl Downloader (TTC-Downloader)** and the **Peak PCAN-USB Adapter**.
- For download via **Ethernet** it is possible to use the **TTControl Downloader** and an **Ethernet Interface**.

**Note:** For unexperienced users of the HY-TTC 500 it is recommended to use the CAN or JTAG option. If you use Ethernet and an application is already flashed to the target, then you have to make sure that the application listens to download requests on the Ethernet. The example application of the Quick Start Guide does not listen to download requests.

## 5 C Programming Howto for HY-TTC 500

### 5.1 Overview

The following C programming Howto gives customers who purchase a product of the HY-TTC 500 family a quick overview of how to program these devices in C. This Howto also lists basic C code to demonstrate how to implement main functions and safety-relevant functions of the device. Finally, this Howto provides a step-by-step guide on how to flash the software with a [Lauterbach Debugging Device](#).

### 5.2 Endianess

The endianess of the ARM®Cortex™-R4F core of the TI TMS570 CPU is configured to BE32. Big-endian systems store the most-significant byte of a multi-byte data field in the lowest memory address. Also, the address of the multi-byte data field is the lowest address. The endianess of the HY-TTC 500 controllers can not be changed.

### 5.3 Tool Chain

The required tool chain is **TI ARM Code Generation Tools**, version 5.1.6. It conforms to the ISO/IEC 9899:1990 C standard [1], which was previously ratified as ANSI X3.159-1989 (ANSI C89).

An integrated development environment (IDE) named **TI Code Composer Studio (TI CCS)**, is available online from [http://processors.wiki.ti.com/index.php/Download\\_CCS#](http://processors.wiki.ti.com/index.php/Download_CCS#) or from your local distributor.

Upgrade or downgrade the tool chain of your TI CCS installation to a specific version as described in the following instruction list:

**Note:** It might be necessary to turn off the antivirus protection in Windows Security for Windows 10 before starting with upgrade or downgrade.

1. Click **Help > Install New Software** (see Figure 5.1 on the following page) to open the **Install** dialog.
2. Clear the check box **Show only the latest versions of available software** in the **Install** dialog (see Figure 5.2 on the next page).
3. From the **Work with** list, select **Code Generation Tools Updates** (see Figure 5.3 on page 17). If this option is not available, then you need to perform the following sub-steps, otherwise continue with step 4 on page 18.
  - a) Click **Add** (see Figure 5.4 on page 17) to open the **Add Repository** dialog.
  - b) In the dialog (see Figure 5.5 on page 18):
    - for **Name**, enter **Code Generation Tools Updates**
    - for **Location**, enter [http://software-dl.ti.com/dsps/dsps\\_public\\_sw/sdo\\_ccstudiocodegen/Updates/p2win32/](http://software-dl.ti.com/dsps/dsps_public_sw/sdo_ccstudiocodegen/Updates/p2win32/)

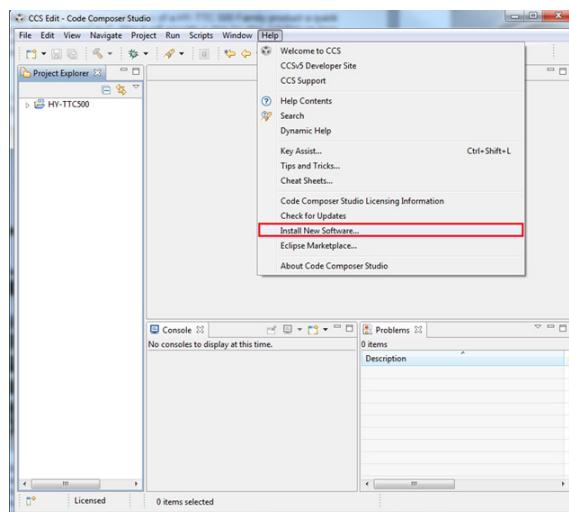


Figure 5.1: CCS Edit – Code Composer Studio

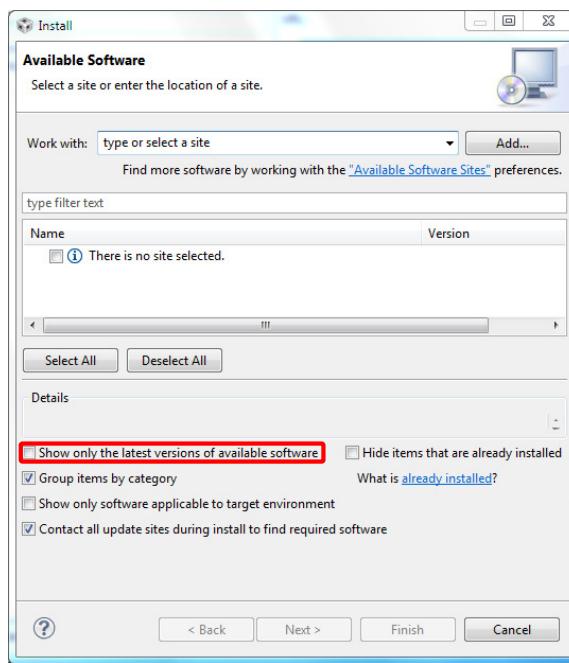


Figure 5.2: **Show only...** check box

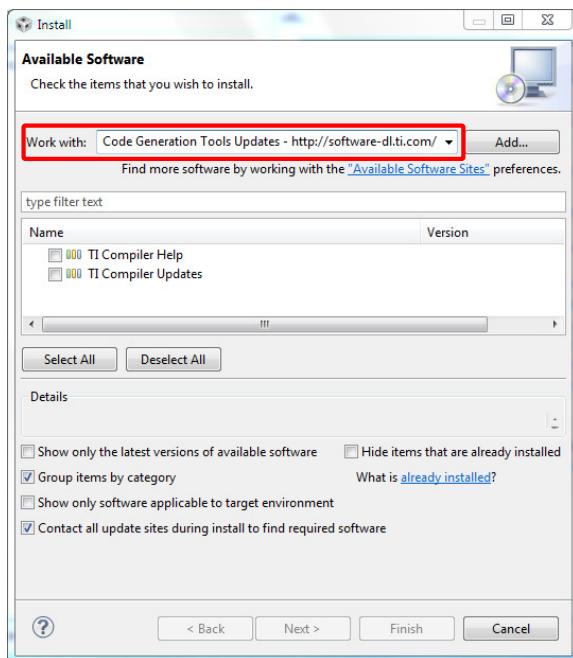


Figure 5.3: **Work with** field

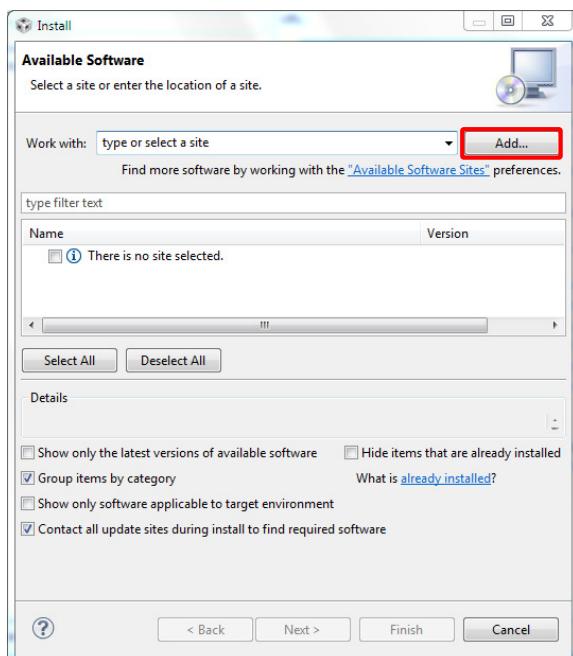


Figure 5.4: **Add** button

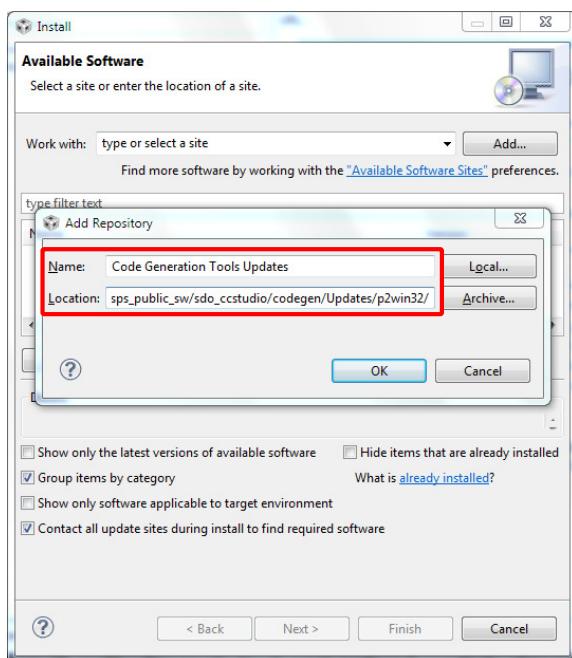


Figure 5.5: **Add Repository** dialog

- c) From the **Work with** field, select **Code Generation Tools Updates**.
- d) Continue with step 4 below.
4. From the list of tools, expand the **TI Compiler Updates** branch (see Figure 5.6 on the next page) and then select **ARM Compiler Tools 5.1.6** (see Figure 5.7 on the facing page)
5. Click **Next** and follow the instructions of the installation wizard.

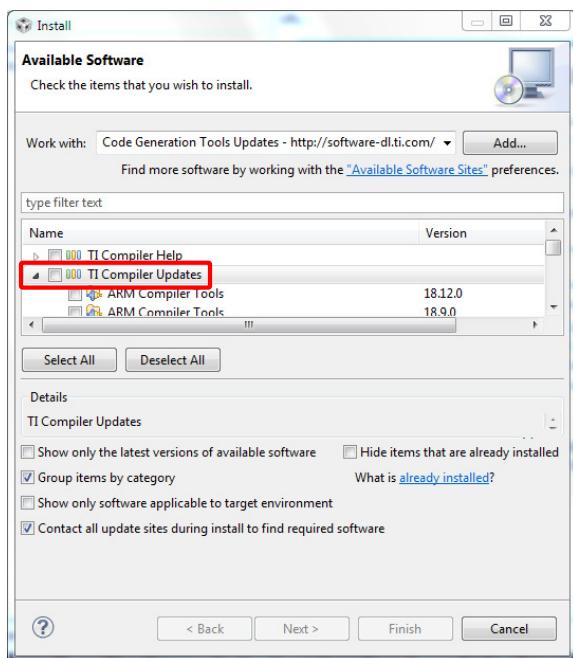


Figure 5.6: **TI Compiler Updates** branch

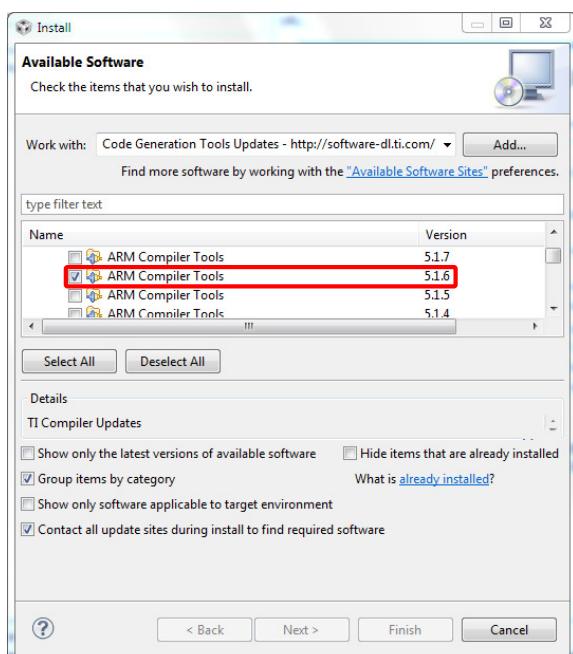


Figure 5.7: **ARM Compiler Tools 5.1.6**

## 5.4 File Structure

The developer package is extracted into five directories:

- **Bootloader**
- **Documentation**
- **FPGA\_Bitstream**
- **Get\_Started**
- **Release\_Notes**

Figure 5.8 on the current page shows the contents of the **Get\_Started** directory.

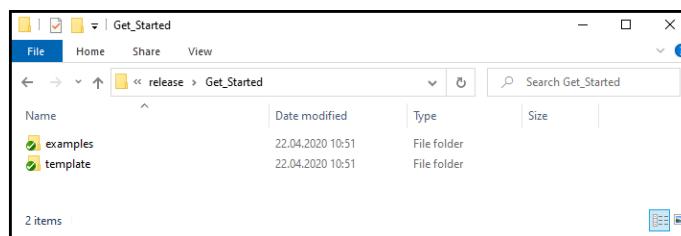


Figure 5.8: Contents of the **Get\_Started** directory

Figure 5.9(a) on the facing page shows the contents of the **examples** directory. The **examples** directory has some prebuilt projects to explain the usage of certain functions. Figure 5.9(b) on the next page shows the contents of the **template** directory, which contains a template for starting a new project.

Name	Date modified	Type	Size
ADC	22.04.2020 10:51	File folder	
CAN	22.04.2020 10:51	File folder	
CAN_SendFIFO	22.04.2020 10:51	File folder	
DEBUG	22.04.2020 10:51	File folder	
DIO	22.04.2020 10:51	File folder	
DO	22.04.2020 10:51	File folder	
DOWNLOAD	22.04.2020 10:51	File folder	
EEPROM	22.04.2020 10:51	File folder	
EEPROM_RTC_Process	22.04.2020 10:51	File folder	
Ext_ShutOff	22.04.2020 10:51	File folder	
FLASH	22.04.2020 10:51	File folder	
LIN	22.04.2020 10:51	File folder	
MPU	22.04.2020 10:51	File folder	
MPU_AllRegions	22.04.2020 10:51	File folder	
MPU_StackCheck	22.04.2020 10:51	File folder	
POWER	22.04.2020 10:51	File folder	
PVG	22.04.2020 10:51	File folder	
PWD	22.04.2020 10:51	File folder	
PWM	22.04.2020 10:51	File folder	
RTC	22.04.2020 10:51	File folder	
UART	22.04.2020 10:51	File folder	
VOUT	22.04.2020 10:51	File folder	
_build_all.bat	22.04.2020 10:51	Windows Batch File	1 KB
_clean_all.bat	22.04.2020 10:51	Windows Batch File	1 KB

(a) Contents of the **examples** directory

Name	Date modified	Type	Size
bsp	22.04.2020 10:51	File folder	
build	22.04.2020 10:51	File folder	
env	22.04.2020 10:51	File folder	
inc	22.04.2020 10:51	File folder	
lib	22.04.2020 10:51	File folder	
src	22.04.2020 10:51	File folder	
flash.cmm	22.04.2020 10:51	CMM File	4 KB
make.exe	22.04.2020 10:51	Application	174 KB
Makefile	22.04.2020 10:51	File	6 KB

(b) Contents of the **template** directory

Figure 5.9: The **examples** and **template** directories

The template should help you getting started. Every new project should start with a copy of this folder as base. The **template** directory contains several subdirectories that are necessary for a project:

- **bsp**: Subdirectory **bsp** (Board Support Package) contains files for the linker and board specific objects.
- **build**: Subdirectory **build** is an empty folder in which the generated files are put during the compile process.
- **env**: Subdirectory **env** (Environment) contains auxiliary tools like these two:
  - File `AddAPDB.exe` patches the `.hex` files with the CRC checksum, application size and build date.
  - File `nowECC.exe` generates a second file which contains the corresponding ECC data if the Lauterbach Device is used for flashing the device.
- **inc**: Subdirectory **inc** (Include) contains all the header files that are necessary to create a project with the C programming language.
- **lib**: Subdirectory **lib** (Libraries) contains the libraries `ttc500.lib` (for the hardware driver) and `bsp.lib` (for the startup code).
- **src**: Subdirectory **src** (Source) contains the source code files to create a project with the C programming language.

## 5.5 Development Environment

TTControl GmbH recommends the **TI Code Composer Studio (TI CCS)** as your primary development environment.

## 5.6 Settings.mk

The `Settings.mk` is needed to tell the `Makefile` what settings to use. Some basic settings can be modified in this file.

```

# TTC-Downloader hardware type
DOWNLOADER_HW_TYPE = 0x00400807

# Bootloader version
BOOTLOADER_VERSION = 3.0

# path with C compiler
ifndef C_COMP_PATH
    C_COMP_PATH = C:\TI\ccsv6\tools\compiler\arm_5.1.6\bin
endif

#-----
# optimization / debugging features
#-----
# compile code for profiling or debugging
#   0 ... profiling
#   1 ... debugging
#   2 ... release
OPT_TYPE = 1

ifeq ($(OPT_TYPE), 0)
    # optimize for profiling
    OPT_STR := PROFILING
endif
ifeq ($(OPT_TYPE), 1)
    # optimize for debugging
    OPT_STR := DEBUGGING
endif
ifeq ($(OPT_TYPE), 2)
    # optimize for release
    OPT_STR := RELEASE
endif

```

Figure 5.10: Settings.mk

- DOWNLOADER\_HW\_TYPE tells AddAPDB.exe for which HW type to compile.
  - 0x00400807 compiles for a 1<sup>st</sup> generation<sup>1</sup> **HY-TTC 580**.
  - 0x00600807 compiles for a 1<sup>st</sup> generation<sup>1</sup> **HY-TTC 540**.
  - 0x00A00807 compiles for a 1<sup>st</sup> generation<sup>1</sup> **HY-TTC 520**<sup>3</sup>.
  - 0x00C00807 compiles for a 1<sup>st</sup> generation<sup>1</sup> **HY-TTC 510**.
  - 0x00201007 compiles for a 2<sup>nd</sup> generation<sup>2</sup> **HY-TTC 580**.
  - 0x00402007 compiles for a 2<sup>nd</sup> generation<sup>2</sup> **HY-TTC 540**.
  - 0x00602007 compiles for a 2<sup>nd</sup> generation<sup>2</sup> **HY-TTC 520**<sup>3</sup>.
  - 0x00802007 compiles for a 2<sup>nd</sup> generation<sup>2</sup> **HY-TTC 510**.
  - 0x00201C07 compiles for a **HY-TTC 590**.
  - 0x00401C07 compiles for a **HY-TTC 590E**<sup>3</sup>.
  - 0x00201807 compiles for a **HY-TTC 508**.

- `C_COMP_PATH` tells the Makefile what compiler to use. **TI ARM Code Generation** tools are the only ones recommended by TTControl GmbH.  
**Note:** Make sure the path is the one you selected as installation folder for the installation of TI CCS.
- `OPT_TYPE` configures the compiler options to predefined values, for code optimizations and debugging symbols. Select **1** for **debugging** and **2** for a **release build**. If any other compiler or linker options are needed, please refer to the *TI ARM Code Generation Tools manuals* [2] and [3].

Additionally all the paths for the different folders are specified in `Settings.mk`. They must be adjusted if any of the folders are moved.

## 5.7 Application Examples

This section shows a basic example. Section 5.7.1 on this page shows how to build the application with CCS. Section 5.7.2 on page 27 shows the code with some overview comments about the code structure. For more information, see Section 5.7.3 on page 30, Section 5.7.4 on page 30, and Section 5.7.5 on page 32.

### 5.7.1 Build Application

1. Open the Code Composer Studio.
2. Click **File > New > Project...** to open the **New Project** dialog (see Figure 5.11 on the next page).
3. On the **Select a wizard** page, select **Makefile Project with Existing Code** (see Figure 5.11 on the facing page) and then click **Next**.
4. On the **Import Existing Code** page, fill the fields as follows (see Figure 5.12 on the next page):
  - for **Project Name**, enter any name (HY-TTC\_500\_Demo in our example)
  - for **Existing Code Location**, select the template folder as shown in Figure 5.13 on page 26
  - for **Languages**, select **C** and **C++**
  - for **Toolchain for Indexer Settings**, select **<none>**

Then click **Finish**.

5. From the menu, select **Project > Build All**. The **Console** pane opens and shows the build process (see Figure 5.14 on page 26).
6. After successful building, the **Console** pane shows **No errors** (see Figure 5.14 on page 26).

<sup>1</sup>Product version ≤ 01.05.

<sup>2</sup>Product version ≥ 01.08.

<sup>3</sup>customer-specific variant only

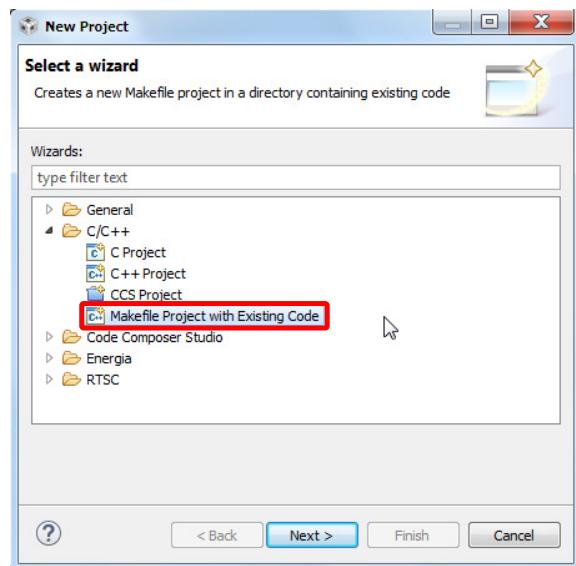


Figure 5.11: Select new wizard page

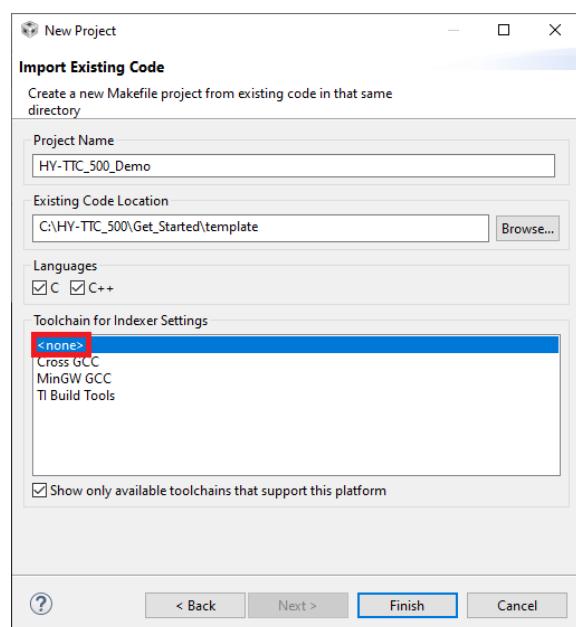
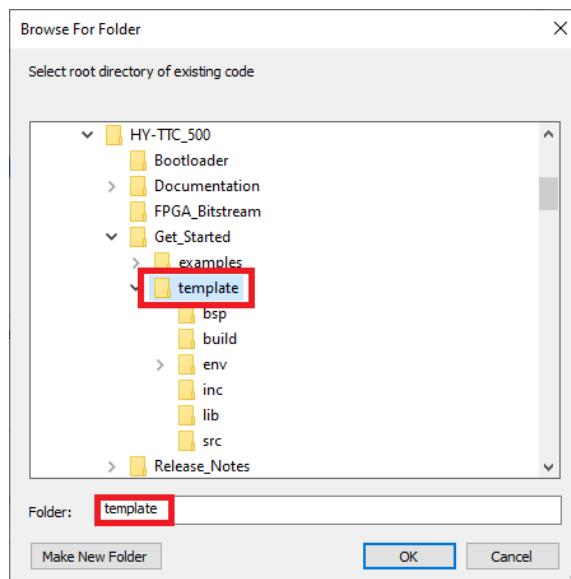
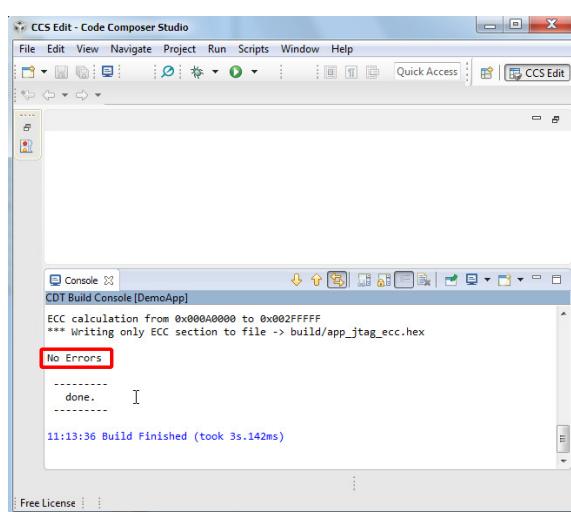


Figure 5.12: Import Existing Code page

Figure 5.13: **Browse For Folder** dialogFigure 5.14: **Console** pane with successful build

### 5.7.2 General

Every program must include at least the header files `IO_Driver.h` and `APDB.h` (see Figure 5.15 on the current page). The Application Descriptor Block (APDB) is a 128-byte structure that is used by download tools and the bootloader to update the application and do CRC checks. The structure `Apdb_t` must be defined (see Figure 5.16 on the following page).

```
*****  
* Includes  
*****  
#include "APDB.h"  
#include "IO_Driver.h" } Main include files for I/O driver and APDB  
#include "IO_DIO.h" } Other includes  
#include "IO_POWER.h"  
#include "IO_RTC.h"
```

Figure 5.15: Includes

```

/*
 * Application Descriptor Block,
 * needed for TTC-Downloader
 ****

#pragma SET_DATA_SECTION(".APDB_SEC")
volatile const BL_APDB Apdb_t =
{
    0,                                     /* ubyte4 APDBVersion; */          */
    { 0 },                                  /* BL_T_DATE FlashDate; */         */
    {
        (ubyte4) (
            (((ubyte4) RTS_TTC_FLASH_DATE_YEAR ) & 0x0FFF) << 0) |
            (((ubyte4) RTS_TTC_FLASH_DATE_MONTH ) & 0x000F) << 12) |
            (((ubyte4) RTS_TTC_FLASH_DATE_DAY   ) & 0x001F) << 16) |
            (((ubyte4) RTS_TTC_FLASH_DATE_HOUR ) & 0x001F) << 21) |
            (((ubyte4) RTS_TTC_FLASH_DATE_MINUTE) & 0x003F) << 26)
    },
    0,                                     /* BL_T_DATE BuildDate; */          */
    0,                                     /* ubyte4 NodeType; */              */
    0,                                     /* ubyte4 CRCStartPosition; */       */
    0,                                     /* ubyte4 CodeSize; */              */
    0,                                     /* ubyte4 LegacyApplicationCRC; */  */
    0,                                     /* ubyte4 ApplicationCRC; */        */
    1,                                     /* ubyte4 NodeNumber; */            */
    0,                                     /* ubyte4 CRCSeed; */              */
    0,                                     /* ubyte4 Flags; */                */
    0,                                     /* ubyte4 Hook1; */                */
    0,                                     /* ubyte4 Hook2; */                */
    0,                                     /* ubyte4 Hook3; */                */
    APPL_START,                            /* ubyte4 MainAddress; */           */
    { 0, 1 },                             /* BL_T_CAN_ID CANDownloadID; */   */
    { 0, 2 },                             /* BL_T_CAN_ID CANUploadID; */     */
    0,                                     /* ubyte4 LegacyHeaderCRC; */      */
    0,                                     /* ubyte4 ApplicationVersion; */   */
    500,                                   /* ubyte4 CANBaudrate; */           */
    0,                                     /* ubyte4 CANChannel; */            */
    0,                                     /* ubyte4 Password; */              */
    0,                                     /* ubyte4 MagicSeed; */             */
    { 10, 120, 30, 200 },                 /* ubyte1 TargetIPAddress[4]; */    */
    { 255, 255, 0, 0 },                  /* ubyte1 SubnetMask[4]; */          */
    { 239, 0, 0, 1 },                   /* ubyte1 DLMulticastIPAddress[4]; */
    0,                                     /* ubyte4 DebugKey; */              */
    0,                                     /* ubyte4 ABRDTimeOut; */           */
    0xFF,                                  /* ubyte1 ManufacturerID; */        */
    0,                                     /* ubyte1 ApplicationID; */         */
    0,                                     /* ubyte2 Reserved; */              */
    0,                                     /* ubyte4 HeaderCRC; */             */
};

#pragma SET_DATA_SECTION()
}

```

Figure 5.16: Application Descriptor Block (APDB)

The first step is to initialize the IO\_Driver in the main() function. After that, every channel, function or interface can be initialized with individual parameters (Figure 5.17 on the current page).

```

*****
 * Main Task
 ****
#pragma TASK( main );
void main(void)
{
    volatile IO_ErrorType io_error = IO_E_OK;
    ubyte4 timestamp = 0;
    ubyte2 pwm0_current = 0;
    bool pwm0_fresh = FALSE;

    /* safety configuration */
    IO_DRIVER_SAFETY_CONF safety_conf;

    /* 10ms cycle period with 25% window size,
     * no resets,
     * 30ms glitch filter,
     * no callbacks
     */
    safety_conf.command_period      = 10000;
    safety_conf.windows_size        = SAFETY_CONF_WINDOW_SIZE_25_PERCENT;
    safety_conf.reset_behavior      = SAFETY_CONF_RESETS_DISABLED;
    safety_conf.glitch_filter_time = 30;
    safety_conf.error_callback      = NULL;
    safety_conf.notify_callback     = NULL;

    /* initialize I/O driver with safety configuration */
    io_error = IO_Driver_Init(&safety_conf); } 1) Initialize I/O driver

    /* setup a PWM output with current measurement */
    io_error = IO_PWM_Init( IO_PWM_00 /* PWM channel 0 */           */
                           , 100      /* frequency is 100Hz */          */
                           , TRUE     /* positive polarity */         */
                           , FALSE    /* no diagnostic margin */       */
                           , NULL     /* not safety critical */        );
} } 2) Initialize I/Os

    /* turn on power stage */
    io_error = IO_POWER_Set( IO_INT_POWERSTAGE_ENABLE
                           , IO_POWER_ON
                           );
} } 3) Enable outputs

    /* turn on safety switch 0 */
    io_error = IO_POWER_Set( IO_INT_SAFETY_SW_0
                           , IO_POWER_ON
                           );
} } 4) Start timestamp

    /* start the RTC */
    IO_RTC_StartTime(&timestamp);
}

```

Figure 5.17: Application initialization

After initialization, typically a while loop (**while (1)**) is executed. This is the main loop of the application, and it will be executed each cycle time ms. See Figure 5.18 on this page.

```

/* main loop */
while (1)
{
    /* task begin */
    io_error = IO_Driver_TaskBegin();                                } 1) Call TaskBegin

    /* set PWM duty cycle */
    io_error = IO_PWM_SetDuty( IO_PWM_00 /* PWM channel 0 */           */
        , 0x8000 /* set duty cycle to 50% */                         */
        , NULL /* duty cycle feedback measurement ignored */          */
        , NULL /* period feedback measurement ignored */             */

    io_error = IO_PWM_GetCur( IO_PWM_00 /* PWM channel 0 */           */
        , &pwm0_current /* variable to store the current value */      */
        , &pwm0_fresh /* variable to store the freshness bit */       );

    if (pwm0_fresh != FALSE)
    {
        /* pwm0_current contains the latest current information for PWM channel 0 */
    }

    /* task end */
    io_error = IO_Driver_TaskEnd();                                    } 3) Call TaskEnd

    /* wait until the configured cycle time is over */
    while (IO_RTC_GetTimeUS(timestamp) < safety_conf.command_period);
        timestamp += safety_conf.command_period;
    }

} /* END OF main */                                              } 4) Wait until application cycle time has passed
                                                               and update the timestamp

```

Figure 5.18: Application loop

Be aware that the duration of each cycle is 10000 µs in this case. See also Figure 5.17 on the previous page, parameter `safety_conf.command_period`.

### 5.7.3 Help for C Driver Functions

The I/O driver API documentation (HY-TTC\_500\_IO\_Driver\_Manual\_V3.1.0.chm [4]) is available in the compiled HTML format and can be found in **Documentation > Manuals**. It provides a short overview of the available I/O driver functions as well as detailed descriptions of those functions, including parameters, return values and examples.

### 5.7.4 Linking Constant Data

Constant data is linked to the application region by default. However, it can also be linked to the external flash memory (if available) or to the application configuration data region. The major difference is the integrity protection of their respective contents. While constant data linked to the external flash memory is protected by the application CRC, the application configuration data region's integrity must be ensured by the application itself. Figure 5.19 on the facing page depicts an example of how to link constant data to the external flash memory as well as to the application configuration data region.

```

/***********************
 * Global data
***********************/

#pragma SET_DATA_SECTION(".EXT_FLASH")
/*
 * After setting the active data section to ".EXT_FLASH", the linker places
 * ALL of the subsequently defined variables to the external flash memory.
 *
 * Attention: Be aware that the constant data linked to the external flash
 * memory is protected by the application CRC! Thus, any modification
 * will lead to an invalid application CRC.
 */
const ubyte1 foo = 'f';      /* foo is linked to external flash      */
const ubyte4 bar = 123456;   /* bar is also linked to external flash */

#pragma SET_DATA_SECTION(".CFG_FLASH")
/*
 * After setting the active data section to ".CFG_FLASH", the linker places
 * ALL of the subsequently defined variables to the configuration flash memory.
 *
 * Attention: Be aware that the content of the configuration flash memory is
 * NOT protected by the application CRC! This means that the data
 * integrity of the configuration flash memory must be ensured by
 * the application itself.
 */
const ubyte2 wayne = 256; /* wayne is linked to configuration flash */

#pragma SET_DATA_SECTION()
/*
 * After placing the respective global variables to the external and
 * configuration flash memory, the active data section needs to be reset
 * to its default value.
 */

```

Figure 5.19: Linking constant data to the external flash and/or application configuration data

### 5.7.5 Safety Configuration

For safety-critical applications, every safety-critical IO pin can be initialized with a safety configuration. Please refer to [HY-TTC\\_500\\_Series\\_C\\_API\\_Manual.chm](#) [4] for further details.

To enable any outputs, the I/O Driver has to be configured with a valid safety configuration. The I/O Driver safety configuration consists of several elements:

- `command_period [μs]`: Sets the period at which the application cycle is executed.  
**Important:** The `command_period` is checked by the watchdog and has to meet the window configuration.
- `window_size [%]`: Configures the size of the window for the watchdog which checks the `command_period`. The application cycle has to fulfill those timing requirements.
- `reset_behavior`: Configures how often the watchdog can restart the CPU in case of an error. If a safety-critical error occurs, the watchdog will reset the CPU and start again. After the maximum number of resets, a permanent safe state will be entered.
- `glitch_filter_time [ms]`: Sets the time a temporary error condition must persist to cause an error reaction by the safety function.
- `error_callback`: Configures the error callback for the application. It can be set to `NULL` to disable the error callback.
- `notify_callback`: Configures the notification callback for the application. It can be set to `NULL` to disable the notification callback.

### 5.7.6 Debugging of a safety-critical Application

The `DebugKey` field of the APDB can be set to `0xC0FFEE` to enable debugging of a safety-critical I/O Driver. This can be done directly in the definition of the APDB ([Figure 5.16 on page 28](#)).

## 5.8 Flashing the HY-TTC 500

There are two ways how to flash a HY-TTC 500 device: Using the Lauterbach Debugging Device or the TTC-Downloader Tool via CAN or Ethernet. Both ways will be described step by step. For further information on each product, please refer to the respective manual.

### 5.8.1 The Lauterbach Debugging Device and Trace32

#### 5.8.1.1 Overview

The Lauterbach product TRACE32-ICD supports a wide range of on-chip debug interfaces. The hardware for the debugger is universal and allows interfacing different target processors by simply changing the debug cable and the software.

#### 5.8.1.2 Installation

The installation is started automatically or by executing `setup.bat` on the installation DVD.



Figure 5.20: Lauterbach Base Station

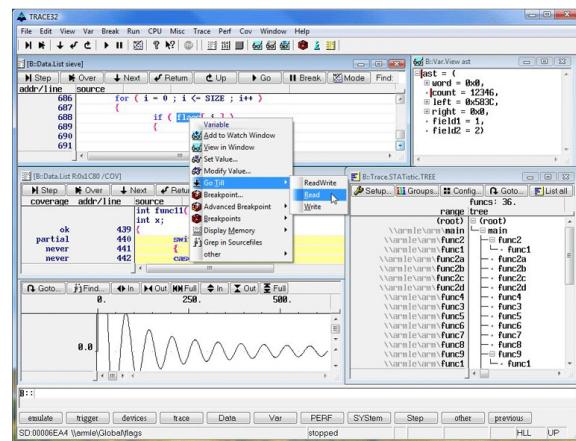


Figure 5.21: Trace32 Software

The installation wizard guides you through the installation process:

1. After the Welcome page, confirm the licensing agreements on the **License Software Agreement** page.
2. On the **Choose Destination Location** page, set the installation path (see Figure 5.22 on this page).

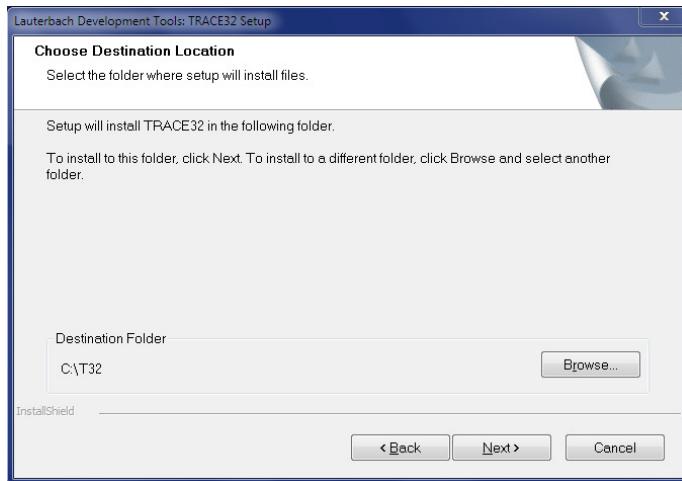


Figure 5.22: Choose Destination Location page

3. On the **Installation Type** page, select **Custom Installation** (see Figure 5.23 on the next page).
4. On the **Setup Type** page, select **New Installation** (see Figure 5.24 on the following page).
5. On the **TRACE32 Product Type** page, select **Debugger** (see Figure 5.25 on page 35).
6. On the **Maintenance Notice** page and click **Next** (see Figure 5.26 on page 35).
7. On the **Debugger Interface Type** page, select **USB Interface** (see Figure 5.27 on page 36).

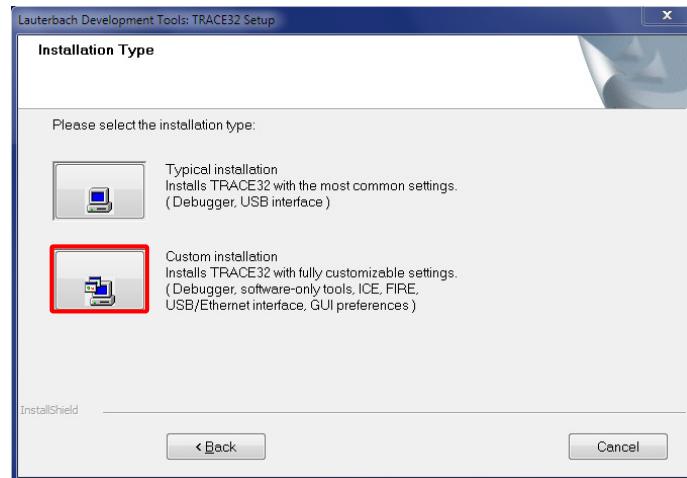


Figure 5.23: **Installation Type** page

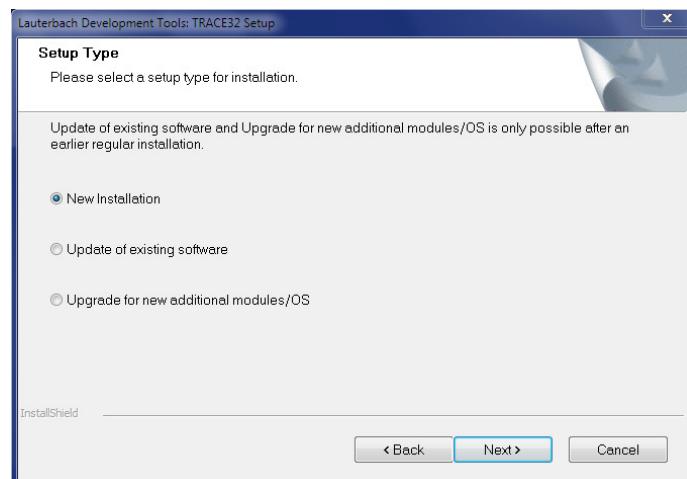


Figure 5.24: **Setup Type** page

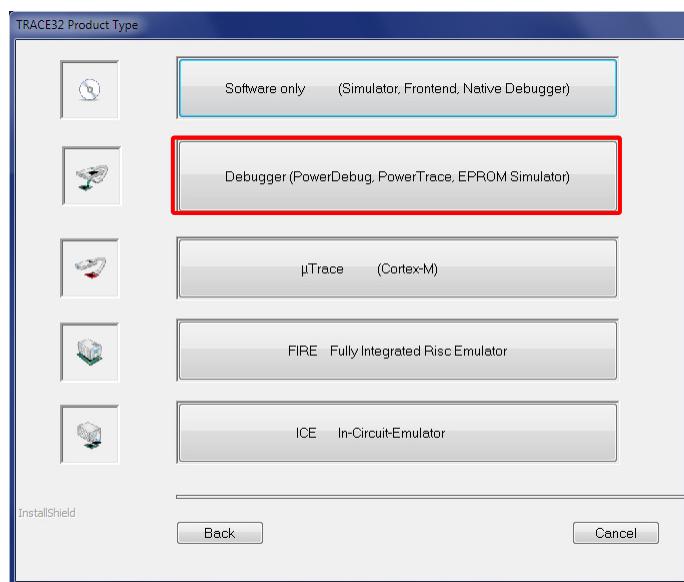


Figure 5.25: **TRACE32 Product Type** page

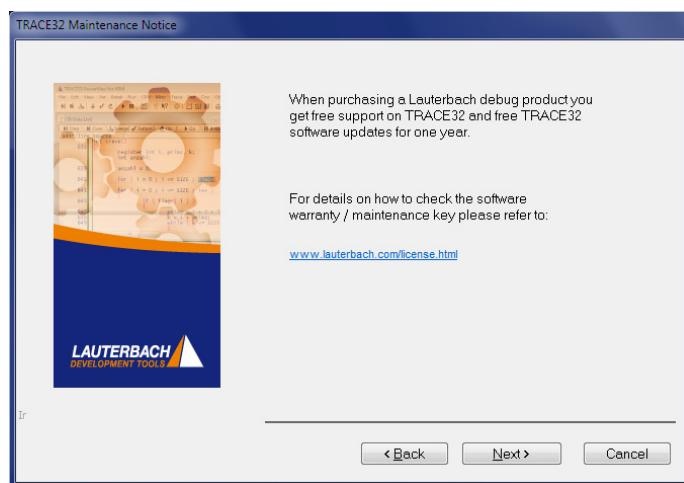


Figure 5.26: **Maintenance Notice** page

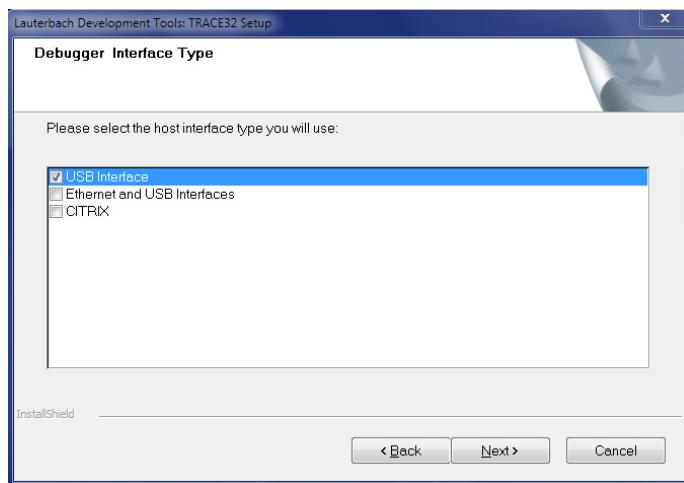


Figure 5.27: **Debugger Interface Type** page

8. On the **OS Selection** page, select **PC Windows XP/VISTA/7/8/10** (see Figure 5.28 on this page).

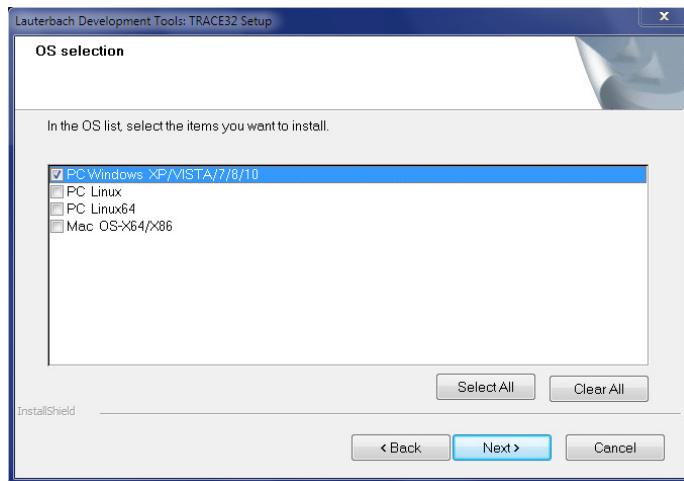


Figure 5.28: **OS Selection** page

9. On the **CPU selection** page, select **ICD ARM 32-bit** (see Figure 5.29 on the next page).
10. In the **TRACE32 executable type** dialog, click **Yes** for a 64-bit OS (see Figure 5.30 on the facing page).
11. On the **Environment variable T32ID** page, choose the default value **T32** (see Figure 5.31 on the next page).
12. If you are asked to install the Lauterbach device software, click **Install** (see Figure 5.32 on page 38). Use the suggested default settings for the device software installation process.
13. On the **Environment variable T32TMP** page, set the destination folder (see Figure 5.33 on page 38).

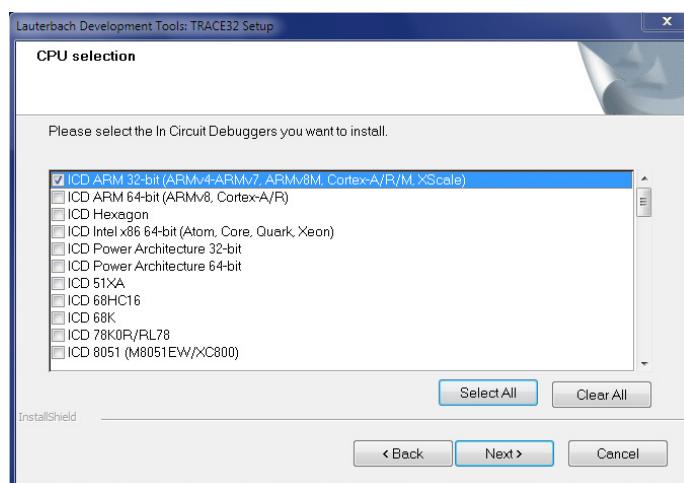


Figure 5.29: **CPU selection** page

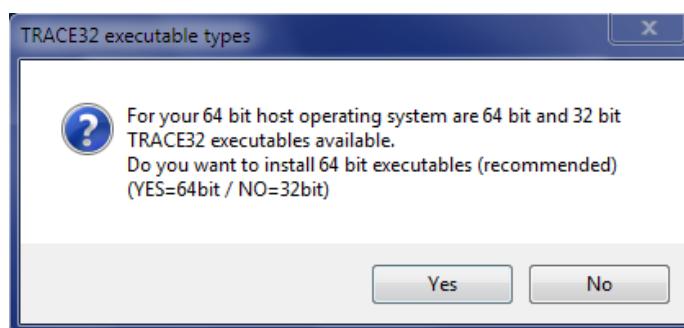


Figure 5.30: **TRACE32 executable type** dialog

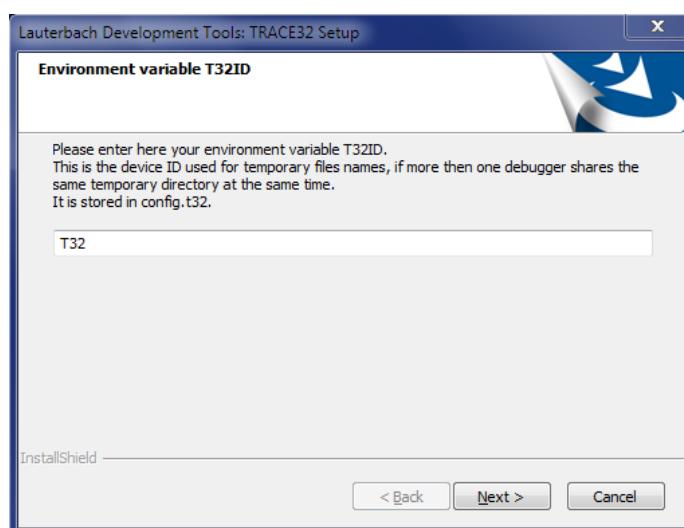


Figure 5.31: **Environment variable T32ID** page

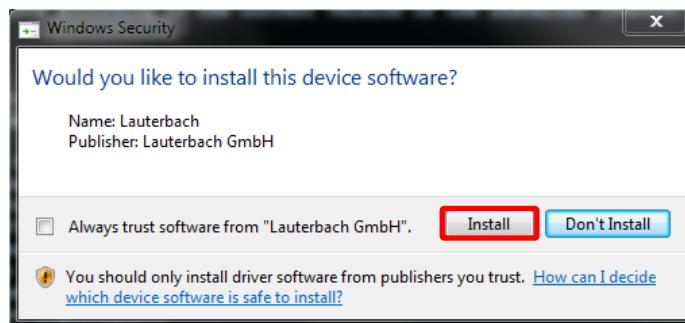


Figure 5.32: Device software installation

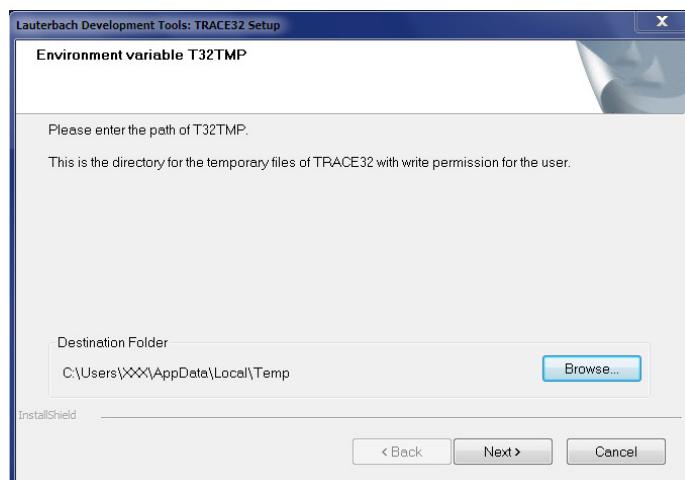


Figure 5.33: Environment variable T32TMP page

14. On the next pages related to screen configuration, use the default settings.
15. On the **Prepare TRACE32 for Integration with other products** page, select **No Integration** (see Figure 5.34 on this page).

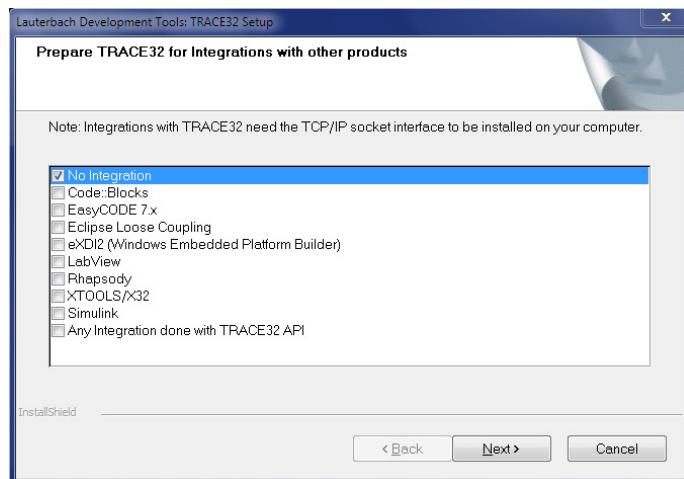


Figure 5.34: Prepare TRACE32 for Integration... page

16. For the pages **Folder Selection** and **Folder program group type**, use the default settings.
17. On the **Select way to submit registration** page, select **Register later** (see Figure 5.35 on the current page).

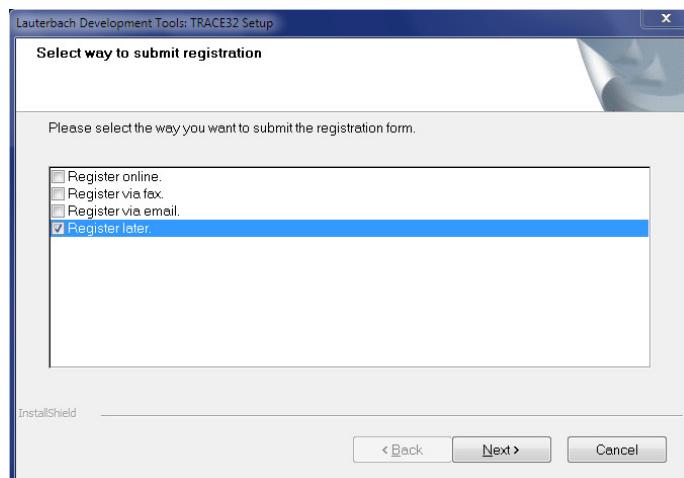


Figure 5.35: Select way to submit registration page

18. The last page **TRACE32 software is successfully finished** shows the installation path and how to start the software (see Figure 5.36 on the following page). Click **Finish** to complete the installation.

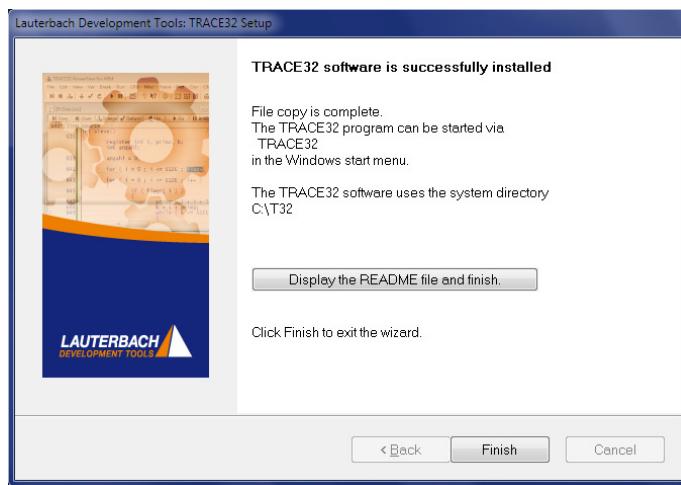


Figure 5.36: **TRACE32 software is successfully finished** page

### 5.8.1.3 Usage

#### 5.8.1.3.1 Hardware

The HY-TTC 580 Starter Kit comes with the following components:

- JTAG Adapter Board (Figure 5.37 on the current page) and JTAG Cable (Figure 5.38 on this page)



Figure 5.37: JTAG Adapter Board



Figure 5.38: JTAG Cable

- open housing of the HY-TTC 500 ECU to enable a connection with the JTAG interface (Figure 5.39 on the next page)

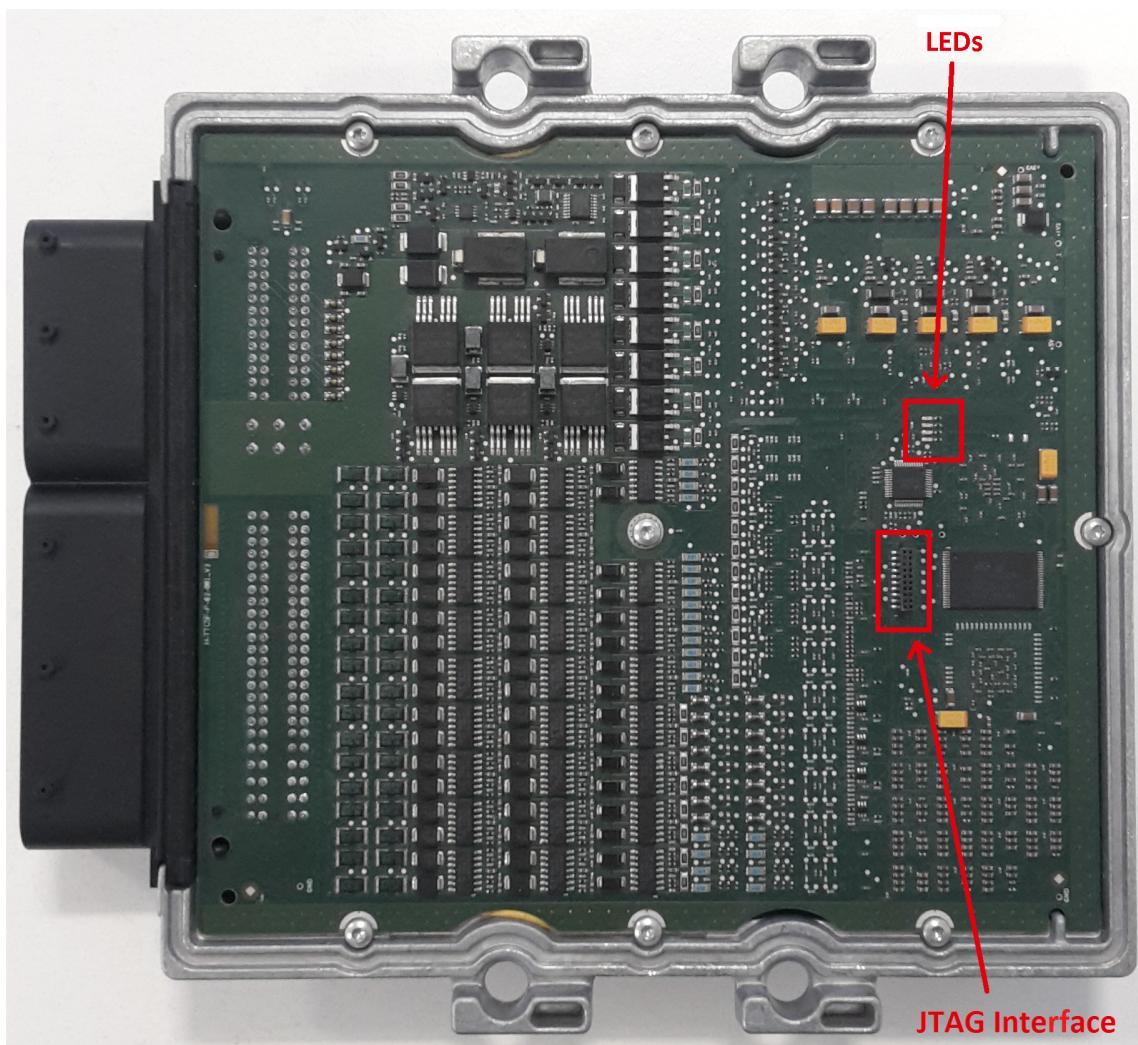


Figure 5.39: HY-TTC 500 with JTAG connector and LEDs

The following components from Lauterbach are required:

- Lauterbach Base Station. For example, **Power Debug Interface USB 3 LA-3500**
- Lauterbach Debug Cable **JTAG-CORTEX-A/R LA-7843**
- Lauterbach AC/DC power supply adapter
- USB connector to connect Lauterbach and PC

Please refer to Figure 5.40 and Figure 5.41 on the following page for how to connect, for example, a HY-TTC 580 with Lauterbach debugger.

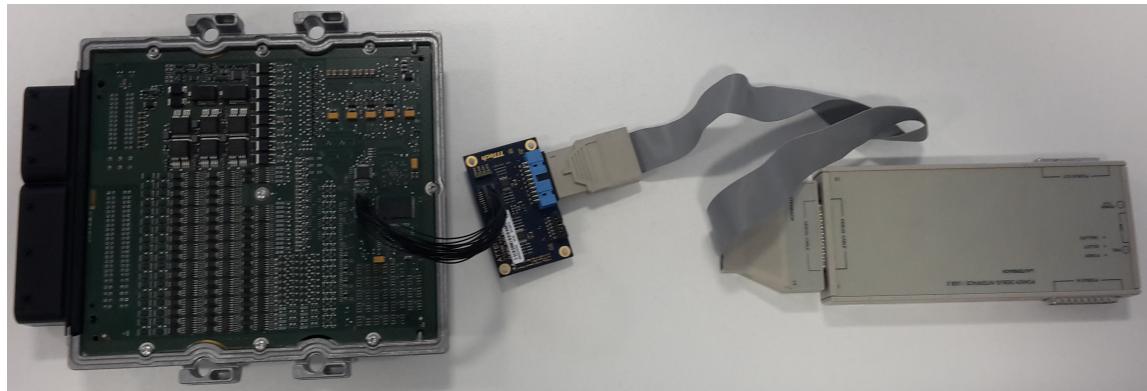


Figure 5.40: Lauterbach connection to HY-TTC 580 (1)

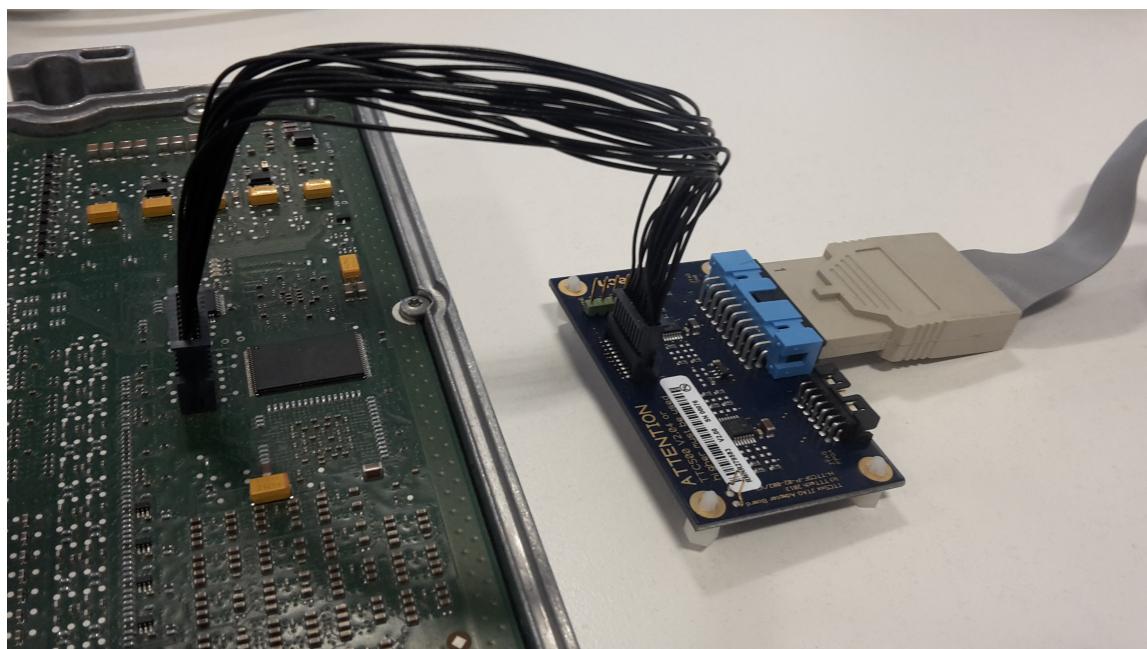


Figure 5.41: Lauterbach connection to HY-TTC 580 (2)

### 5.8.1.3.2 LED Description

#### DONE LED

The DONE LED (see Figure 5.42 on the current page) indicates the configuration status: If the DONE LED is ON, the FPGA is *not* configured. If the DONE LED is OFF, the FPGA is configured.

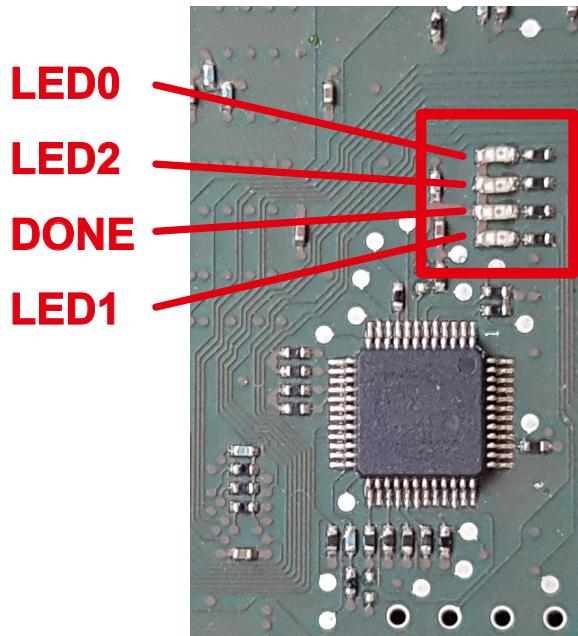


Figure 5.42: HY-TTC 500 with LEDs

#### Debug LEDs

The LEDs LED0...LED2 shown in Figure 5.42 on this page are for debugging. They are controlled by the `IO_DEBUG_SetOutputPin()` function. See [4] for details. They are completely controllable by the application.

#### Signal pins

The signal pins on the JTAG adapter board (see Figure 5.43 on the next page) indicate the inverted status of the DONE LED and the Debug LEDs. That is, if a LED is ON, the corresponding signal pin has low voltage (0).

**Note:** The signal pins shown in Figure 5.43 on the following page are unprotected CMOS outputs. Any external voltage applied on these pins can damage the ECU.



Figure 5.43: HY-TTC 500 with JTAG connector and signal pins

#### 5.8.1.3.3 Software

1. Connect the Lauterbach Base Station and Debug Cable to the PC and power supply and install the necessary drivers.
2. Make the connections as described in Section 4.3 on page 9 and in Section 5.8.1.3.1 on page 40.
3. Perform a power cycle.
4. Click **Start > Trace32 ICD ARM32 USB** on the Windows start menu to start the Lauterbach debugger software.
5. The HY-TTC 500 template comes with a predefined \*.cmm script. Start the script by clicking **File > Run Script** (Figure 5.44 on the current page).

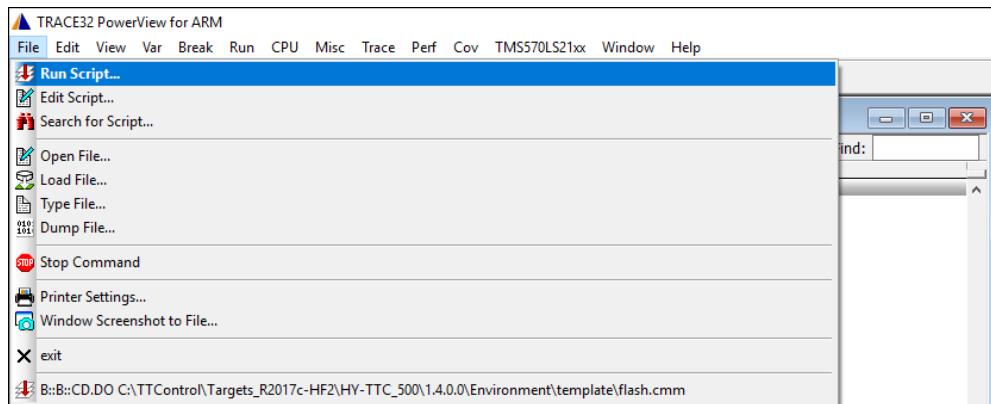


Figure 5.44: Trace32 and .cmm batch file

6. A dialog asks whether you want to flash the application or not (Figure 5.45 on the facing page). If you click **Yes**, the flashing process starts.
7. When the flashing process has finished, the next dialog asks whether you want to keep the current Trace32 window configuration or reset to a default configuration (Figure 5.46 on the next page).

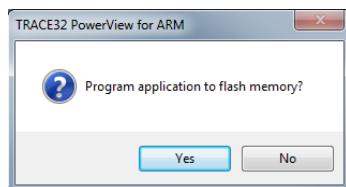


Figure 5.45: Flash application dialog

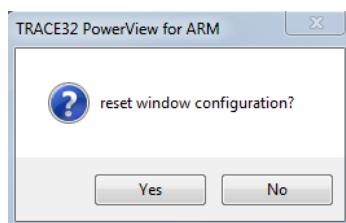


Figure 5.46: Reset Trace32 windows dialog

8. After the flashing procedure, you return to the Trace32 window (Figure 5.47 on the following page). Its appearance depends on the chosen windows configuration.

## 5.8.2 The TTC-Downloader Tool

### 5.8.2.1 Tool Download

You can download the TTC-Downloader tool from our Service Area:

1. Enter the **HY-TTC Downloader** page.
2. From there, enter the page with the latest release.
3. Download **Installer - TTC-Downloader <version>.zip**.
4. Extract the .zip file on your computer.

The tool comes with a documentation file help.chm.

### 5.8.2.2 Device Connection

A connection with the HY-TTC 500 can be established either via CAN or via Ethernet as described in Section 5.8.2.2.1 on the current page and Section 5.8.2.2.2 on the following page.

#### 5.8.2.2.1 Connecting via CAN

To connect to the device via CAN, use a CAN connector (e.g., **Peak PCAN-USB**) and connect it to **CAN0** of the HY-TTC 500.

Then power off the device and open the TTC-Downloader.

If the correct CAN settings are not known, then force the default connection settings by connecting the pin sensor supply 0 and sensor supply 1 of the HY-TTC 500 to ground. For variants without

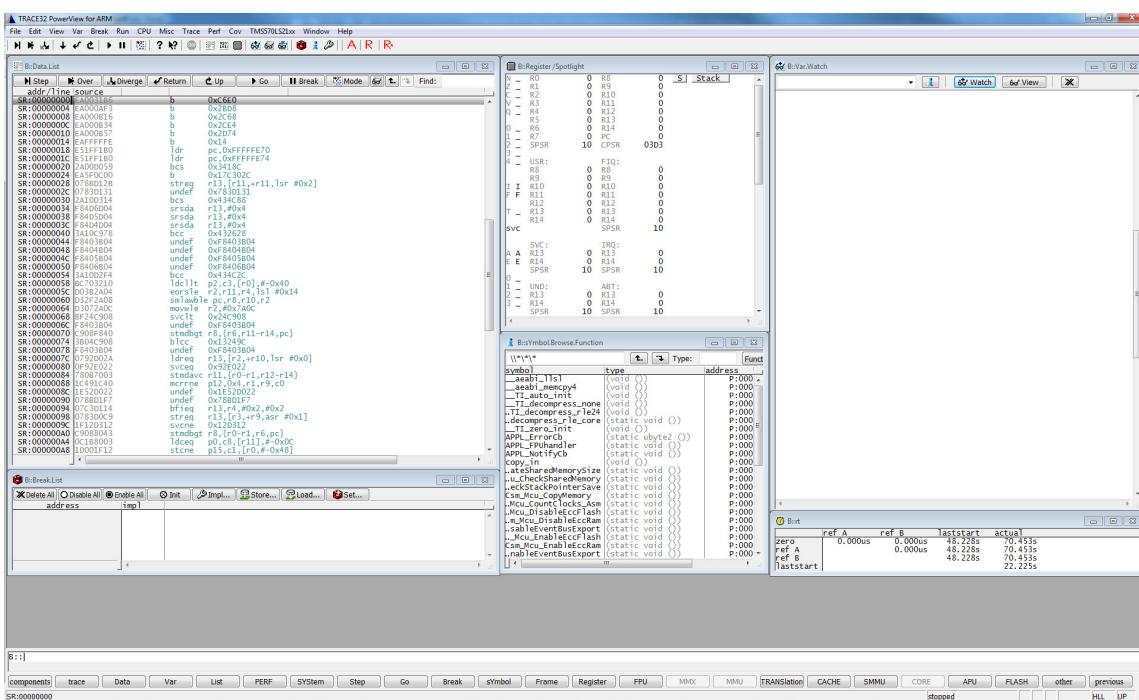


Figure 5.47: Trace32 Window after flashing

pin sensor supply 1, the default connection can be set by connecting the pin sensor supply 0 to ground. See the TTC-Downloader documentation `help.chm`, Section *Advanced Features > The CAN Fallback Mode* for the HY-TTC 500 variant specific default CAN channel.

**Note:** If you have changed the TTC-Downloader settings in previous applications of the HY-TTC 500, then make sure that you reset them to the default values.

To start connecting press **F2** and quickly power on the device, while the Downloader progress bar appears (see Figure 5.48(a) on the next page).

After successful connection, the downloader will identify the device and prepare the flashing process (see Figure 5.48(b) on the facing page).

### 5.8.2.2.2 Connecting via Ethernet

**Note:** For unexperienced users of the HY-TTC 500 it is recommended to use the CAN option. If you use Ethernet and an application is already flashed to the target, then you have to make sure that the application listens to download requests on the Ethernet. The example application of the Quick Start Guide does not listen to download requests.

To connect to the device via Ethernet, use the Ethernet port of your computer and connect it to the Ethernet port of the HY-TTC 500. Note that a point-to-point Ethernet connection is mandatory.

Open the TTC-Downloader.

The next step depends on the content of the flash memory:

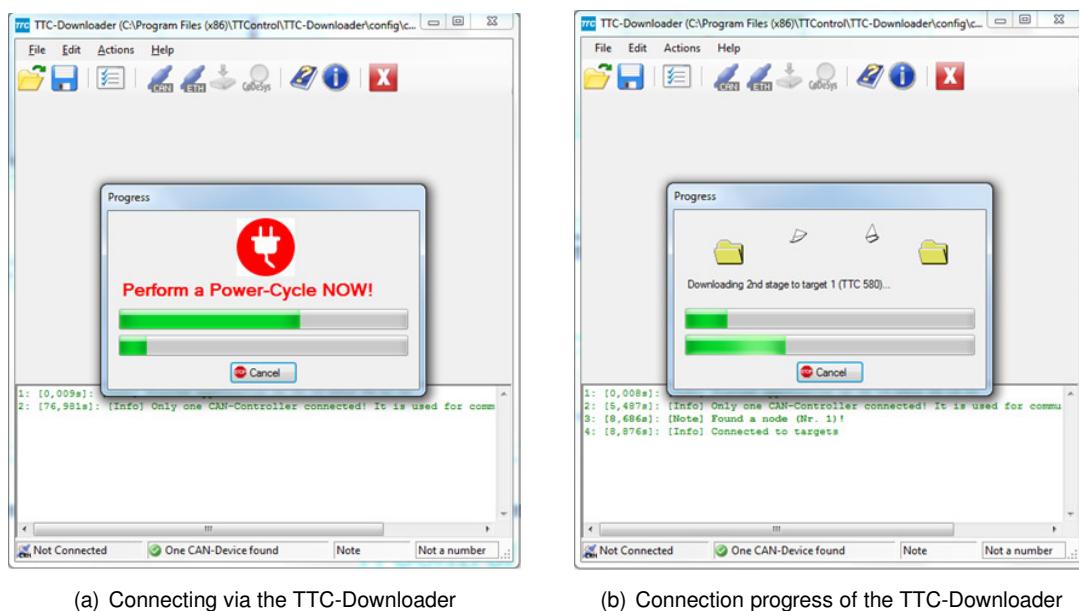


Figure 5.48: TTC-Downloader

- If the target has no application and no CODESYS runtime system flashed, then use the default connection settings. To set the default settings, open the **Preferences** dialog box (e.g. with **Ctrl + P**) and click **Default**.
  - If the target has an application or CODESYS runtime system flashed, then enter the correct Ethernet settings. If you do not know the correct Ethernet settings, please use CAN to connect with the target as described in Section 5.8.2.2.1 on page 45.
- To set the correct Ethernet settings, open the **Preferences** dialog box (e.g. with **Ctrl + P**), enter the values, and click **Apply**.

In the **Preferences** dialog box, open the **Ethernet** tab, select the used Ethernet adapter in the **Network Adapter** dropdown menu, and click **Apply**.

Close the **Preferences** dialog box.

Now power on the target.

Start the connection procedure by pressing F5. After successful connection, the downloader will identify the device and prepare the flashing process (see Figure 5.48(b) on this page).

### 5.8.2.3 Flashing to HY-TTC 500

After successful connection via CAN or Ethernet, you can open your .hex-file and flash the device with the Download icon in the **Application Download** group in the Node area. For further information about how to update the bootloader/FPGA IP, read/write from/to EEPROM, and other features of the TTC-Downloader, please refer to the documentation help.chm for the tool.

## 6 Memory Mapping

### 6.1 Memory Map for Internal Flash and RAM

The following table specifies the start address and size of the **bootloader**, **FPGA IP**, **APDB**, **application** and **application configuration data** for the HY-TTC 500 controllers. See also the file `mem_ttc500_b1.lsf`, which is delivered with the package.

**Note:** A compatible FPGA is delivered with the package.

**Note:** The application configuration data region is **not** protected by the application CRC and thus, its integrity must be ensured by the application itself. For detailed information about modifying the flash memory, refer to the documentation `help.chm` for the TTC-Downloader, page *Modifying Flash/EEPROM Memory*.

HY-TTC 500	Start Address	Size
Application Cfg. Data	0xF0200000	65536 Bytes
Application	0x000A0100	2490112 Bytes
APDB + CRC	0x000A0000	256 Bytes
FPGA IP	0x00020000	524288 Bytes
Bootloader	0x00000000	131072 Bytes

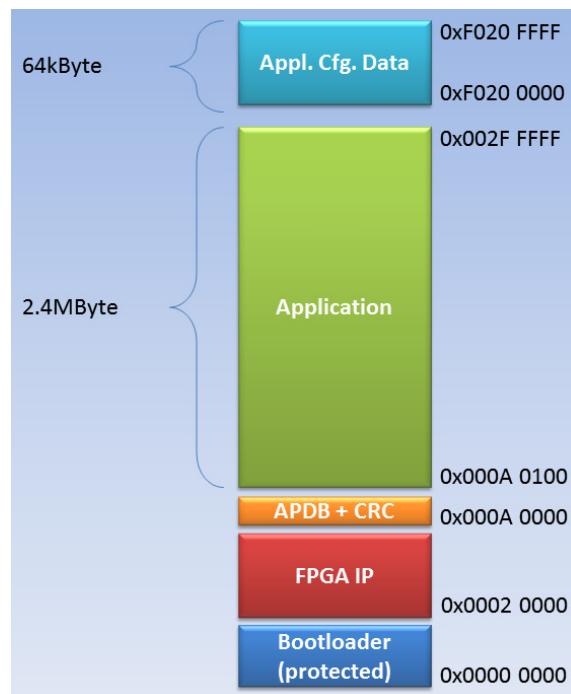


Figure 6.1: Memory map for HY-TTC 500 internal Flash

The following table specifies the start address and size of the **internal RAM** for the HY-TTC 500 controllers.

HY-TTC 500	Start Address	Size
Int. RAM	0x08003000	217088 Bytes

## 6.2 Memory Map for External Flash and RAM

The following tables specify the start address of the **external RAM** and **Flash** for the HY-TTC 500 controllers.

HY-TTC 580	Start Address	Size
Ext. RAM	0x60000000	2097152 Bytes
Ext. Flash	0x64000000	8388608 Bytes

HY-TTC 540	Start Address	Size
Ext. RAM	0x60000000	2097152 Bytes

HY-TTC 520	Start Address	Size
Ext. RAM	0x60000000	2097152 Bytes

HY-TTC 510	Start Address	Size
Ext. RAM	0x60000000	2097152 Bytes

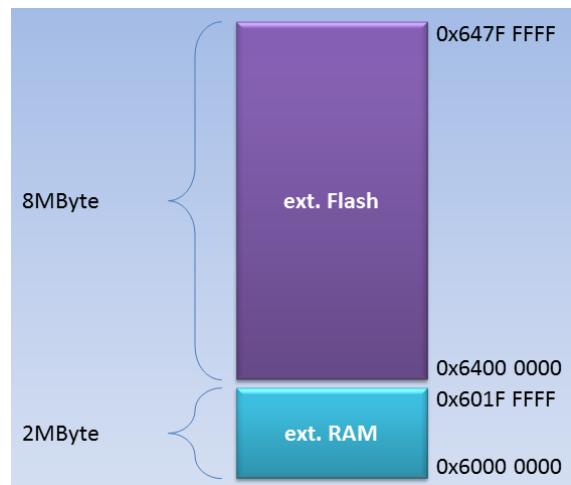


Figure 6.2: Memory map for HY-TTC 500 external RAM and Flash

## Bibliography

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- [4] TTControl GmbH. HY-TTC 500 I/O Driver Manual. S-TTC5F-G-20-001.
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