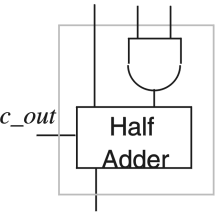


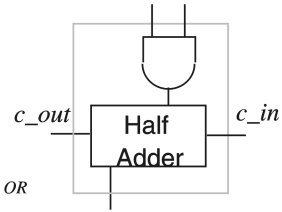
sum_in x_bit y_bit



sum_out

Module MH

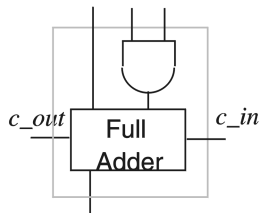
x_bit y_bit



sum_out

OR

sum_in x_bit y_bit



sum_out

Module MF

