

EE531 - TURMA S

Diodos

Laboratório de Eletrônica Básica I - Segundo Semestre de 2010

PROFESSOR: JOSÉ CÂNDIDO SILVEIRA SANTOS FILHO

DANIEL LINS MATTOS	RA: 059915
RAQUEL MAYUMI KAWAMOTO	RA: 086003
TIAGO CHEDRAOUI SILVA	RA: 082941

10 de setembro de 2010

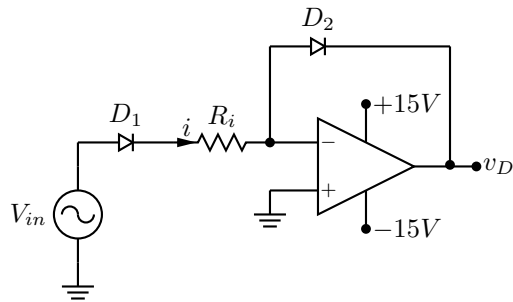


Figura 1: Circuito para caracterização V versus I do diodo

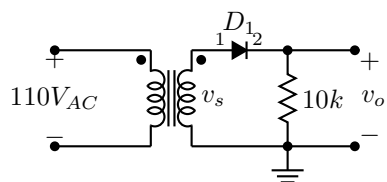


Figura 2: Circuito retificador de meia onda

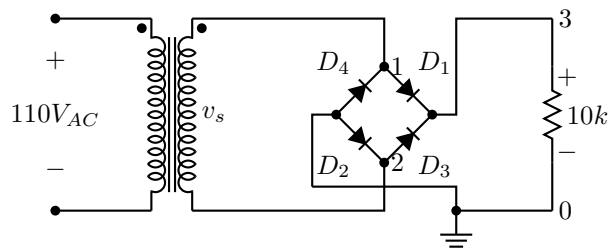


Figura 3: Circuito retificador de onda completa

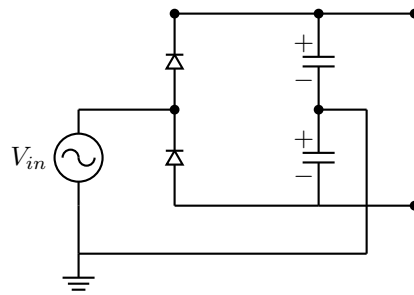


Figura 4: Duplicador de tensão

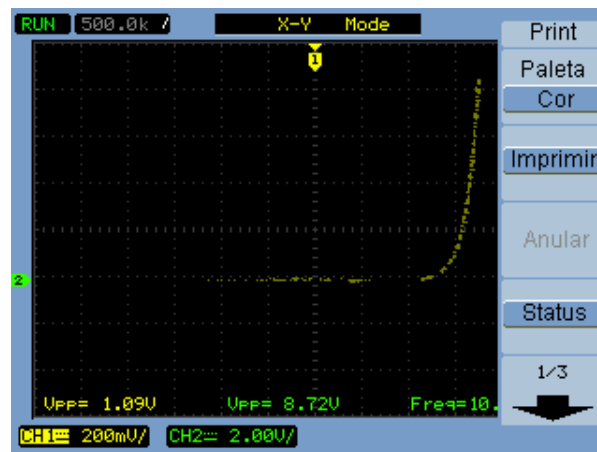


Figura 5: Curva característica V versus I do diodo

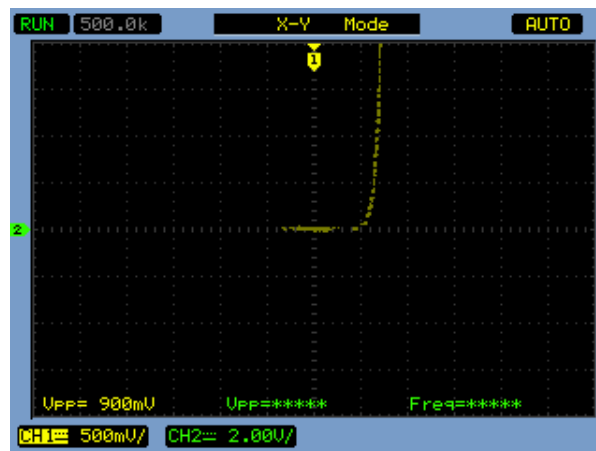


Figura 6: Curva característica V versus I do diodo



Figura 7: Curva de histerese

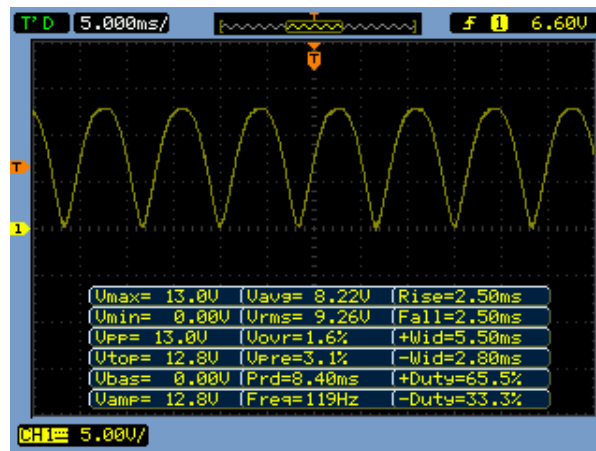


Figura 8: Medida da tensão no nó 3

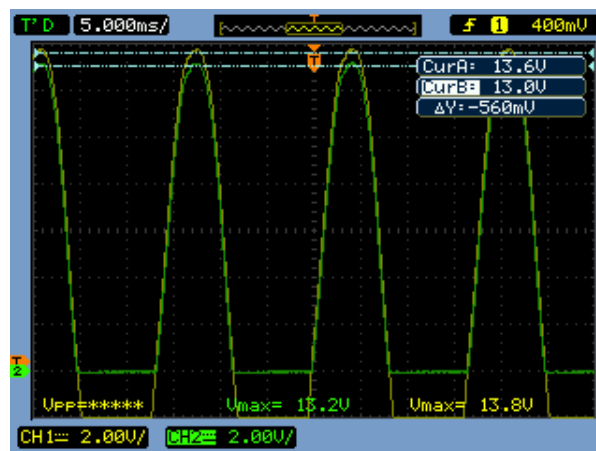


Figura 9: Medida da tensão diferencial entre os nós 2 e 3

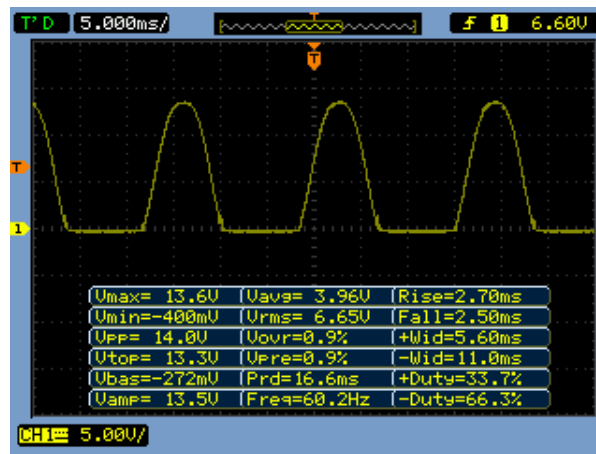


Figura 10: Medida da tensão no nó 1

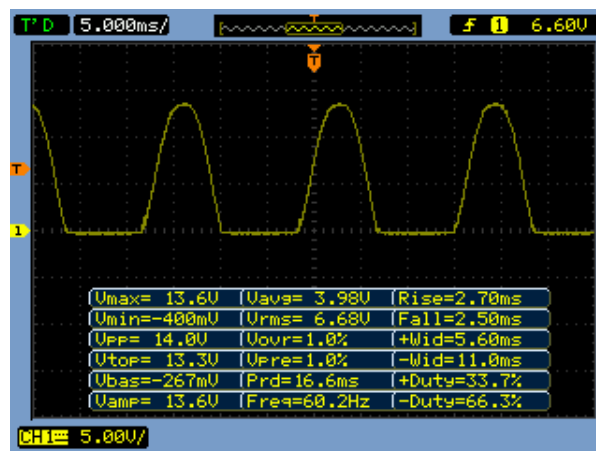


Figura 11: Medida da tensão no nó 2

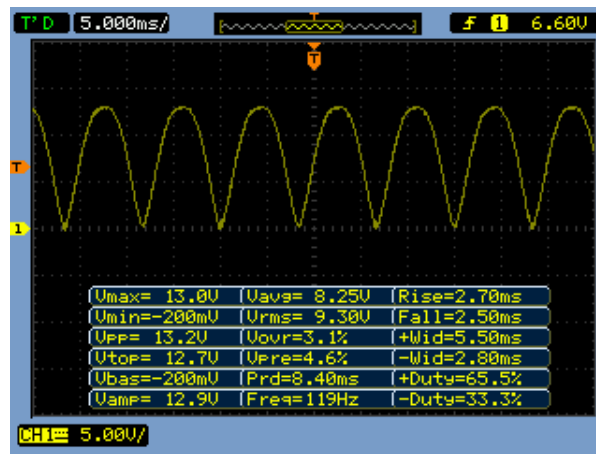


Figura 12: Medida da tensão diferencial entre os nós 2 e 3

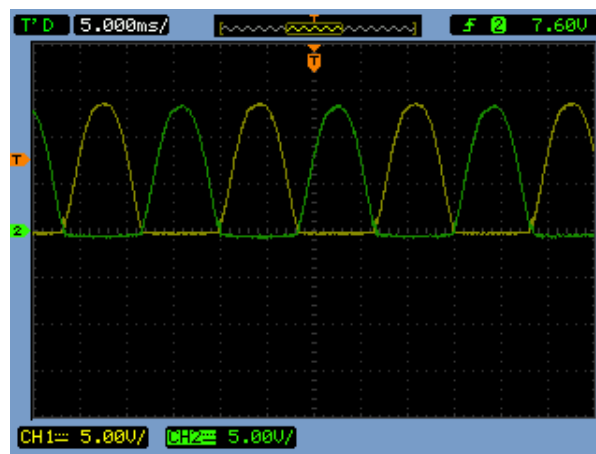


Figura 13: Medida da tensão nos nós 1 e 2

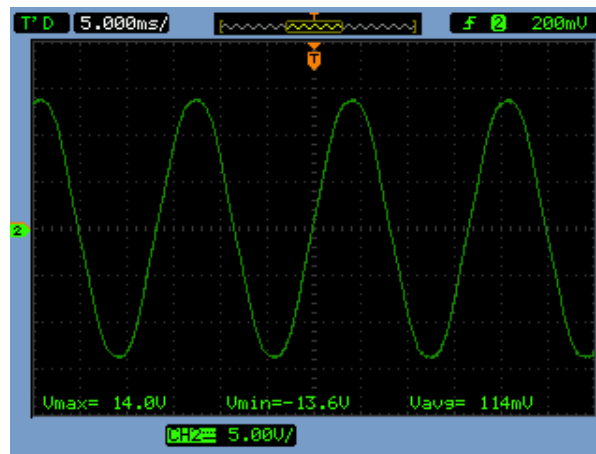


Figura 14: Medida da tensão diferencial entre os nós 2 e 3

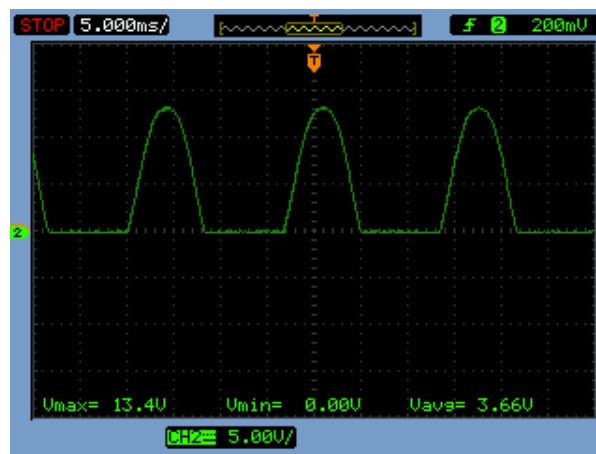


Figura 15: Medida da tensão diferencial entre os nós 2 e 3

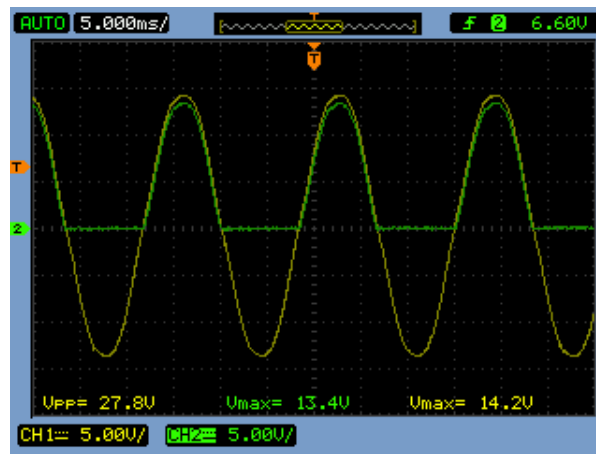


Figura 16: Medida da tensão diferencial entre os nós 2 e 3

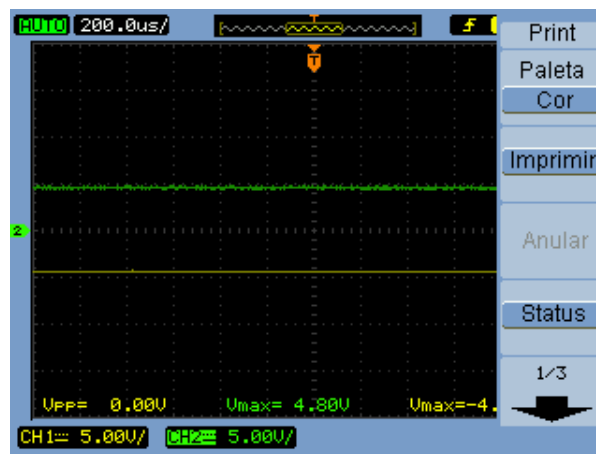


Figura 17: Medida da tensão diferencial entre os nós 2 e 3

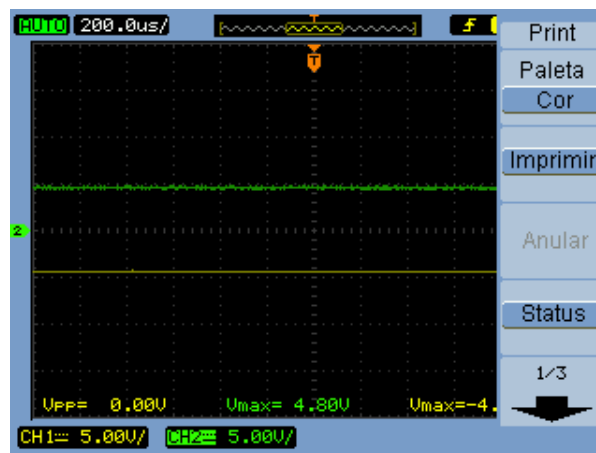


Figura 18: Tensão de saída nos nós 1 e 2

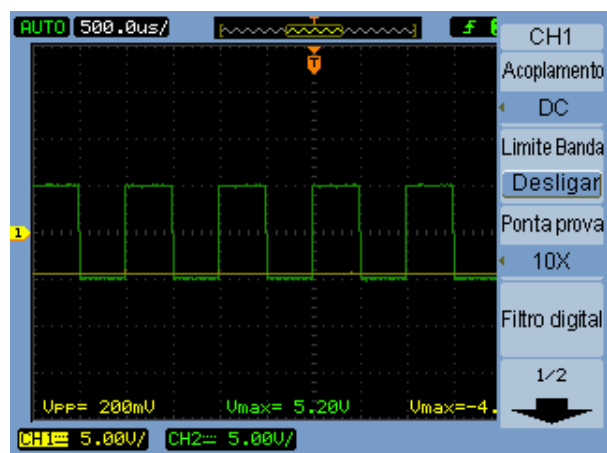


Figura 19: Análise do sinal de saída no nó x

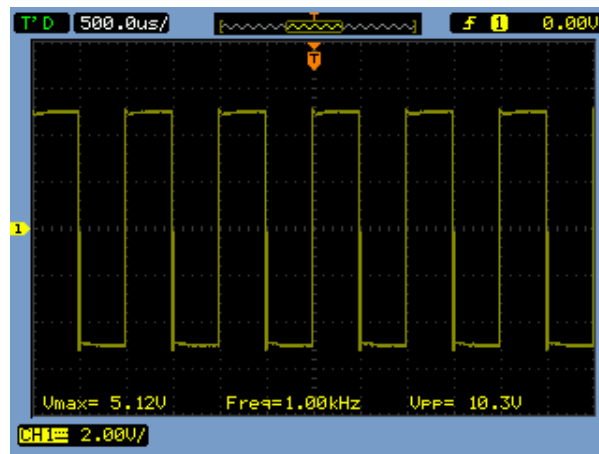


Figura 20: Onda de entrada do circuito

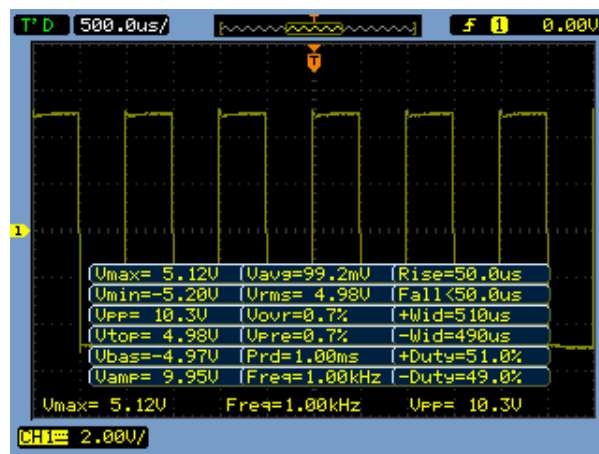


Figura 21: Medidas da onda de entrada do circuito

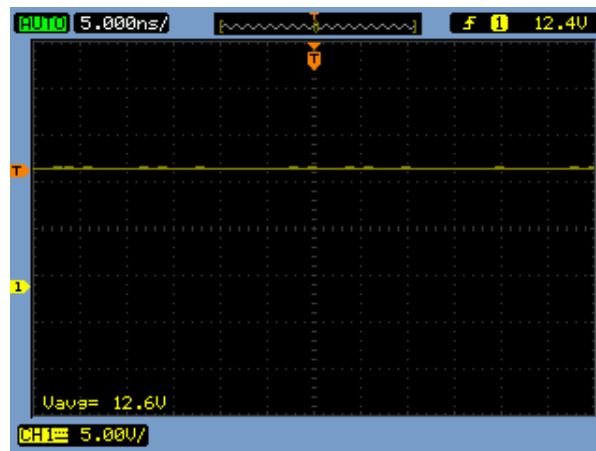


Figura 22: Característica onda após a inserção de capacitor em paralelo



Figura 23: Aproximação da imagem da onda após a inserção de capacitor em paralelo