

/home/tbt/workspace
/dpol/smsimulator5.5
/d_work/macros/dpol/EventGenerator
/GenInputRoot_np_atime.cc

```
graph TD; A["/home/tbt/workspace  
/dpol/smsimulator5.5  
/d_work/macros/dpol/EventGenerator  
/GenInputRoot_np_atime.cc"] --> B[fstream]; A --> C[iostream]; A --> D[sstream]; A --> E[vector]; A --> F[TFile.h]; A --> G[TTree.h]; A --> H[TBeamSimData.hh];
```

fstream

iostream

sstream

vector

TFile.h

TTree.h

TBeamSimData.hh