



UEFI & EDK II Training

EDK II Build Process and Environment

tianocore.org

LESSON OBJECTIVE

- ★ Define EDK II
- ★ Describe EDK II's elements including file extensions, directories, modules, packages, and libraries
- ★ Explain the EDK II build process
- ★ Explain the Build tools

EDK II OVERVIEW

The Edk II Infrastructure

PHILOSOPHY OF EDK II

Support
UEFI & PI
needs

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Support
UEFI & PI
needs

Separate tool
& source
code

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Package
Definition
File: DEC

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FLASH
Mapping Tool

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Move as
much code to
C

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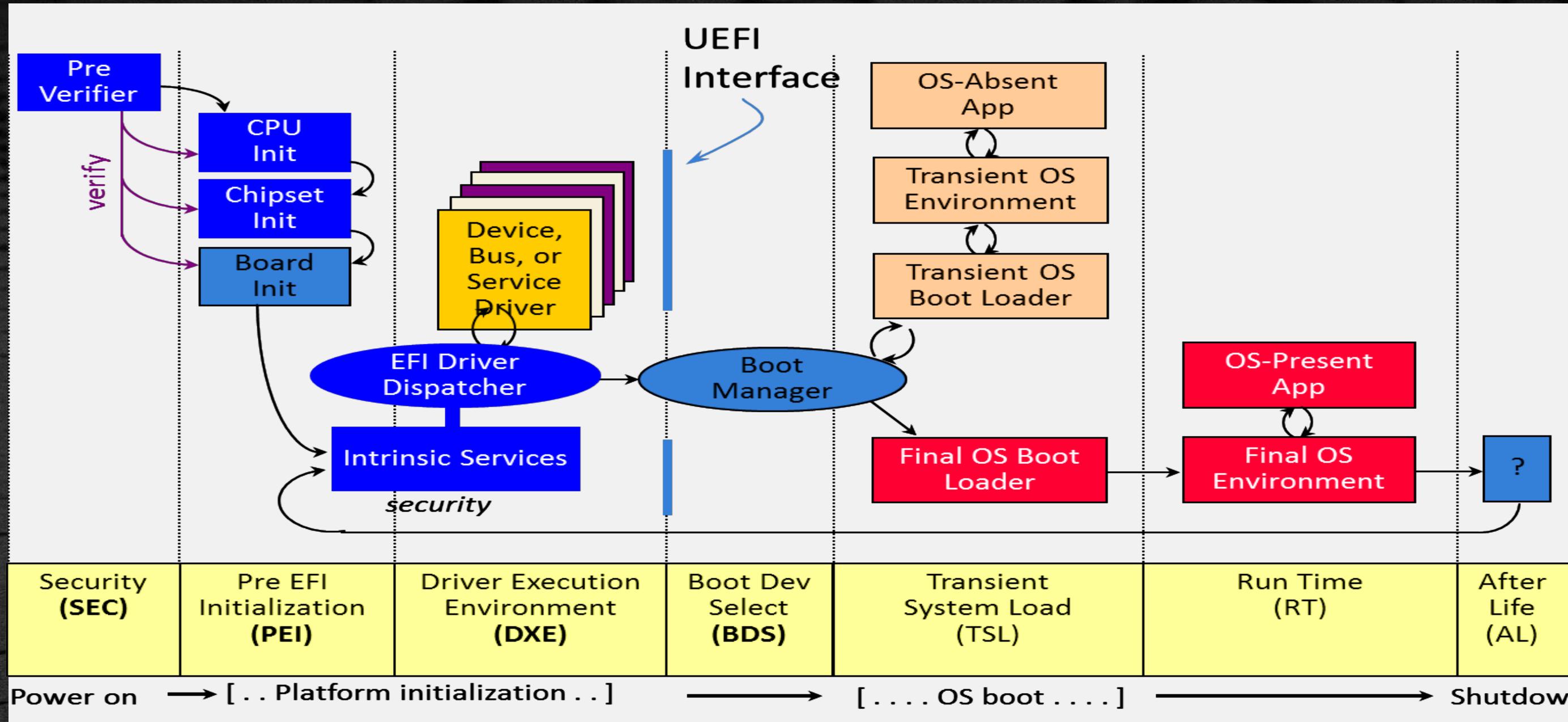
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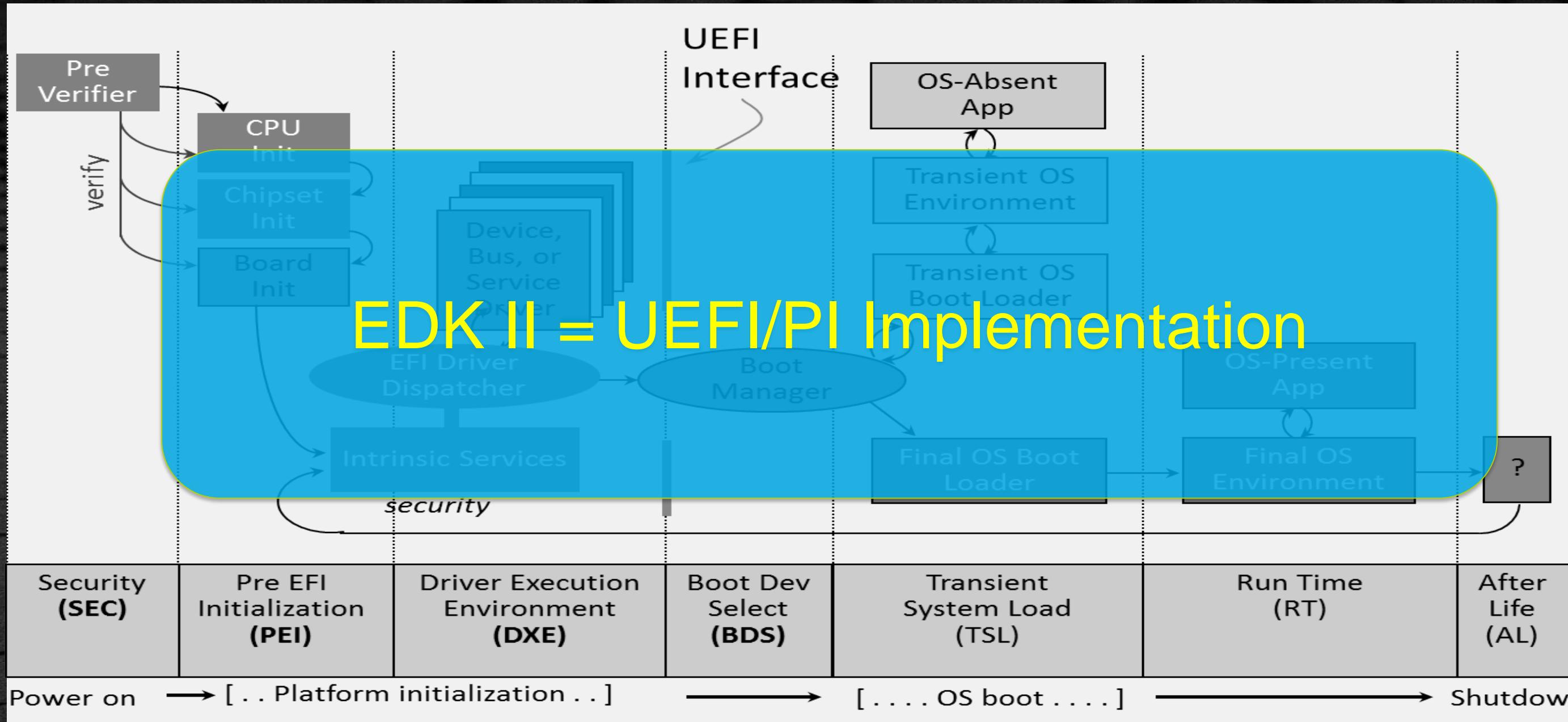
Move as
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Open source
EDK II on
tianocore.org

IMPLEMENTATION OF EDK II



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FILE EXTENSIONS

- Located on tianocore.org project edk2

.DSC file - Platform Description

.DEC file - Package Declaration

.INF file - Module Definition (define a component)

.FDF file - Flash Description File

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.VFR file - Visual Forms Representation for User interface

.UNI file - String text file for ease of localization

.c & .h files - Source code files

.DXS file - Dependency expression file – now [DEPEX]

.FV file - Firmware Volume image file

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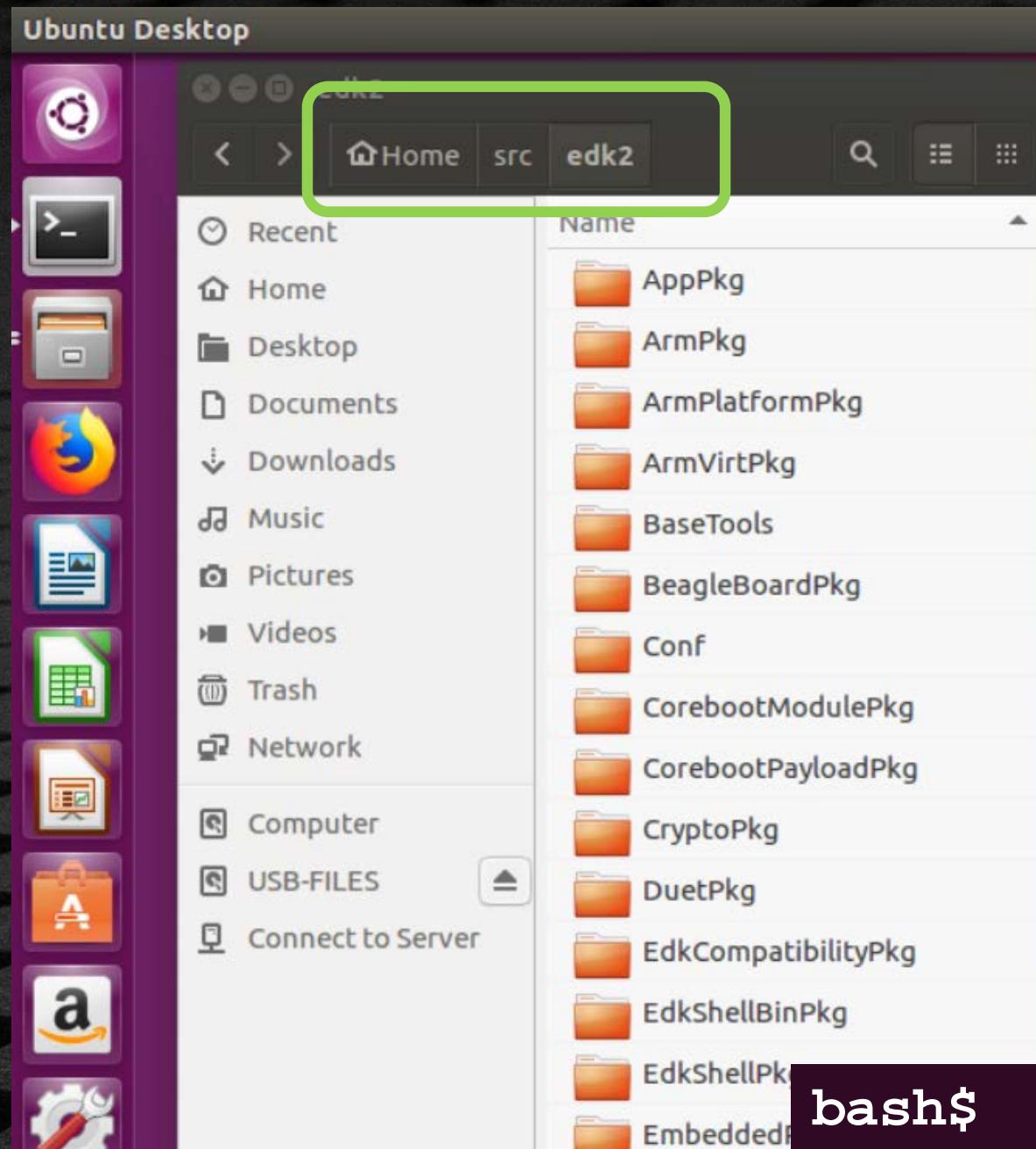
.FV file - Firmware Volume image file

EDK II
Spec

Source

Output

EDK II DIRECTORY STRUCTURE



- Package concept for each EDK II sub-directory
- Platforms are contained in an EDK II package
- EDK II build process reflects the package
- Concept of “Work Space”

\$HOME/src/edk2

```
bash$ cd $HOME/src/edk2
bash$ . edksetup.sh
bash$ build
```

DIRECTORY STRUCTURE- REAL PLATFORM

Open Source Directories

MyWorkSpace/
BaseTools/
Conf/
CryptoPkg/
EdkCompatibilityPkg/
FatBinPkg/
IntelFrameworkModulePkg/
IntelFrameworkPkg/
IntelFsp2Pkg
MdeModulePkg/
MdePkg/
NetworkPkg/
Nt32Pkg
OptionRomPkg/
OvmPkg
PcAtChipsestPkg/
ShellBinPkg/
Etc.

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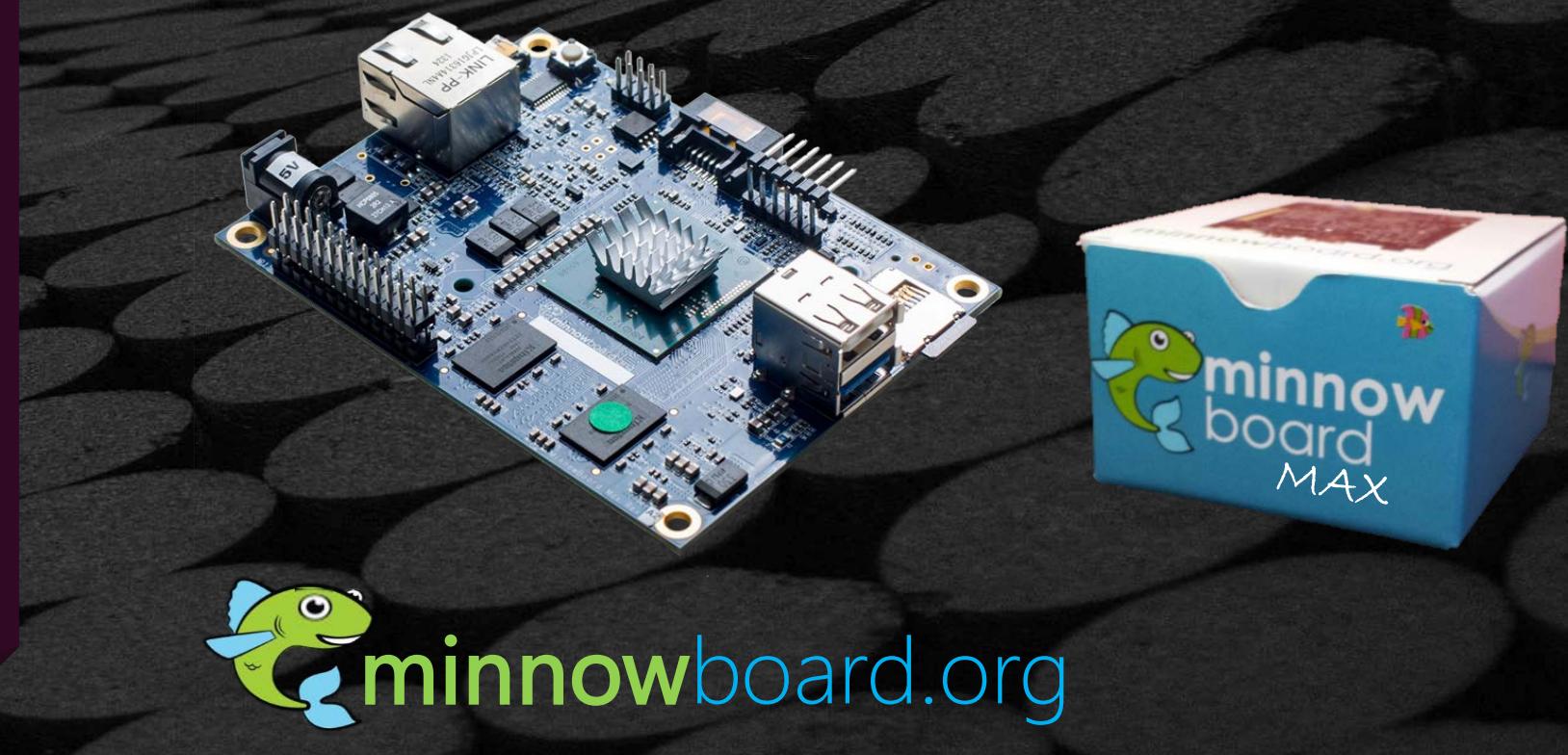
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OvmPkg
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Etc.

Platform and Silicon Directories

IA32FamilyCpuPkg /
Vlv2DeviceRefCodePkg /
Vlv2TbltMiscPkg /
Vlv2TbltDevicePkg /



DIRECTORY STRUCTURE- REAL PLATFORM

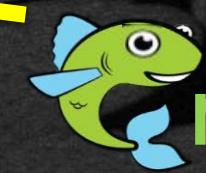
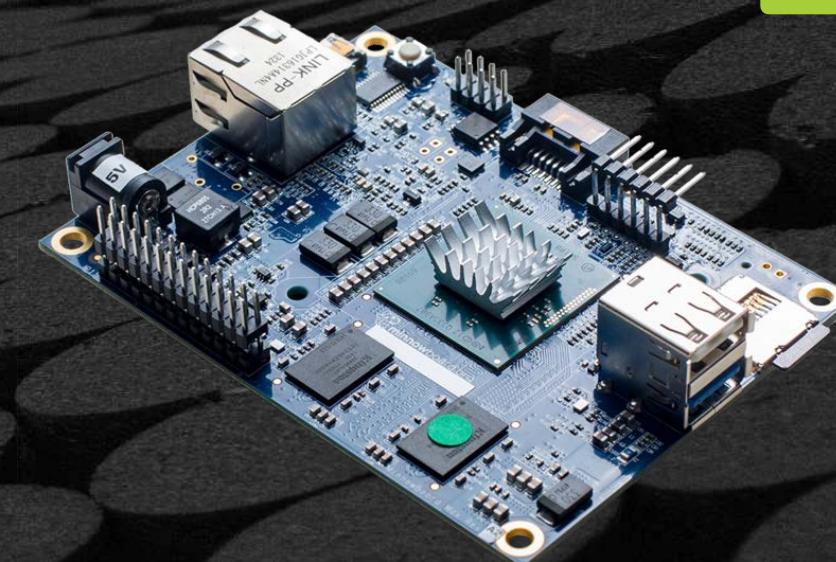
Open Source Directories

```
MyWorkSpace/  
  Build/  
  edk2/  
    - "edk2 Open Source"  
    - . . .  
  edk2-platforms/  
    V1v2DeviceRefCodePkg/  
      AcpiTablesPCAT/  
      Include/  
      ValleyView2Soc/  
        CPU/  
        NorthCluster/  
        SouthCluster  
    V1v2TbtDevicePkg/ ←  
    Silicon/  
      IA32FamilyCpuPkg/  
      V1v2BinaryPkg  
      V1v2MiscBinariesPkg/
```

Platform and Silicon Directories

Key

Open Source
Silicon/Chipset
Platform



minnowboard.org



Modules: Building blocks of EDK II

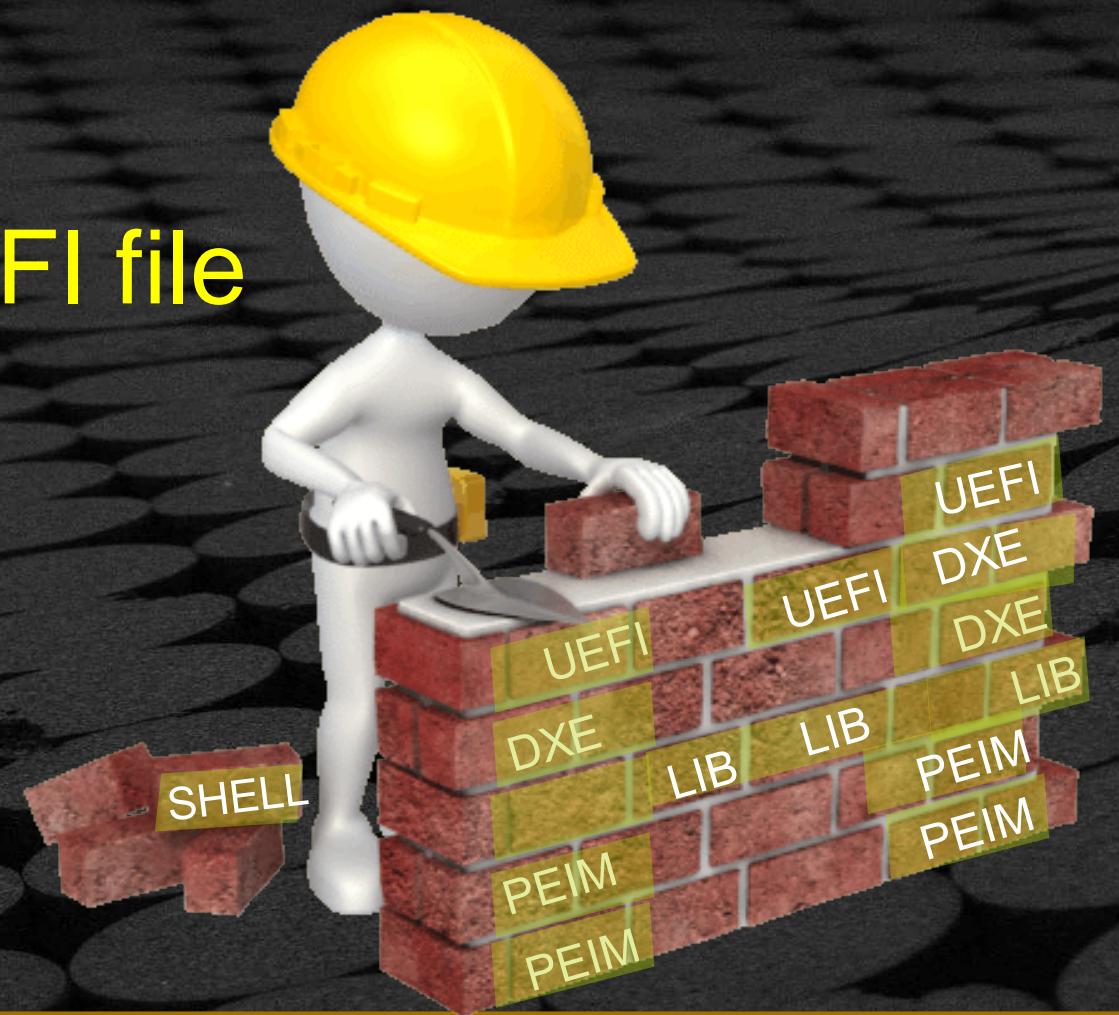
Smallest separate object compiled in EDK II



Modules: Building blocks of EDK II

Smallest separate object compiled in EDK II

Compiles to .EFI file



Modules: Building blocks of EDK II

MODULES

Smallest separate object compiled in EDK II

Compiles to .EFI file



UEFI/DXE Driver
PEIM
UEFI App.
Library

Modules: Building blocks of EDK II

PACKAGES

- EDK II projects are made up of packages
- Make your own packages
- Package contains only the necessities
- Remove packages from projects when not required



EDK II PACKAGE EXAMPLES: SPECS

MdePkg

Include files and libraries for Industry Standard Specifications

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Modules only definitions from the Industry Standard Specification are defined in the MdePkg

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Include files and libraries for those parts of the Intel Platform Innovation Framework for EFI specifications not adopted “as is” by the UEFI or PI specifications

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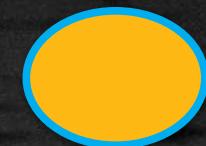
IntelFrameworkPkg

Include files and libraries for those parts of the Intel Platform Innovation Framework for EFI specifications not adopted “as is” by the UEFI or PI specifications

IntelFrameworkModulePkg

Contains modules that make reference to one or more definitions in the IntelFrameworkPkg

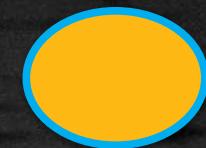
EDK II ADDITIONAL PACKAGES



Platforms

Nt32Pkg & OvmfPkg

EDK II ADDITIONAL PACKAGES



Platforms

`Nt32Pkg & OvmfPkg`



Chipset/Processor `IA32FamilyCpuPkg & BroxtonSiPkg`

EDK II ADDITIONAL PACKAGES



Platforms

Nt32Pkg & OvmfPkg

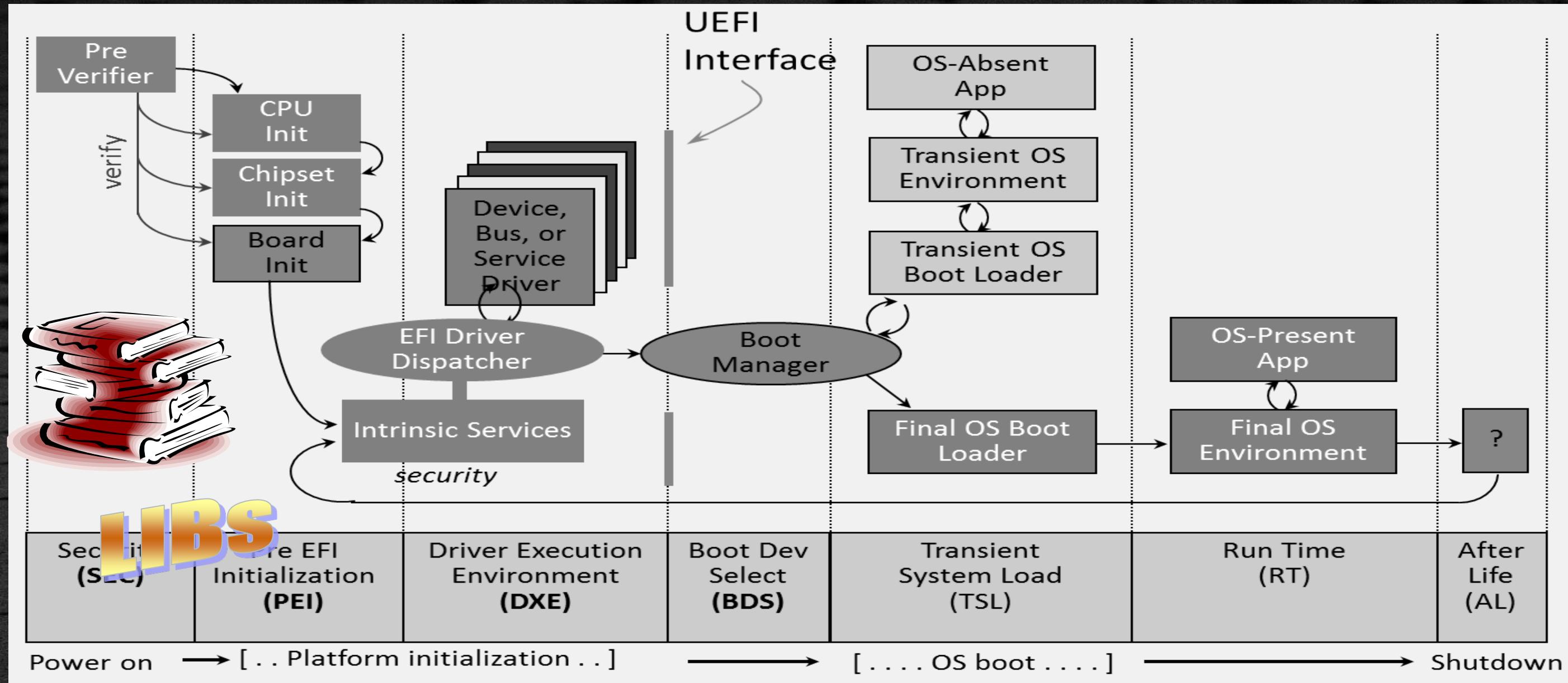


Chipset/Processor **IA32FamilyCpuPkg & BroxtonSiPkg**

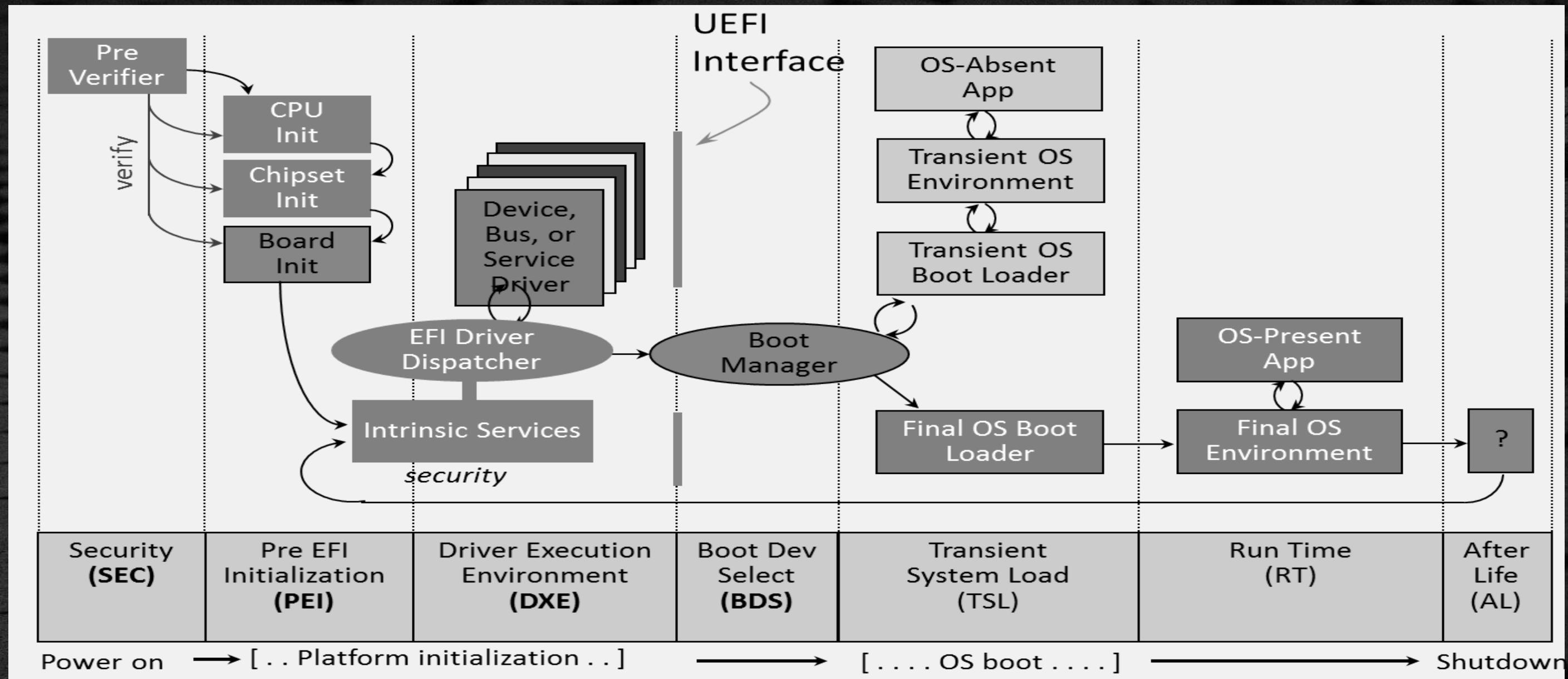


Functionality

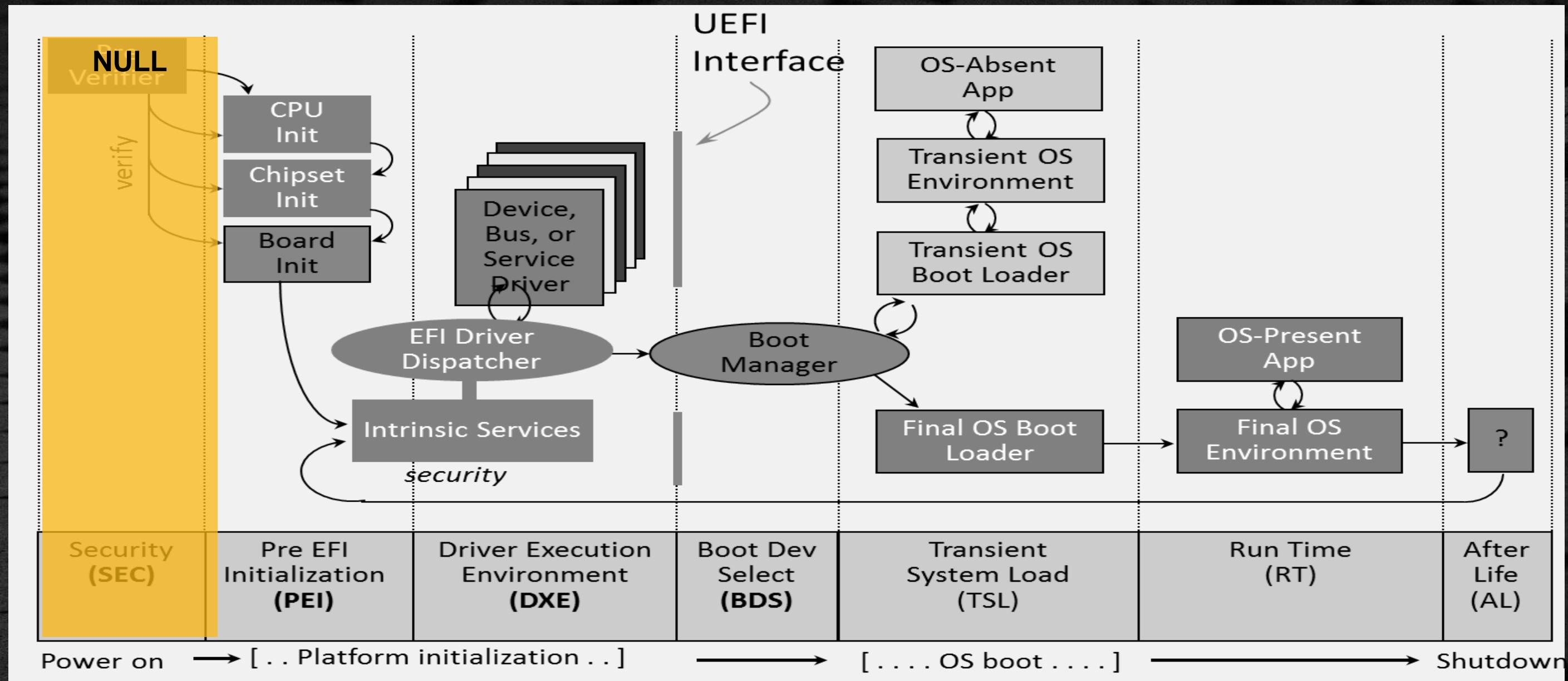
ShellPkg & NetworkPkg



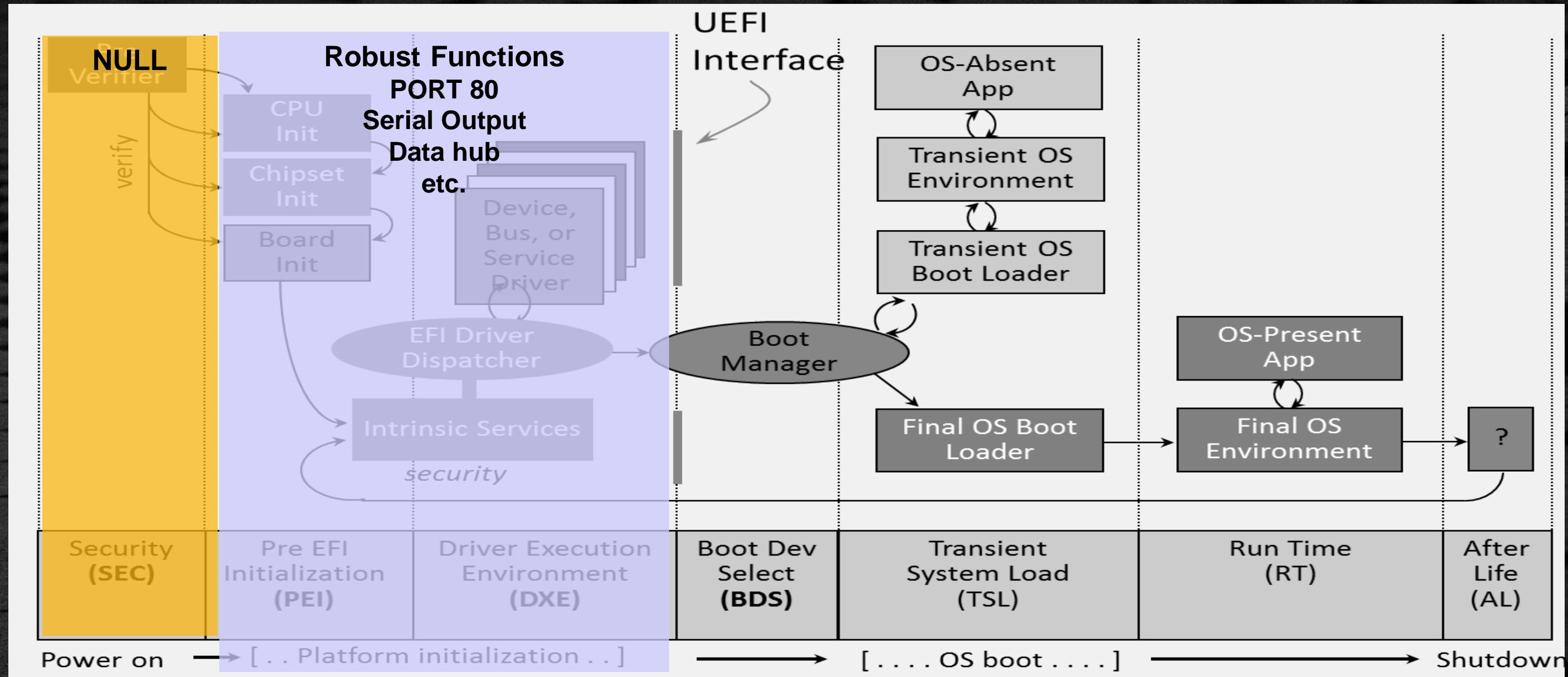
EXAMPLE – LIBRARY “DEBUGLIB”



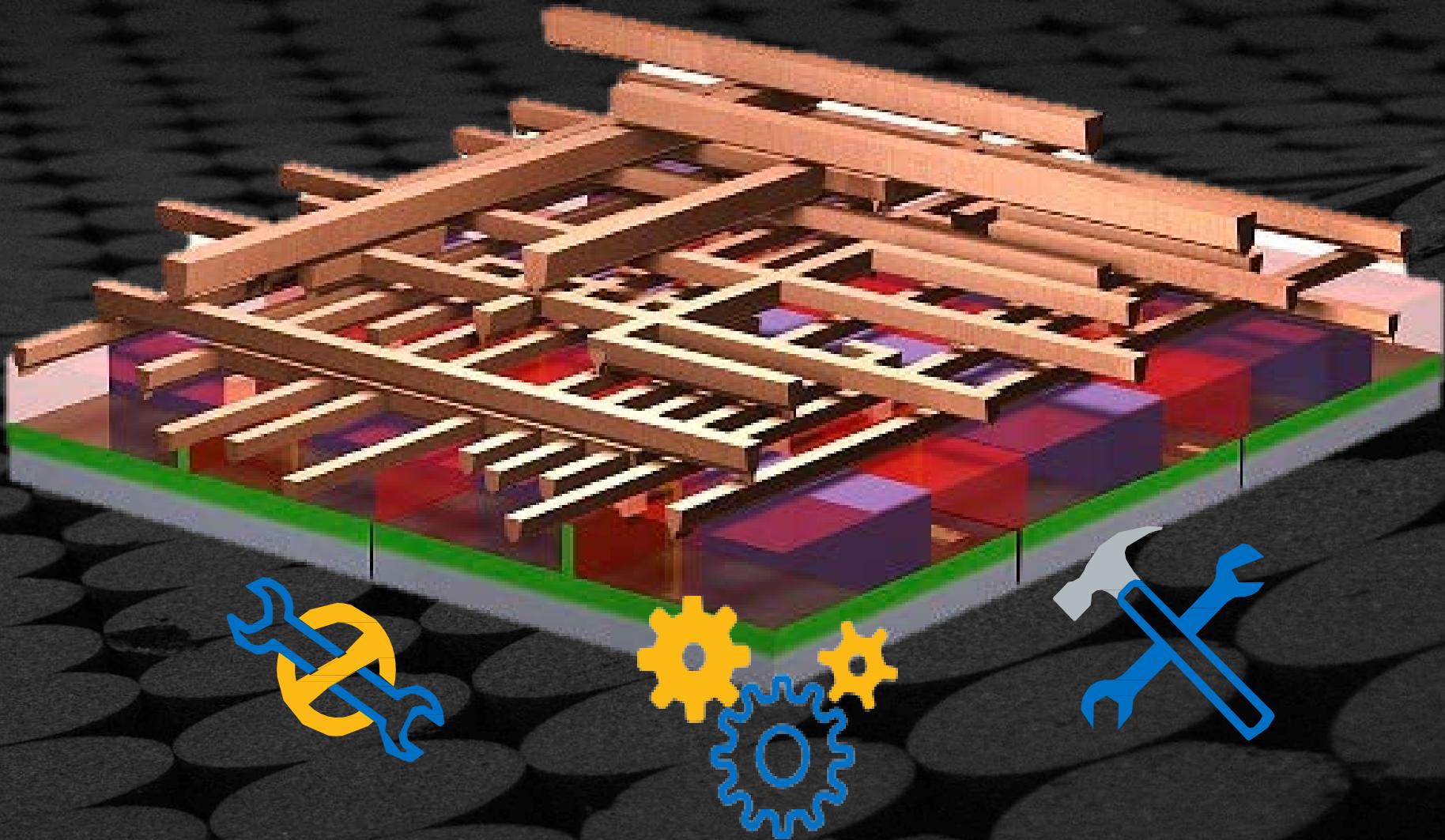
EXAMPLE – LIBRARY “DEBUGLIB”



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PLATFORM CONFIGURATION DATABASE (PCD)



Goals

Define module parameters

- Store module / platform configurations

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Reduce edits of source code

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Reduce edits of source code

- Maximize module reuse across platforms

Remove #define

- No searching for “magic” **#define** statements

API functions

- Get and Set functions for access to PCD variable DB

Advantages

Binary Modularity

- Offers a way to configure firmware settings by patching binaries without building

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Configure

- Provide for options to configure features

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Patching

- Simplify the patching process

EDK II INFRASTRUCTURE SUMMARY



Packages
List of
modules

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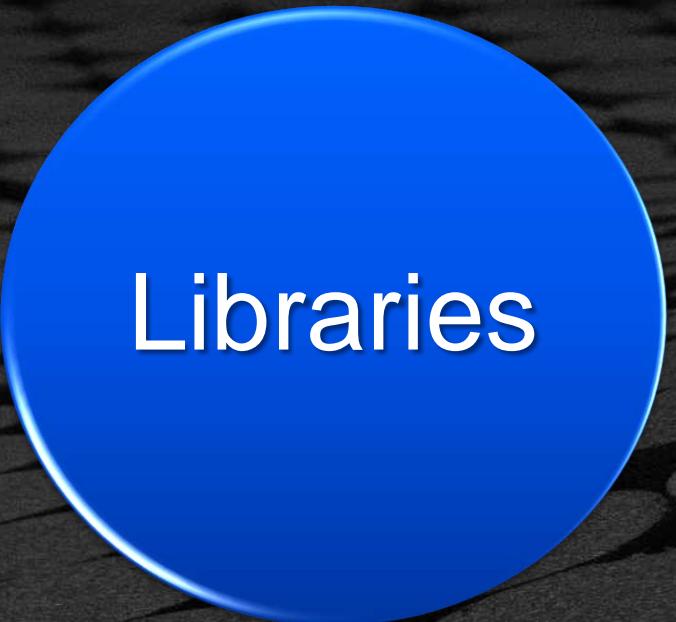


Libraries

EDK II INFRASTRUCTURE SUMMARY



Packages
List of
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Libraries



PCD
Platform
Config. DB

EDK II VS. UDK (2010| 2017 .. 2018)

UEFI Developer's Kit 2018 (UDK2018)

Stable build of the EDK II project

Neither contain Intel silicon or platform code

wiki on tianocore.org [Differences between UDK - EDK II](#)

BUILD TOOLS

EDK Build Tools and Configuration Files

DEVELOPMENT ENVIRONMENT

Compiler Tool Chains

- Microsoft Visual Studio (VS2015, VS2013, VS2012, VS2010, etc.)
- Microsoft WDK
- Intel C/C++ compiler
- Intel C EFI Byte Code (EBC) compiler
- GCC V5.x or later

Operating Systems

- Microsoft Windows XP/7/8/10
- Apple Mac OS X
- RedHat Enterprise Linux
- Novell SuSE Linux
- Ubuntu

ENVIRONMENT VARIABLES

Set by **edksetup**

Windows = .bat

Linux = .sh

1. EDK_TOOLS_PATH
2. PATH
3. WORKSPACE
4. PACKAGES_PATH (*optional*)
5. EFI_SOURCE/EDK_SOURCE

CONFIGURATION FILES - SCRIPTS

1

edksetup.bat or edksetup.sh

```
bash@usid:~/src/edk2
```

```
bash@usid:~/src/edk2$ . edksetup.sh
```

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First time use will set up configuration files:

Conf/build_rule.txt

Conf/target.txt

Conf/tools_def.txt

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Setup & verify a developer's workspace

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Setup & verify a developer's workspace

4

Ensure environment variables set correctly
WORKSPACE, EDK_TOOLS_PATH, ...

Multiple Workspace Environment Variable

PACKAGES_PATH

WORKSPACE

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Example:

```
set WORKSPACE=%CWD%/MyWorkspace  
set \  
PACKAGES_PATH=%WORKSPACE%/edk2;%WORKSPACE%/Platform;\ \  
%WORKSPACE%/Silicon
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\$HOME/MyWorkspace

-  BaseTools
-  Conf
-  Edk2
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-  Silicon

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\$HOME/MyWorkspace

	BaseTools
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USING TARGET.TXT

Tag	Description
ACTIVE_PLATFORM	Pointer to DSC file being built
TARGET	Build mode: DEBUG or RELEASE
TARGET_ARCH	Build architecture (IA32, IPF, X64, EBC, ARM)
TOOL_CHAIN_CONF	Path to tools_def.txt
TOOL_CHAIN_TAG	Compiler/tool set to use, based on definitions in tools_def.txt
MAX_CONCURRENT_THREAD_NUMBER	Number of threads available to the build process (multi-threaded build)

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USING TOOLS_DEF.TXT



Paths for compilers, assemblers, and linkers

- Comes with definitions for all compilers

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Only modify this file when ...

- Tools are installed in a non-default location
- Different compilers/tools need to be added

USING TOOLS_DEF.TXT



Paths for compilers, assemblers, and linkers

- Comes with definitions for all compilers



Only modify this file when ...

- Tools are installed in a non-default location
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Default values are set by **edksetup** script

- Default values will cover most compiler needs
- If there are problems with the file after editing, just delete and re-run **edksetup** (restores default)

BUILD PROCESS

EDK Build Files

BUILD PROCESS OVERVIEW

INF Files

DEC Files

DSC Files

FDF Files

Binary Files

tools_def.txt
target.txt

Source Files

Source/Input



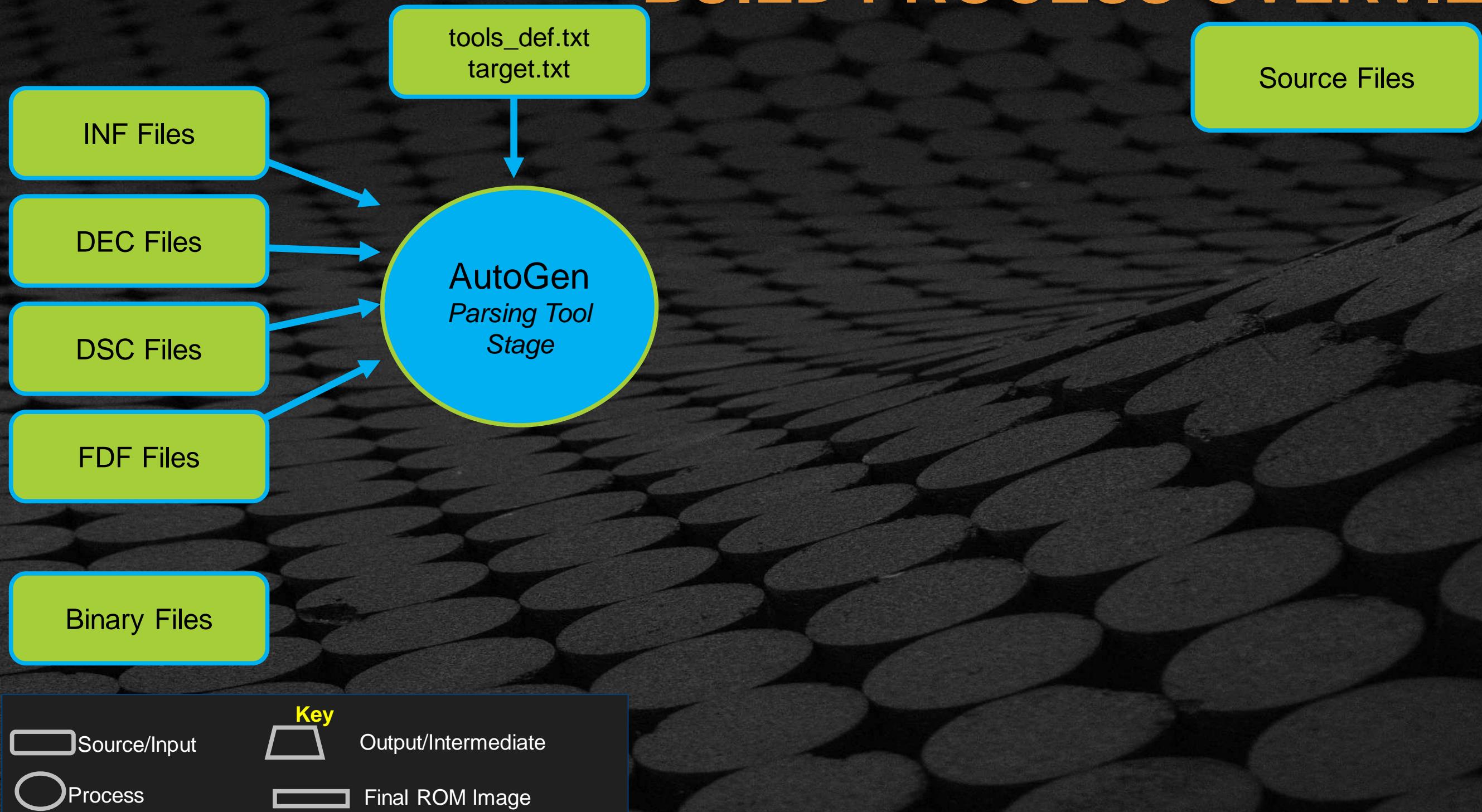
Output/Intermediate

Process

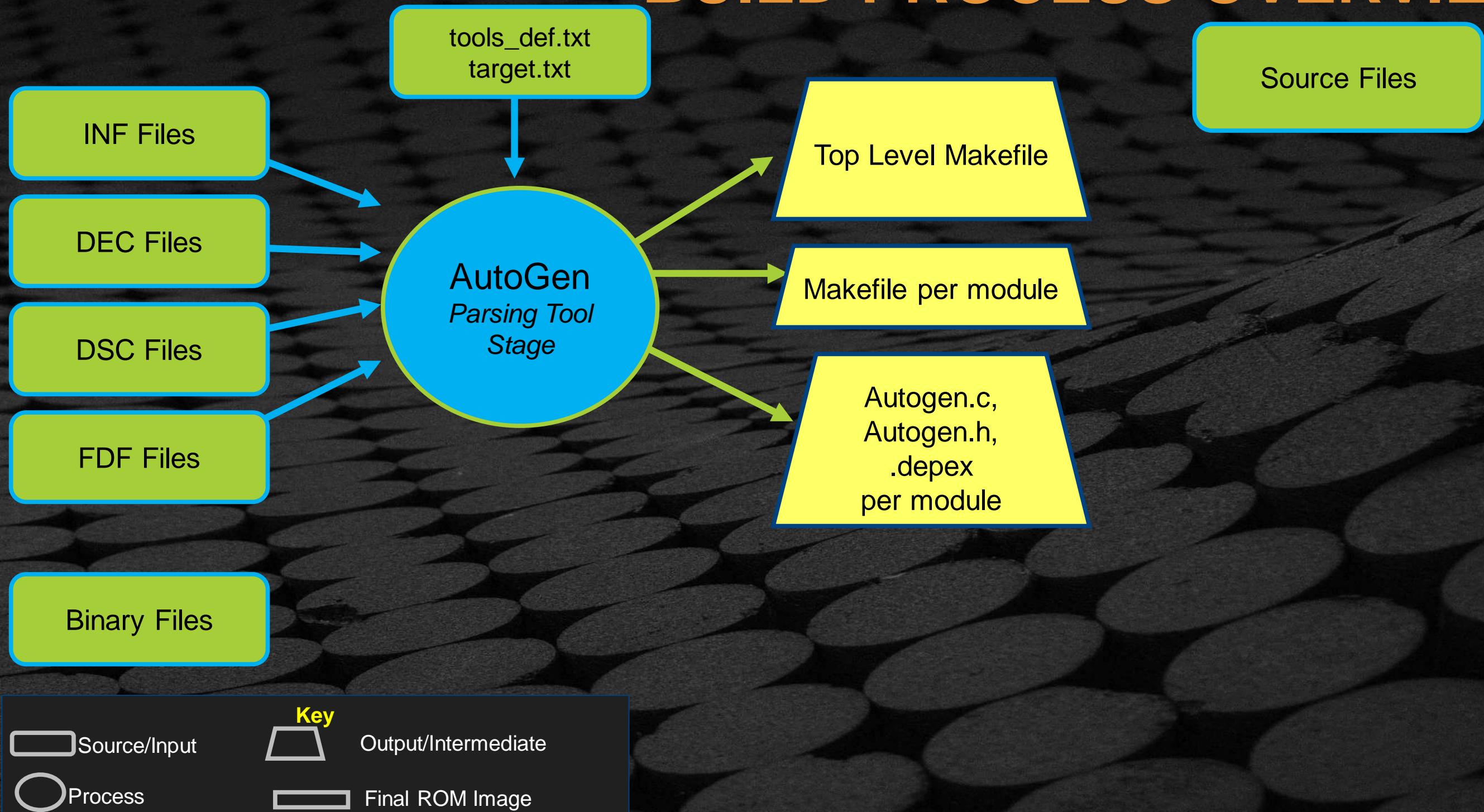


Final ROM Image

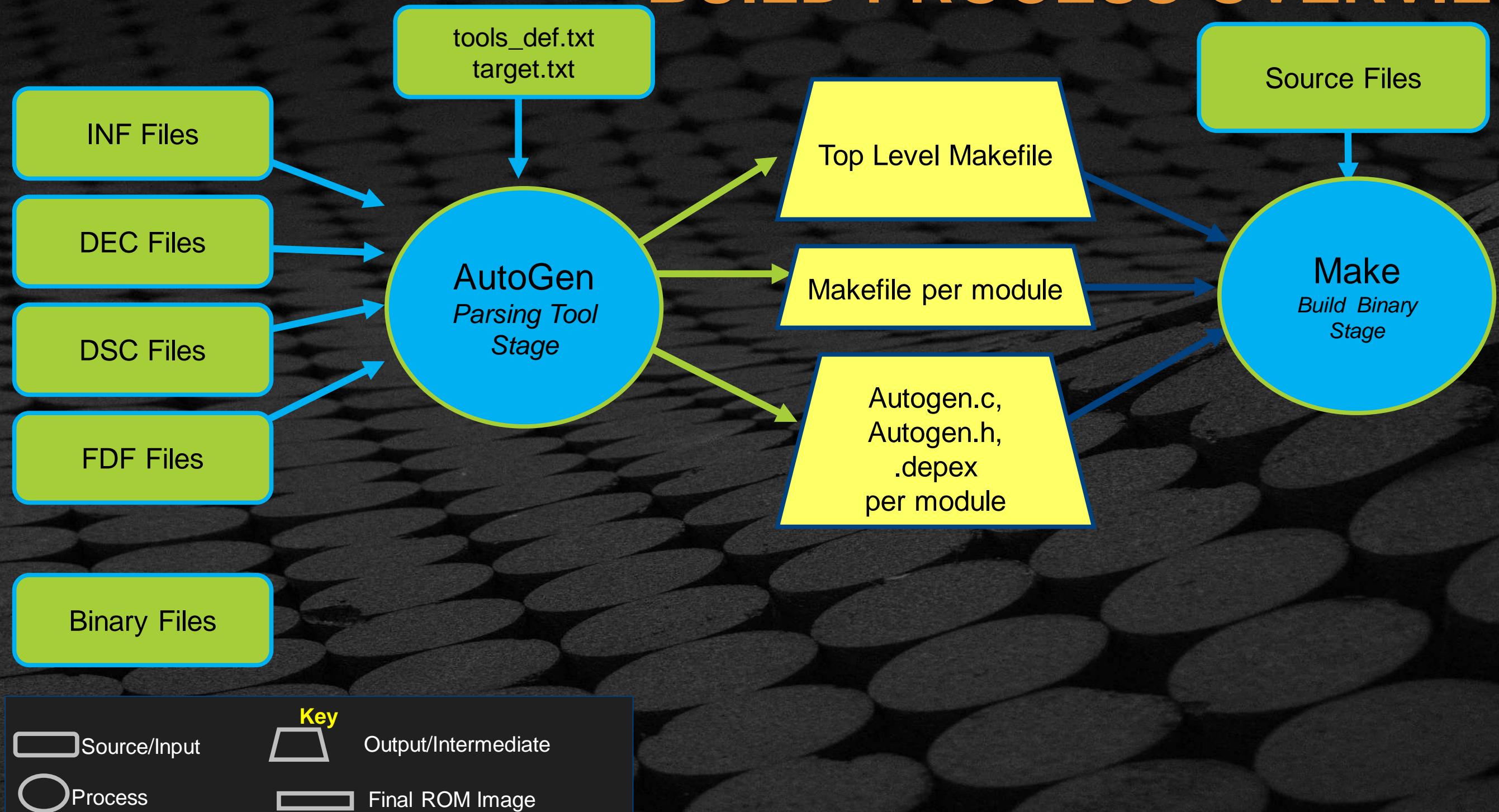
BUILD PROCESS OVERVIEW



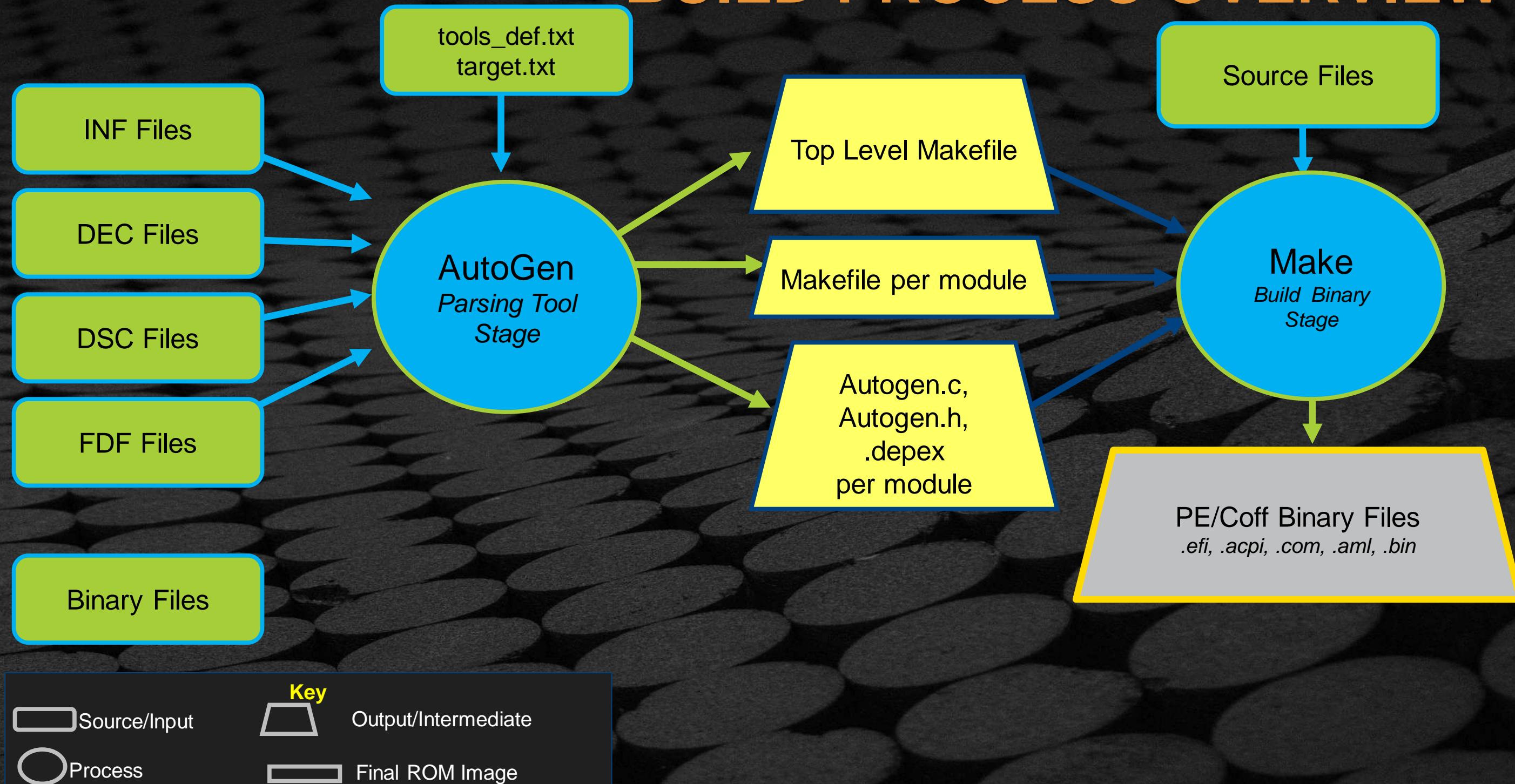
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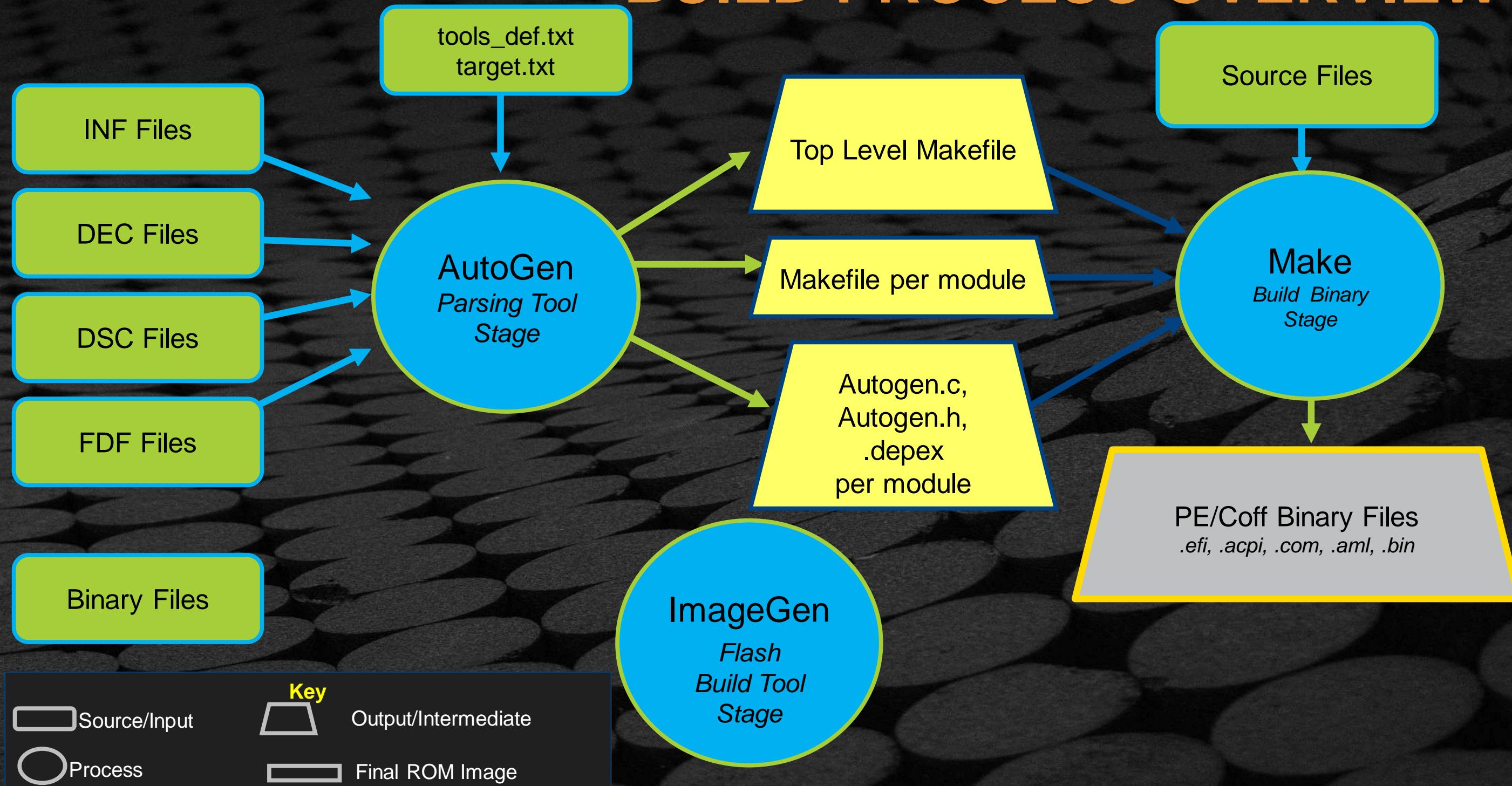
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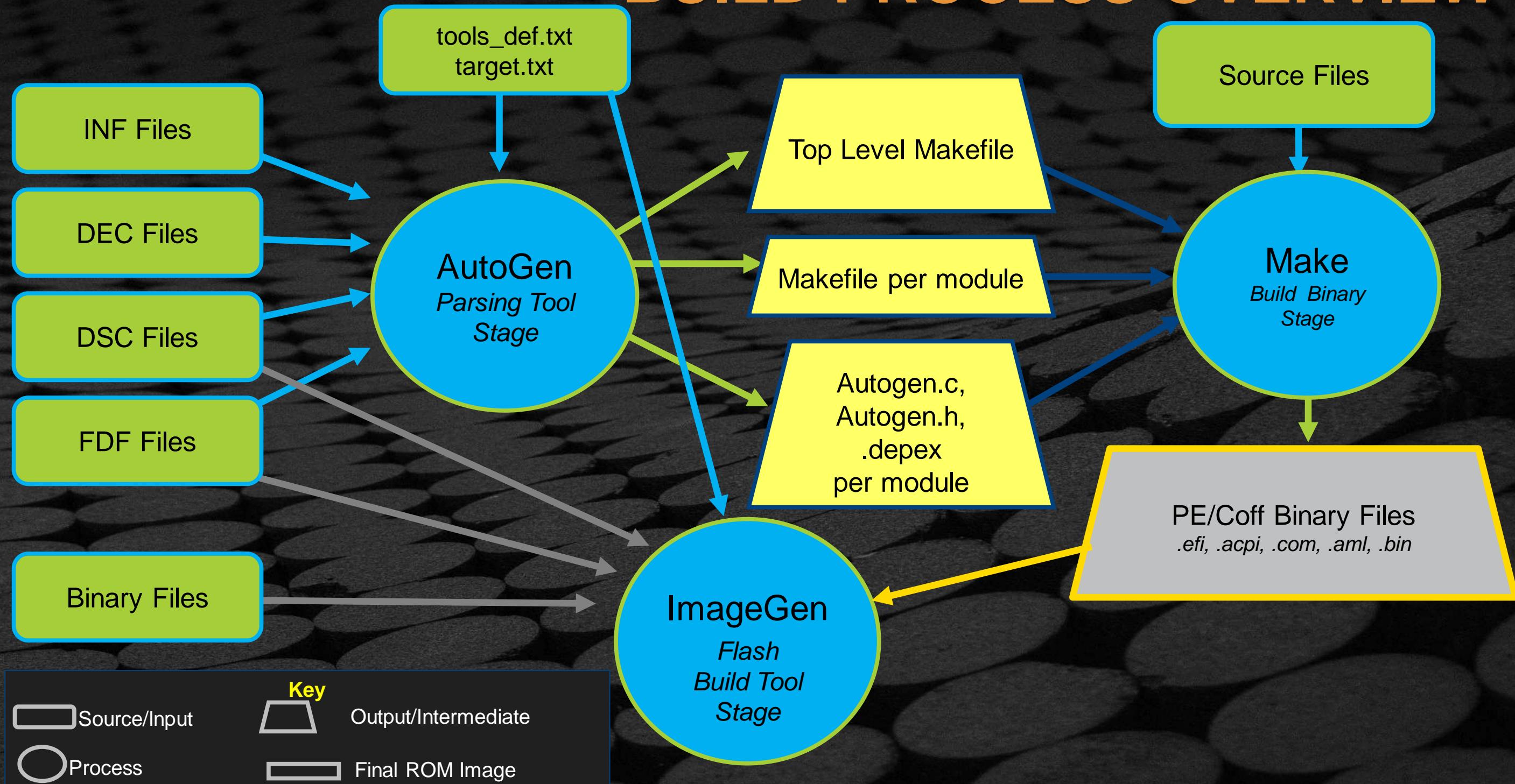
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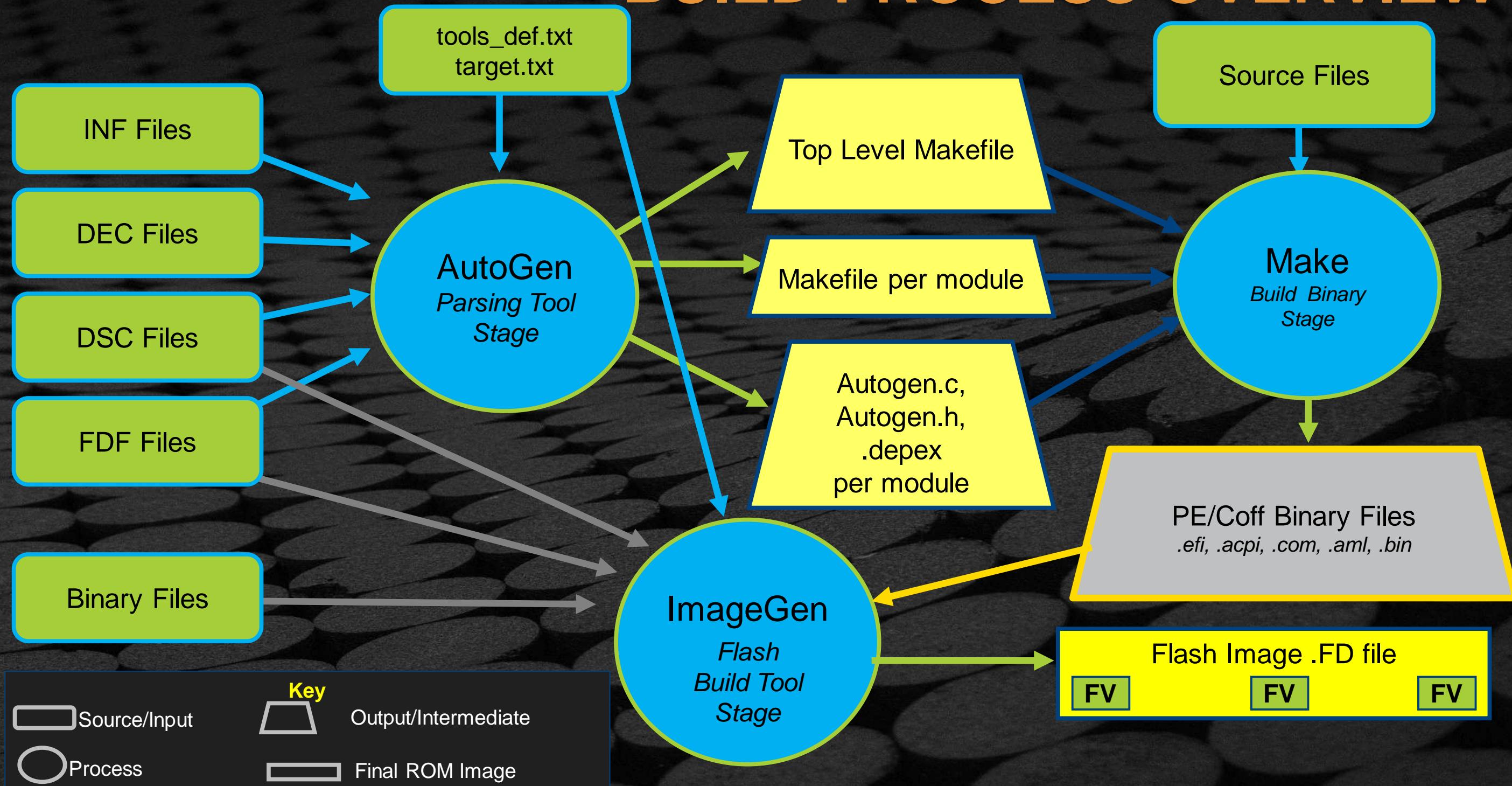
BUILD PROCESS OVERVIEW



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BUILD PROCESS OVERVIEW



BASIC BUILD STEPS

Platform

1. Open Terminal prompt
2. Navigate to root of EDK II workspace
3. Run **edksetup**
4. Edit **Conf/target.txt**
5. Run **build**
6. Output: firmware image (FD) file under **Build** directory

Module

1. Open Terminal prompt
2. Navigate to root of EDK II workspace
3. Run **edksetup**
4. Change to a directory with the proper INF
5. Run **build**
6. Output: .EFI files under **Build** directory

Note: Module .inf in .dsc components

BUILD OUTPUT LOCATION

Build

Build

Build

Path Element	Description	Notes
Build	Build directory	This is default.
Ovmfpkg	platform being used	
DEBUG_MYTOOLS	build mode and tool chain	From target.txt
FV	contains final image	Both FV and FD images
IA32	processor architecture	Contains platform makefile
Pkg/ModuleName	path to INF file	One for each INF
Foo	name of INF file (Module)	Contains module makefile
OUTPUT	.EFI file location	
DEBUG	Autogen files	

BUILD OUTPUT LOCATION

Build /Ovmfx64

Build /Ovmf¹

Build /Ovmf¹

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¹ IA32 or X64

BUILD OUTPUT LOCATION

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Build /Ovmf¹/DEBUG_MYTOOLS

Build /Ovmf¹ /DEBUG_MYTOOLS

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Build /Ovmf¹/DEBUG_MYTOOLS /IA32

Build /Ovmf¹ /DEBUG_MYTOOLS /IA32

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Pkg/ModuleName	path to INF file	One for each INF
Foo	name of INF file (Module)	Contains module makefile
OUTPUT	.EFI file location	
DEBUG	Autogen files	

¹ IA32 or X64

BUILD OUTPUT LOCATION

Build /Ovmfx64 /DEBUG_MYTOOLS /FV

Build /Ovmf¹/DEBUG_MYTOOLS /IA32 /Pkg /ModuleName

Build /Ovmf¹ /DEBUG_MYTOOLS /IA32 /Pkg /ModuleName

Path Element	Description	Notes
Build	Build directory	This is default.
Ovmfpkg	platform being used	
DEBUG_MYTOOLS	build mode and tool chain	From target.txt
FV	contains final image	Both FV and FD images
IA32	processor architecture	Contains platform makefile
Pkg/ModuleName	path to INF file	One for each INF
Foo	name of INF file (Module)	Contains module makefile
OUTPUT	.EFI file location	
DEBUG	Autogen files	

¹ IA32 or X64

BUILD OUTPUT LOCATION

Build /Ovmfx64 /DEBUG_MYTOOLS /FV

Build /Ovmf¹/DEBUG_MYTOOLS /IA32 /Pkg /ModuleName/Foo

Build /Ovmf¹ /DEBUG_MYTOOLS /IA32 /Pkg /ModuleName /Foo

Path Element	Description	Notes
Build	Build directory	This is default.
Ovmfpkg	platform being used	
DEBUG_MYTOOLS	build mode and tool chain	From target.txt
FV	contains final image	Both FV and FD images
IA32	processor architecture	Contains platform makefile
Pkg/ModuleName	path to INF file	One for each INF
Foo	name of INF file (Module)	Contains module makefile
OUTPUT	.EFI file location	
DEBUG	Autogen files	

¹ IA32 or X64

BUILD OUTPUT LOCATION

Build /Ovmfx64 /DEBUG_MYTOOLS /FV

Build /Ovmf¹/DEBUG_MYTOOLS /IA32 /Pkg /ModuleName/Foo

Build /Ovmf¹ /DEBUG_MYTOOLS /IA32 /Pkg /ModuleName /Foo /OUTPUT

Path Element	Description	Notes
Build	Build directory	This is default.
Ovmfpkg	platform being used	
DEBUG_MYTOOLS	build mode and tool chain	From target.txt
FV	contains final image	Both FV and FD images
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¹ IA32 or X64

BUILD OUTPUT LOCATION

Build /Ovmfx64 /DEBUG_MYTOOLS /FV

Build /Ovmf¹/DEBUG_MYTOOLS /IA32 /Pkg /ModuleName/Foo /DEBUG

Build /Ovmf¹ /DEBUG_MYTOOLS /IA32 /Pkg /ModuleName /Foo /OUTPUT /DEBUG

Path Element	Description	Notes
Build	Build directory	This is default.
Ovmfpkg	platform being used	
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Foo	name of INF file (Module)	Contains module makefile
OUTPUT	.EFI file location	
DEBUG	Autogen files	

¹ IA32 or X64

EDK II BUILD PROCESS STAGES

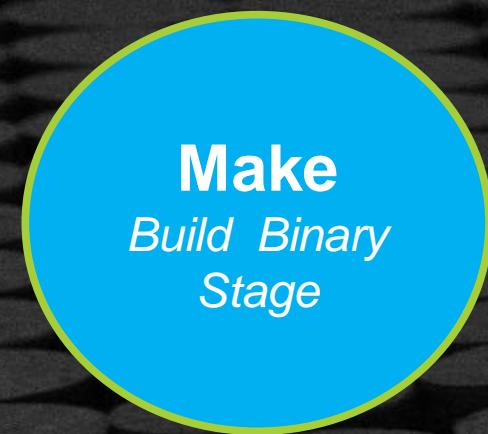
AutoGen
*Parsing Tool
Stage*

Parse meta-data files to generate some C source code files and the make files

EDK II BUILD PROCESS STAGES

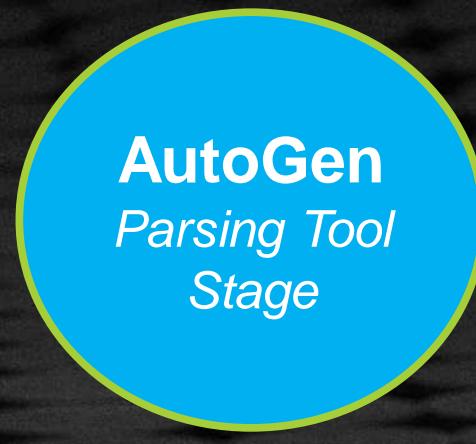


Parse meta-data files to generate some C source code files and the make files

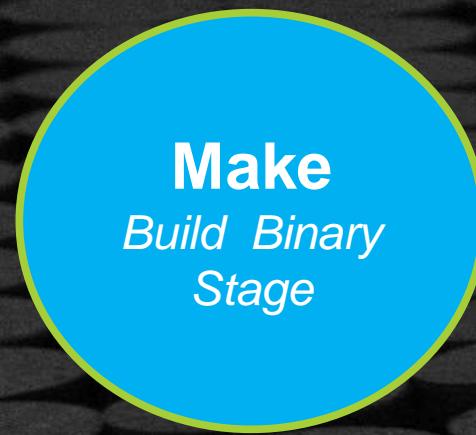


Process source code files to create PE32/PE32+/COFF images processed to UEFI format using \$(MAKE) tool

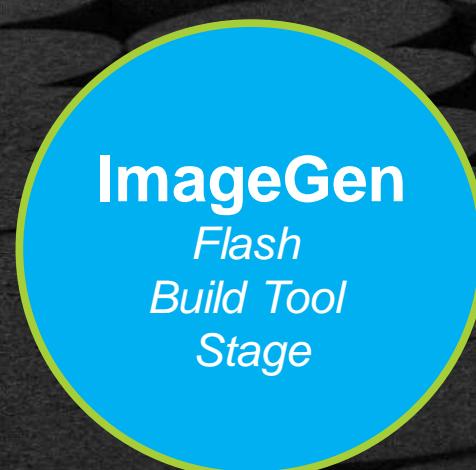
EDK II BUILD PROCESS STAGES



Parse meta-data files to generate some C source code files and the make files



Process source code files to create PE32/PE32+/COFF images processed to UEFI format using \$(MAKE) tool



Takes the UEFI format files, creates UEFI “FLASH” images, UEFI apps, or UEFI PCI option ROMs

EDK II BUILD: AUTOGEN STAGE

EDK II Open Source

Build -p OvmfPkg/OvmfX64Pkg.dsc

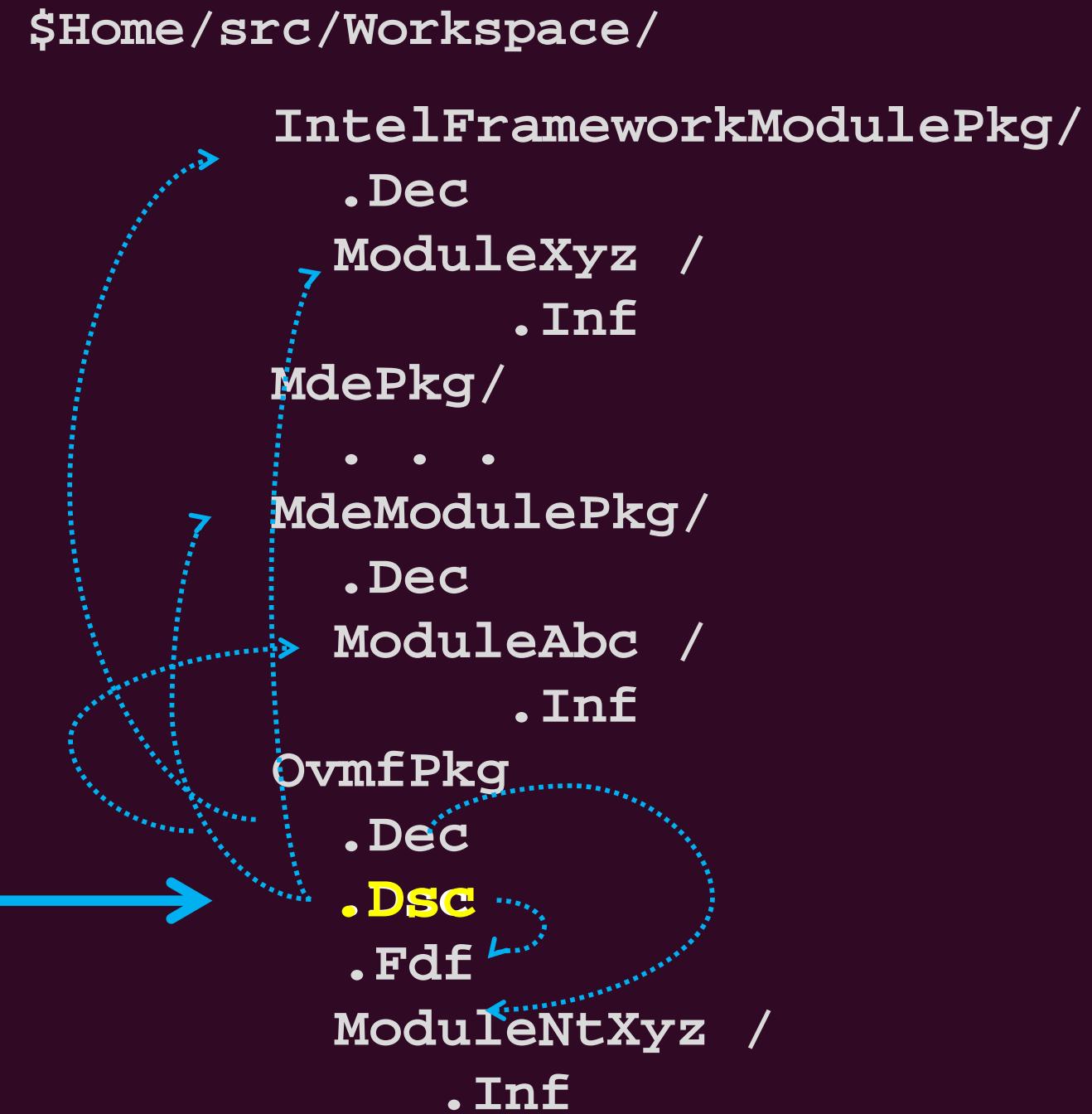


```
$Home/src/Workspace/  
  IntelFrameworkModulePkg/  
    .Dec  
    ModuleXyz /  
      .Inf  
  MdePkg/  
    . . .  
  MdeModulePkg/  
    .Dec  
    ModuleAbc /  
      .Inf  
  OvmfPkg  
    .Dec  
    .Dsc  
    .Fdf  
    ModuleNtXyz /  
      .Inf
```

EDK II BUILD: AUTOGEN STAGE

EDK II Open Source

Build -p OvmfPkg/OvmfX64Pkg.dsc



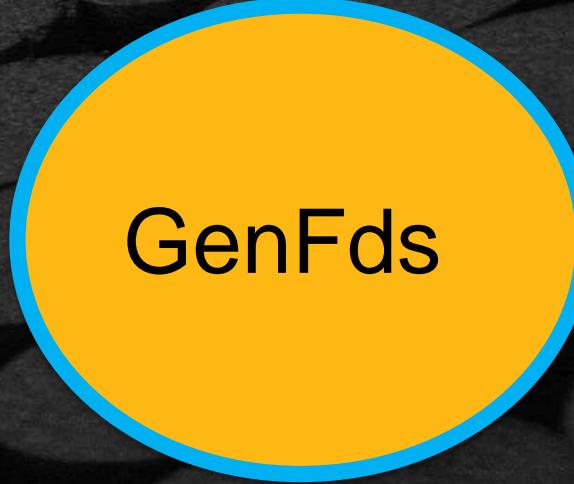
EDK II BUILD: MAKE STAGE

Uses assemblers/compilers/linkers to generate PE32/PE32+ COFF image file

Uses ImageGen tools to modify PE32/PE32+/COFF image file;
creates UEFI file (EFI_IMAGE_SECTION_HEADER structure)



GenFW



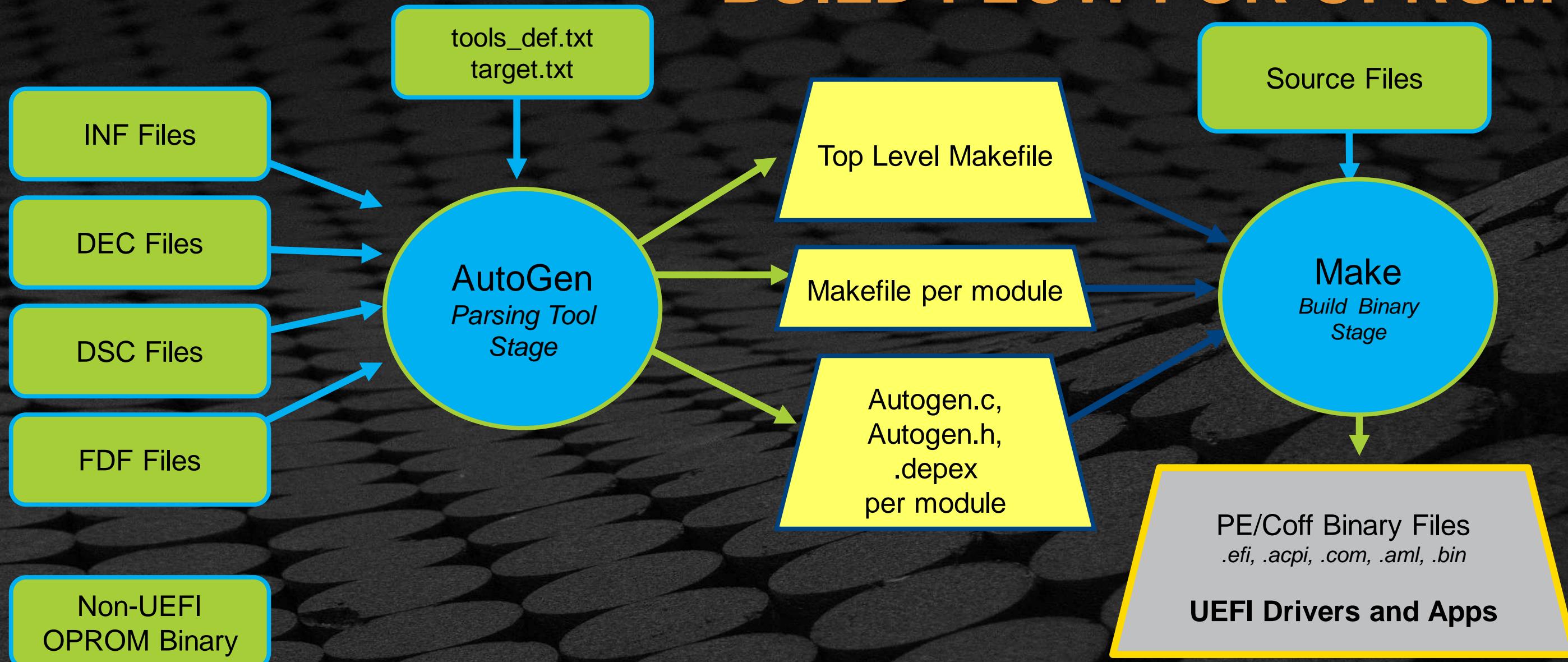
GenFds

EDK II BUILD: IMAGEGEN STAGE

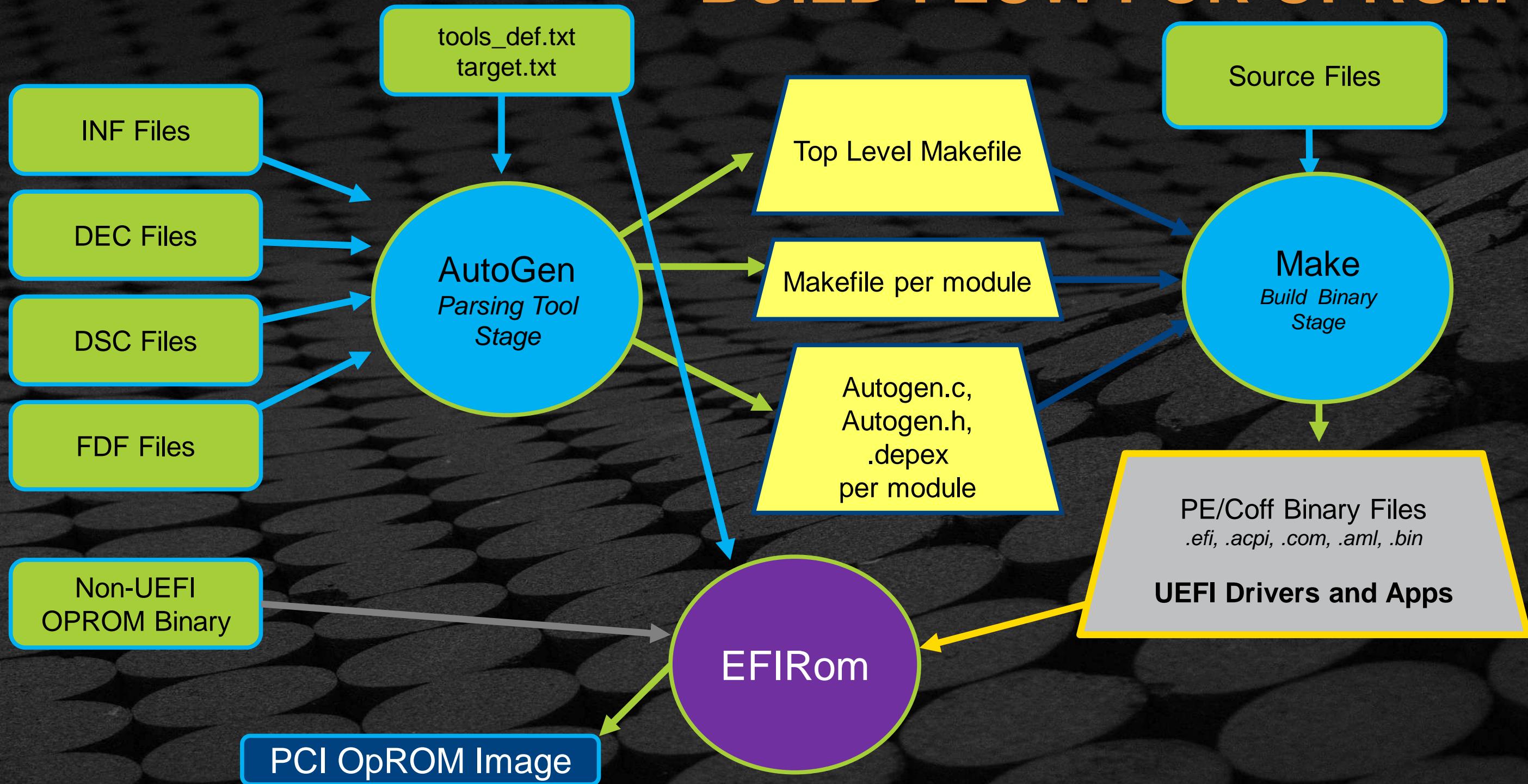
- Builds one image for each specified firmware volume (FV)
- The FDF file supports all syntax available in the PI Specification Vol. 3



BUILD FLOW FOR OPROM



BUILD FLOW FOR OPROM



THE BUILD COMMAND

- Accepts command line arguments to support scripted builds
- Overrides most settings found in `target.txt`
- Overrides DSC with a minimal INF build
- Overrides some settings in DSC file (.FDF)
- Choose settings from the FDF file (`ROMIMAGE`, `FVIMAGE`)
- Choose `$(make)` options (silent, verbose, quiet)

USING EDK II BUILD COMMAND

Usage: build.exe [options] [all|fds|genc|genmake|clean|cleanall|cleanlib|modules|libraries|run]

Copyright (c) 2007 - 2017, Intel Corporation All rights reserved.

Options:

- version show program's version number and exit
- h, --help show this help message and exit
- a TARGETARCH, --arch=TARGETARCH
ARCHS is one of list: IA32, X64, IPF, ARM or EBC,
which overrides target.txt's TARGET_ARCH definition
To specify more archs, please repeat this option.
- p PLATFORMFILE, --platform=PLATFORMFILE
Build the platform specified by the DSC file name
argument, overriding target.txt's ACTIVE_PLATFORM
definition.
- m MODULEFILE, --module=MODULEFILE
Build the module specified by the INF file name
argument.

bash\$ Build -h

USING EDK II BUILD COMMAND

Usage: build.exe [options] [all|fds|genc|genmake|clean|cleanall|cleanlib|modules|libraries|run]

Copyright (c) 2007 - 2017, Intel Corporation All rights reserved.

Options:

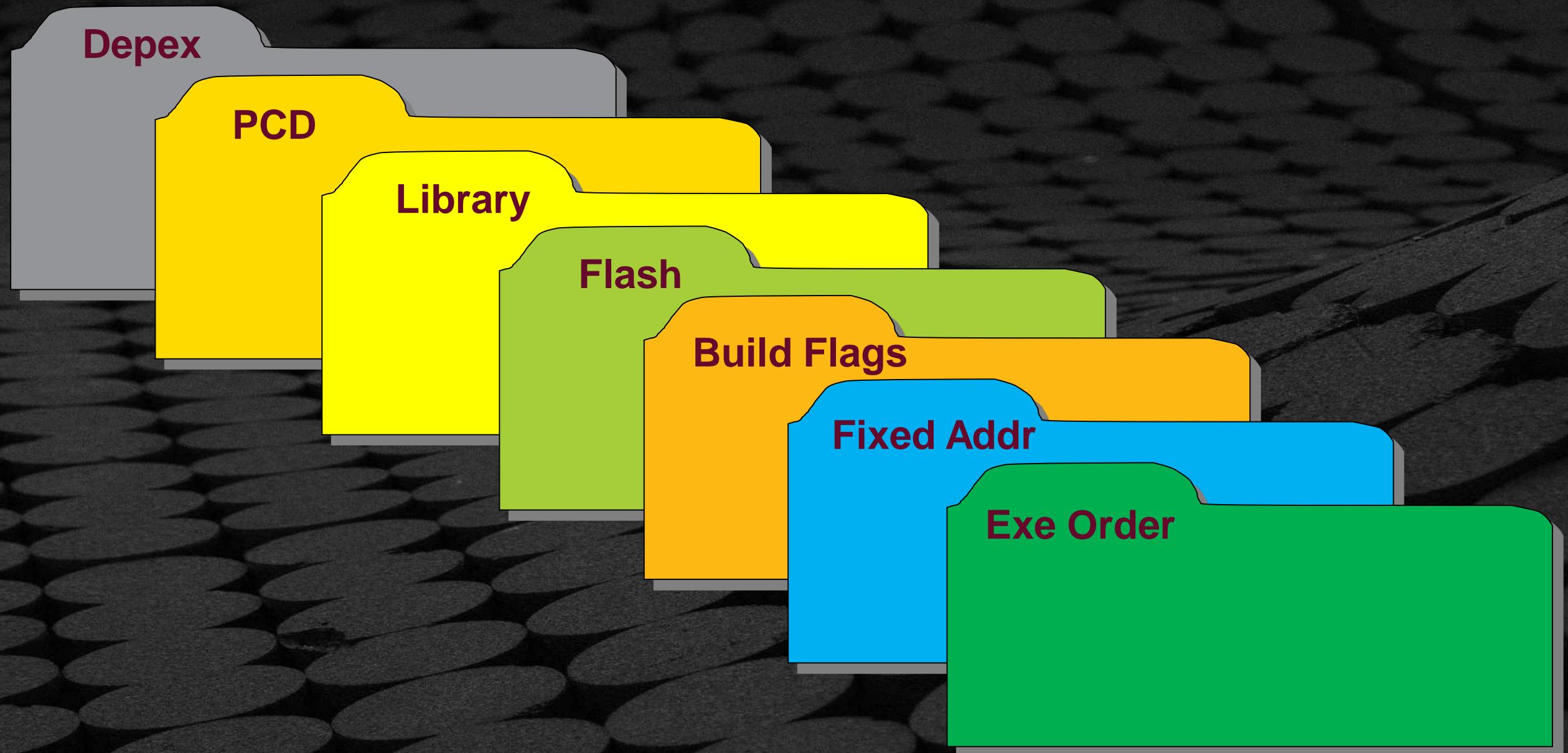
- version show program's version number and exit
- h, --help show this help message and exit
- a TARGETARCH, --arch=TARGETARCH
ARCHS is one of list: IA32, X64, IPF, ARM or EBC,
which overrides target.txt's TARGET_ARCH definition
To specify more archs, please repeat this option.
- p PLATFORMFILE, --platform=PLATFORMFILE
Build the platform specified by the DSC file name
argument, overriding target.txt's ACTIVE_PLATFORM
definition.
- m MODULEFILE, --module=MODULEFILE
Build the module specified by the INF file name
argument.

bash\$ Build -h

build -h

https://gitpitch.com/Laurie0131/EDK_II_Build_Process_Pres_2/master#/36/2

USING BUILD -Y COMMAND



```
build -Y PCD -y pcd.log
```

USING BUILD -Y FOR REPORTS

- Scroll through examples of reports from the Build -Y commands
- [Link](#) to on line presentation

https://gitpitch.com/Laurie0131/EDK_II_Build_Process_Pres_2/master#/37/2

https://gitpitch.com/Laurie0131/EDK_II_Build_Process_Pres_2/master#/37/3

build -Y LIBRARY

https://gitpitch.com/Laurie0131/EDK_II_Build_Process_Pres_2/master#/37/4

build -Y FLASH

https://gitpitch.com/Laurie0131/EDK_II_Build_Process_Pres_2/master#/37/5

build -Y BUILD_FLAGS

https://gitpitch.com/Laurie0131/EDK_II_Build_Process_Pres_2/master#/37/6

build -Y FIXED_ADDRESS

https://gitpitch.com/Laurie0131/EDK_II_Build_Process_Pres_2/master#/37/7

build -Y EXECUTION_ORDER

https://gitpitch.com/Laurie0131/EDK_II_Build_Process_Pres_2/master#/37/8

Local Report.html is generated on the host build machine - pop up this in the Browser window.

Link: [Link to Report.html on local machine](#)

BUILD TOOL BINARIES

Utility	Description
Build.exe	Tool is written in Python and calls AutoGen.exe, then it calls \$(MAKE) –f Makefile.out, and finally, it calls GenFds.exe
EfiRom.exe	used to build an option ROM image
PatchModule.exe	used to patch a binary module that has a PCD of type PATCHABLE_IN_MODULE
PatchPlatform.exe	used to modify either the PCD Database or the VPD settings in a flash device image

SUMMARY

- ★ Define EDK II
- ★ Describe EDK II's elements including file extensions, directories, modules, packages, and libraries
- ★ Explain the EDK II build process
- ★ Explain the Build tools

Questions?





tianocore

