### Makefiles

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**COP 3503** 

- Introduction
- Pitfalls of Last Lab
- This Lab
- Wrapping Up



### Agenda

- Talk about some of the recurring problems from last lab
- Makefiles



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#### Return values

If you have a function that does some useful work and then
returns a value you need (for instance, whether the work was
successful), then when you want to use the result, don't call the
function multiple times to check it.

```
set.insert("chocolate");
if(!set.insert("chocolate")){
  cout << "Error, chocolate exists..." << endl;
}</pre>
```

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returns a value you need (for instance, whether the work was
successful), then when you want to use the result, don't call the
function multiple times to check it.

```
set.insert("chocolate");
if(!set.insert("chocolate")){
  cout << "Error, chocolate exists..." << endl;
}</pre>
```

#### Instead do:

```
bool success = set.insert("chocolate");
if(!success){
  cout << "Error, chocolate exists..." << endl;
}</pre>
```

### Returns necessary?

- A lot of times, functions don't need to return values at all.
- You should make these functions void
- For example, in our last lab, we needed to write a function that set a parameter instead of returning a value. It should be void.
- How do you return from a void function?

```
void fun_1(int & control){
//...
if(/*something bad happens*/){
control = 0;
return;
}
//...
}
```

### Code after return

```
void fun_2() {
    //Useful code
    return;
    int a = 0;
}
```

- Is line 4 ever executed?
- Don't put code after returns.

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# Review of Separate Compilation

- Idea is to put function and class declarations in something called a header (or .h) file. Then, you include that file in your .cpp where you define your functions (or classes).
- This allows you to separate your code into logical units that you can then compile separately.
- Allows you to have code reusability and portability



### An Example

See website, done in lab



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#### ifndef

- The ifndef directive should be added to all of your .h files to prevent multiple inclusion.
- Syntax looks likes:

```
#ifndef FILENAME_H_1234
#define FILENAME_H_1234

//Useful .h code here

#endif
```

### **Makefiles Motivation**

- Make is a commad done from the command line (linux) that looks for and "executes" a makefile
- A makefile is basically just a sequence of commands that accomplish something. Normally, this is compilation of some large-ish (more than one file) project.
- It also keeps track of when dependencies change, and only compiles what needs to be compiled.
- Makes the compilation process for large projects so much simpler
- If you use an IDE, it automatically generates a makefile (it just hides it from you). You will write your own today.

# A simple example

```
# Makefile example
all:
g++ main.cpp count.cpp -o count
```

- all is called a target. You can tell make to build specific targets. By default, it builds the first one in the file.
- The line following the targets MUST BEGIN WITH A TAB CHARACTER
- You don't just have to put g++ commands, any valid command line command will work fine. You can also do multiple lines.

## **Multiple Targets**

```
all: count
2
 count: main.o count.o
      q++ -o count main.o count.o
4
6 main.o: main.cpp count.h
     g++ -c main.cpp
 count.o: count.cpp count.h
10
     g++ -c count.cpp
 clean:
     rm -rf *.o
      rm -rf count
14
```

### Variables or macros

```
1 \mid CXX = q++
2 CXXFLAGS = -c -Wall
3 LDFLAGS =
4
5 all: count.
 count: main.o count.o
      $(CXX) $(LDFLAGS) -o count main.o count.o
10 main.o: main.cpp count.h
      $(CXX) $(CXXFLAGS) main.cpp
11
12
13 count.o: count.cpp count.h
      $(CXX) $(CXXFLAGS) count.cpp
14
 clean:
     rm -rf *.o count
17
```

# Getting more fancy...

```
1 \mid CXX = q++
2 | CXXFLAGS = -c - Wall
3 LDFLAGS =
4 SRCS = main.cpp count.cpp
5 \mid OBJS = \$ (SRCS:.cpp=.o)
6 DEPS = count.h
7 EXEC = count
 all: $(EXEC)
11 $ (EXEC): $ (OBJS)
      $(CXX) $(LDFLAGS) $(OBJS) -0 $@
12
14 %.o: %.cpp $ (DEPS)
      $(CXX) $(CXXFLAGS) $<
15
17
 clean:
      rm -rf *.o count
18
```

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### Questions

???

