

Name: Ngô Tiến Tú

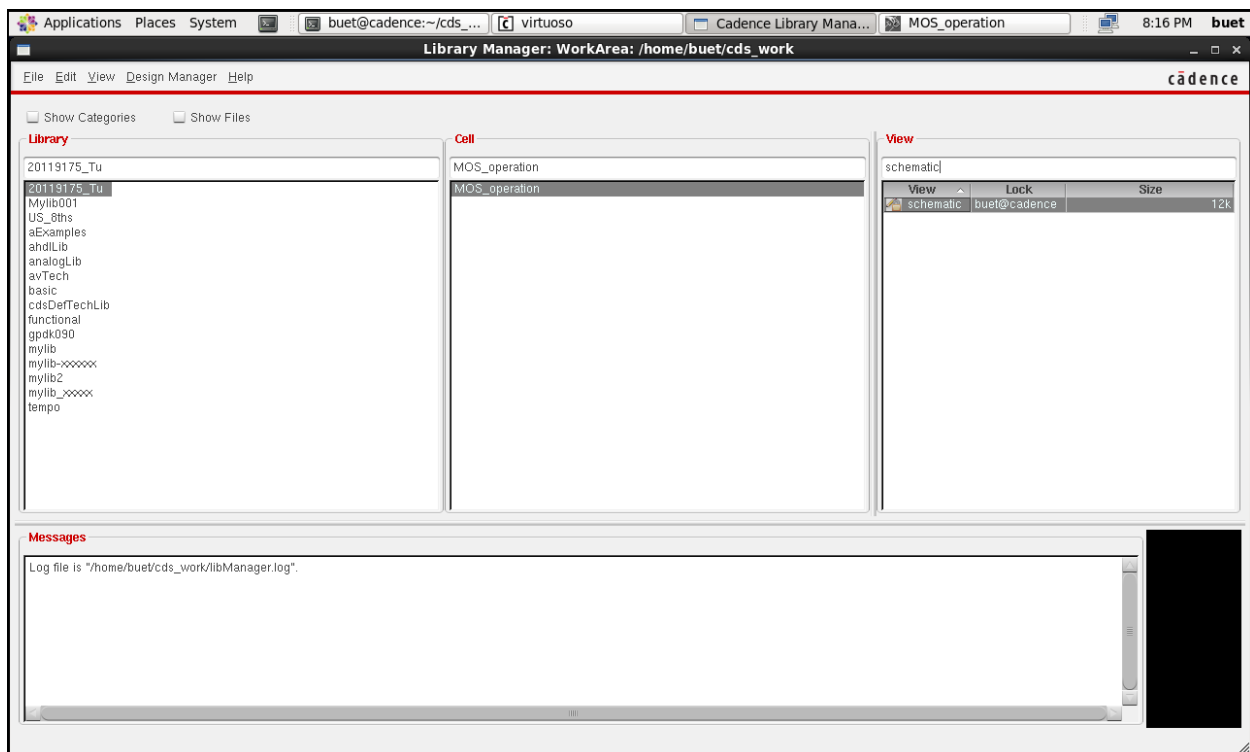
ID: 20119175

## EXPERIMENT NO.1

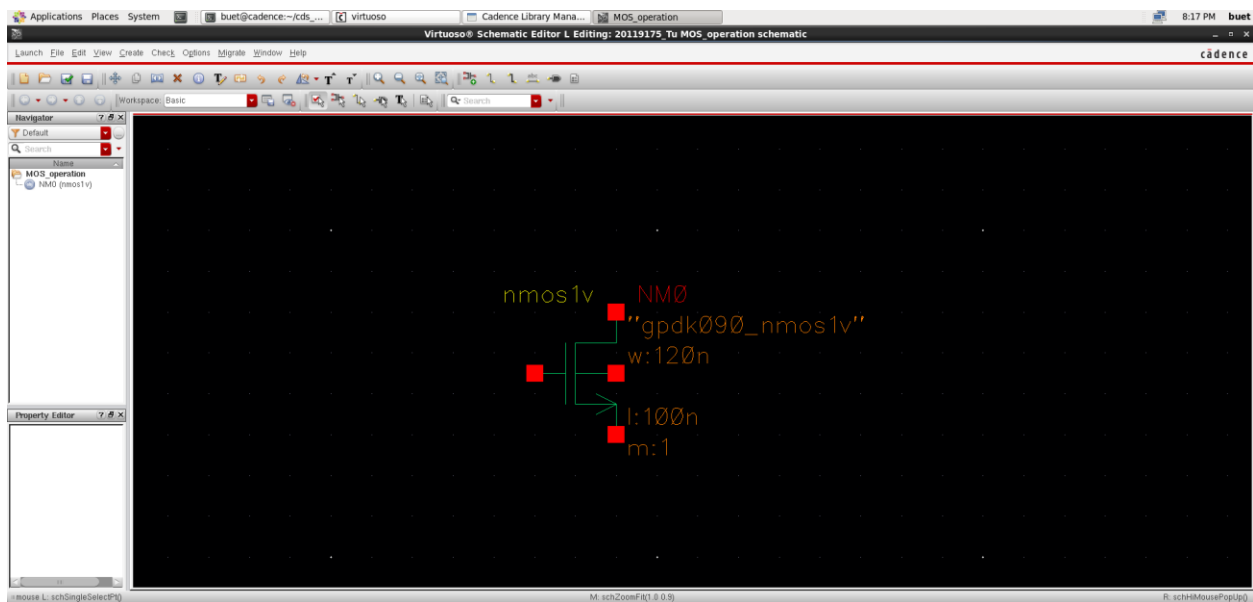
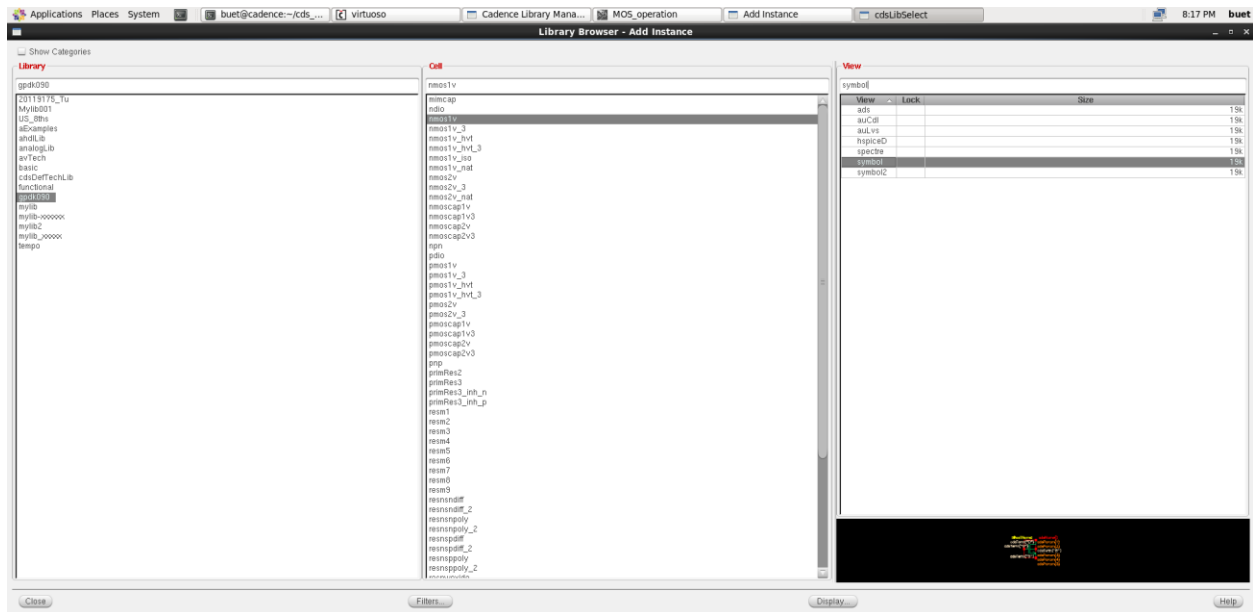
First, open the terminal, go to the cds\_work directory with the command "cd\_cds\_work". Then, open the cadence virtuoso software with the command "virtuoso". A menu appears, select tools in the toolbar and select Library Manager... Then the program will initialize as shown below.

To create a library, you choose file -> new -> Library. Next, give the library a name -> click apply -> Select Technology File for New Library -> Attach to an existing technology library -> Attach Library to Tech Library -> gpdk090.

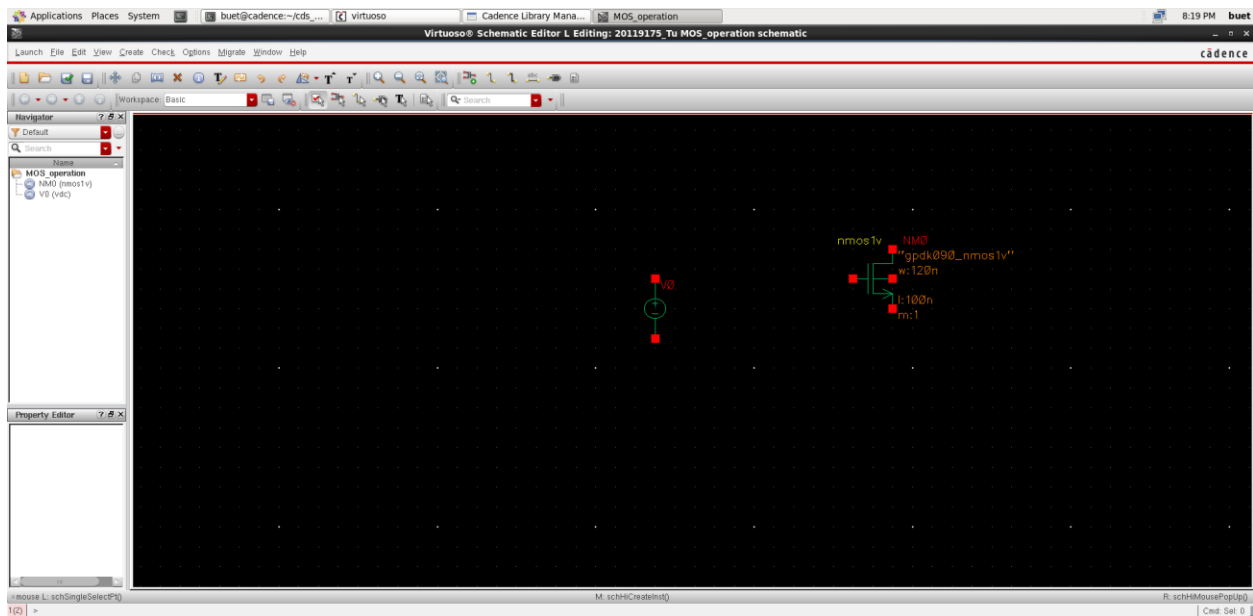
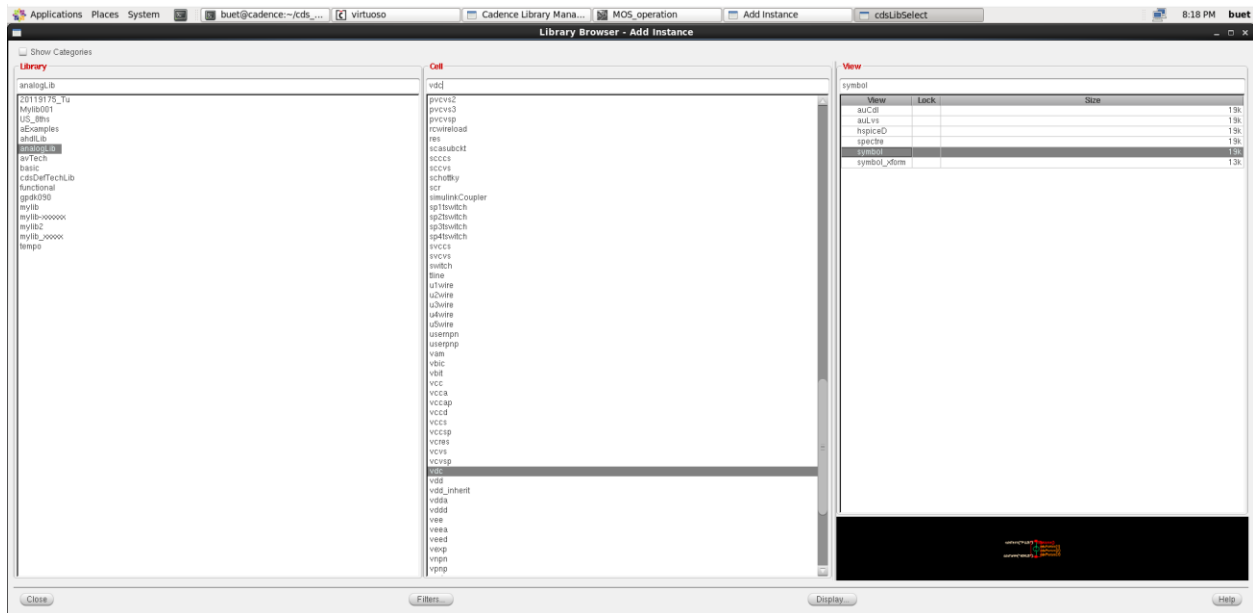
To create Simulation Design, choose File -> New -> Cell View -> New File -> Name the file in the Cell box.



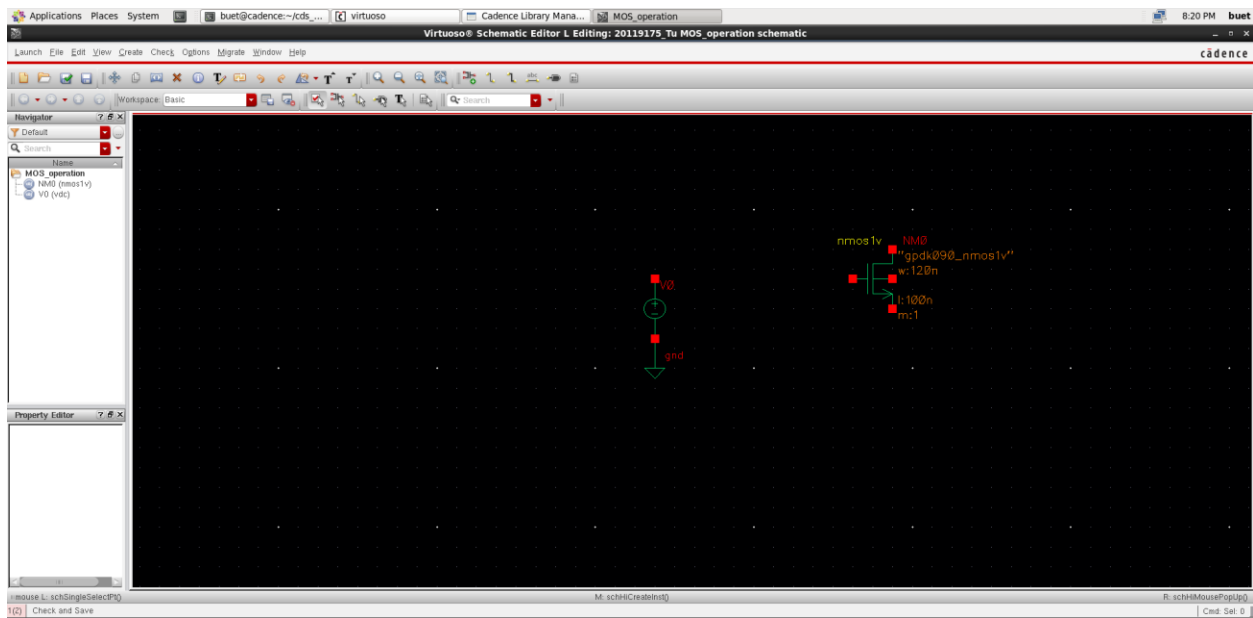
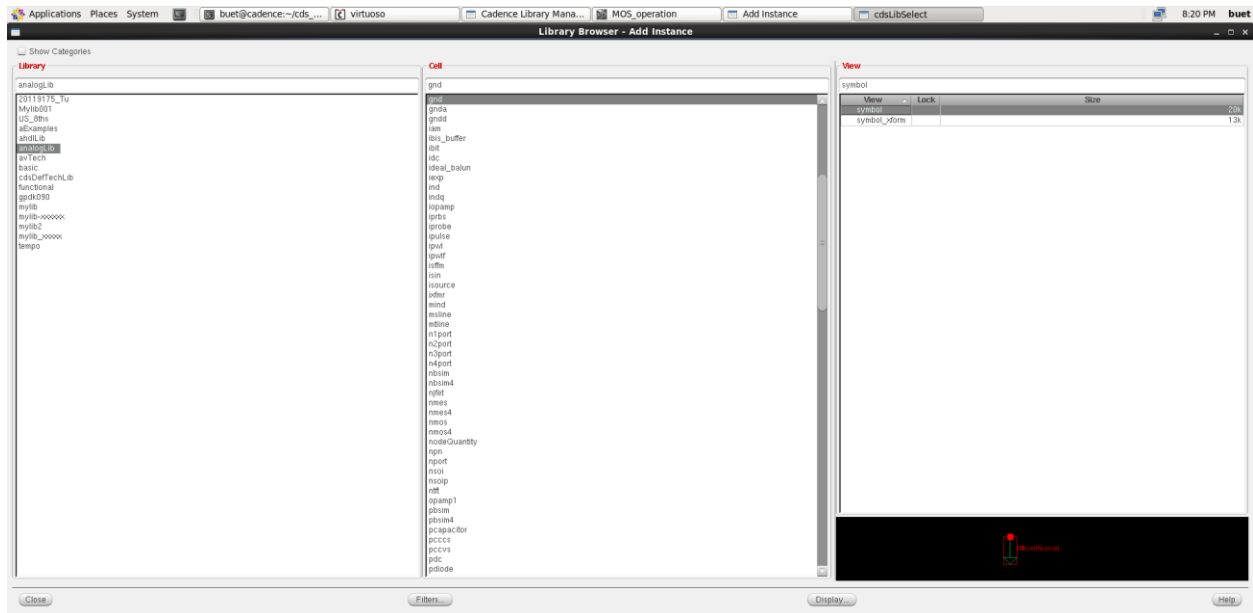
Then we press I to get components, click browse -> point to gpd090. First, we have to get the symbol of nmos here.



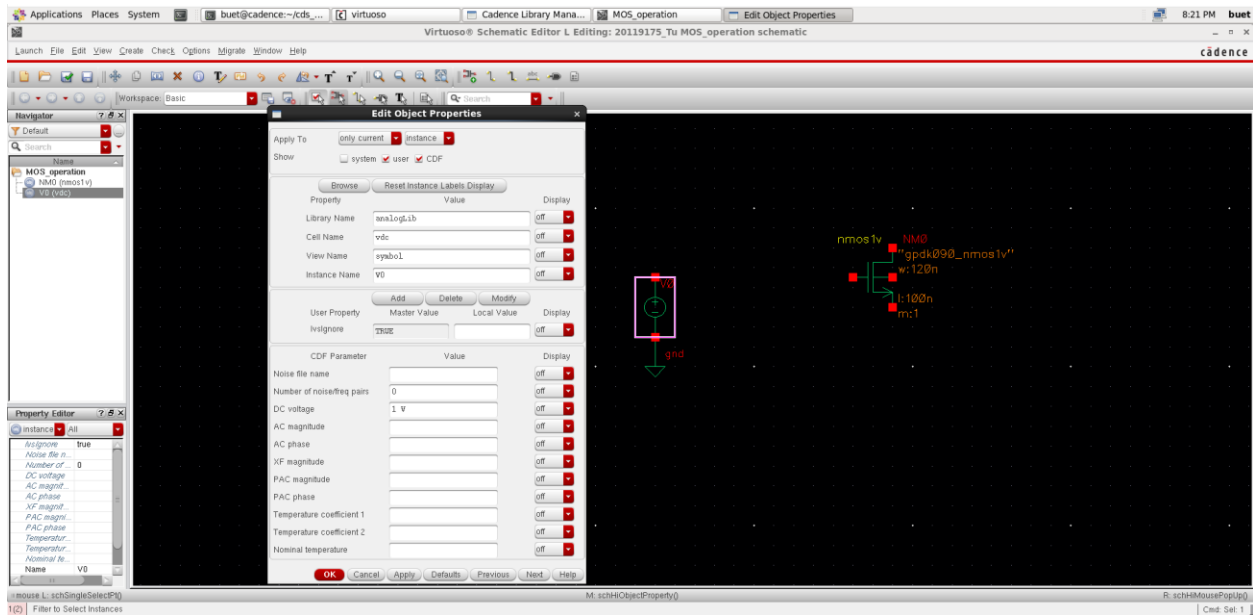
Then we get the vdc source from analogLib library.



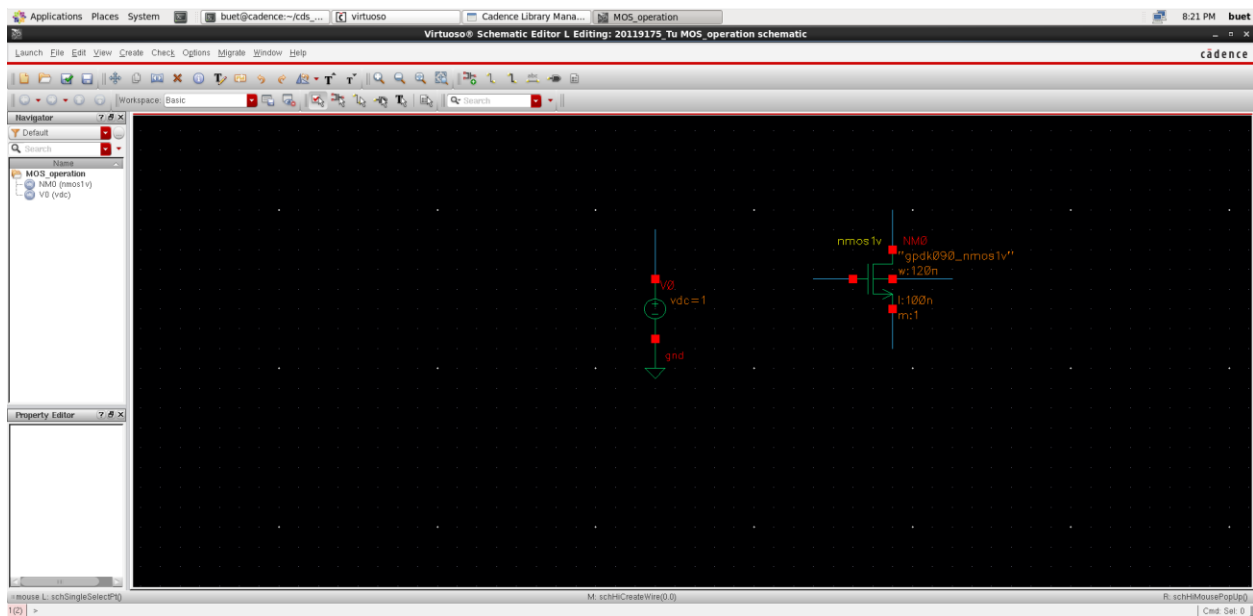
Take gnd to attach to create voltage for vdc source.



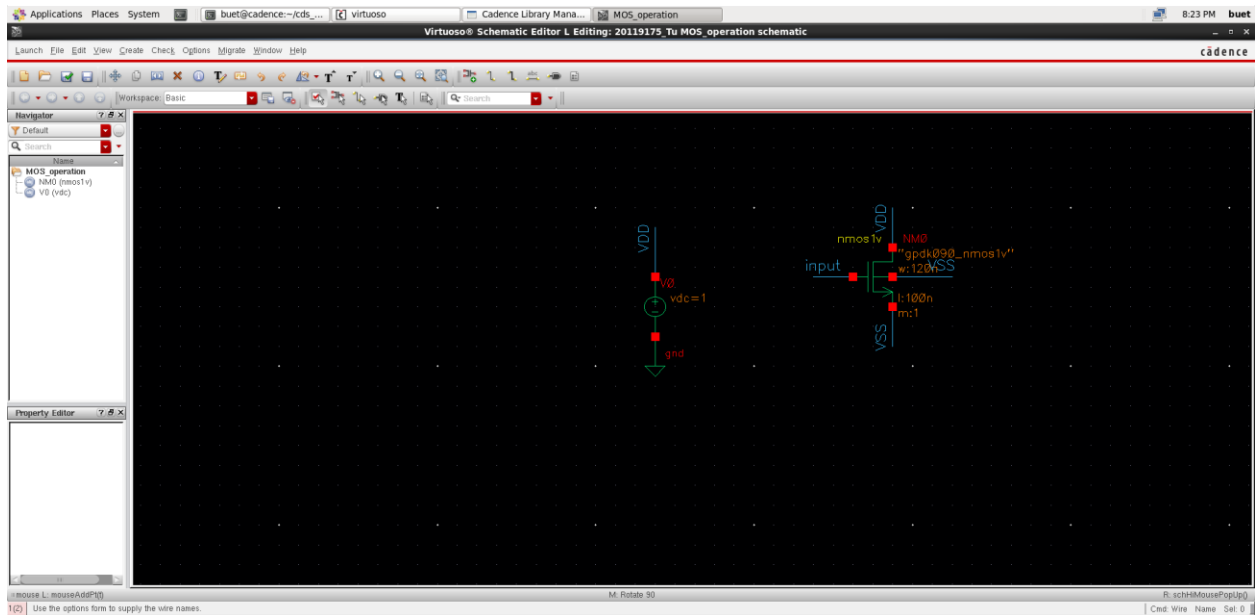
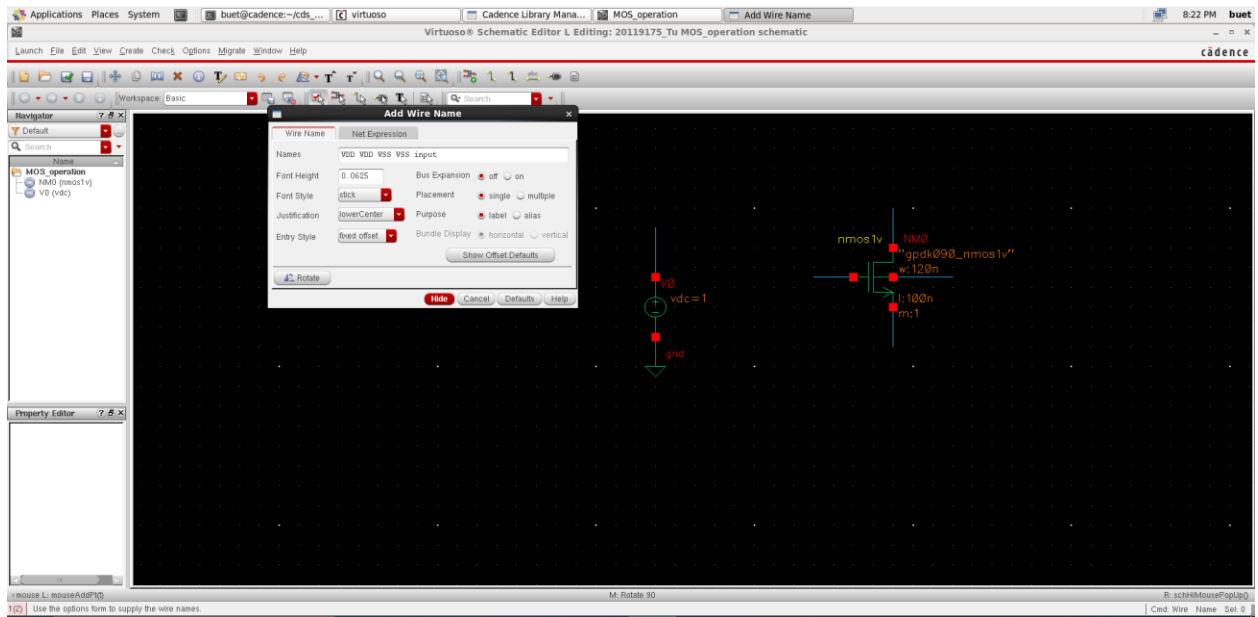
Set the value of the source vdc to 1V.



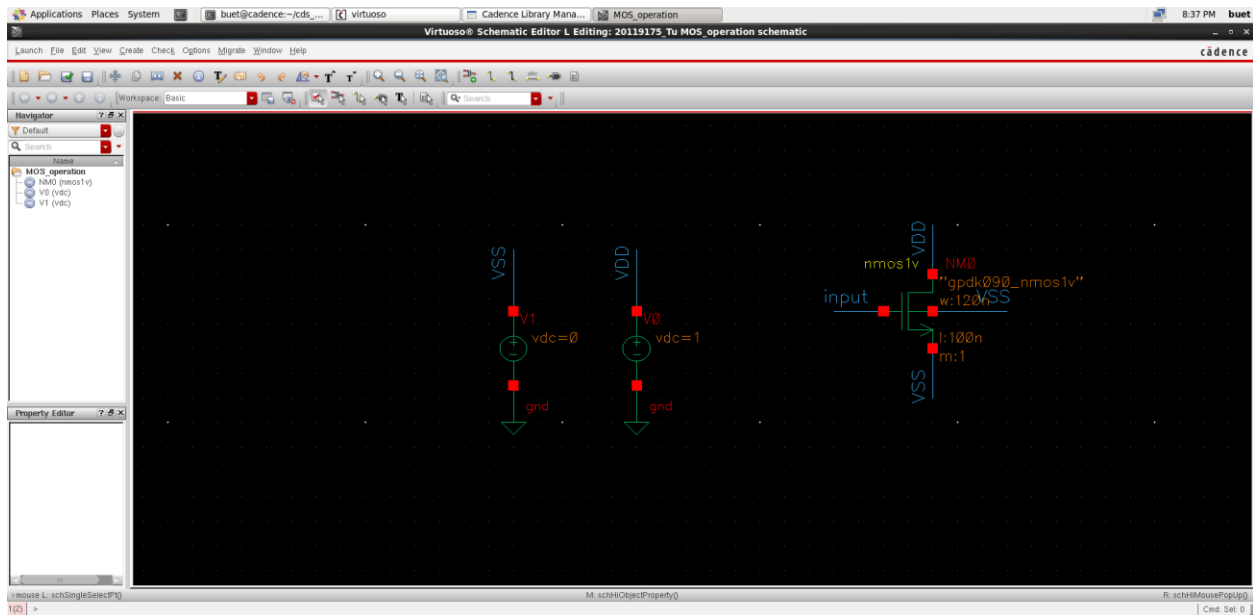
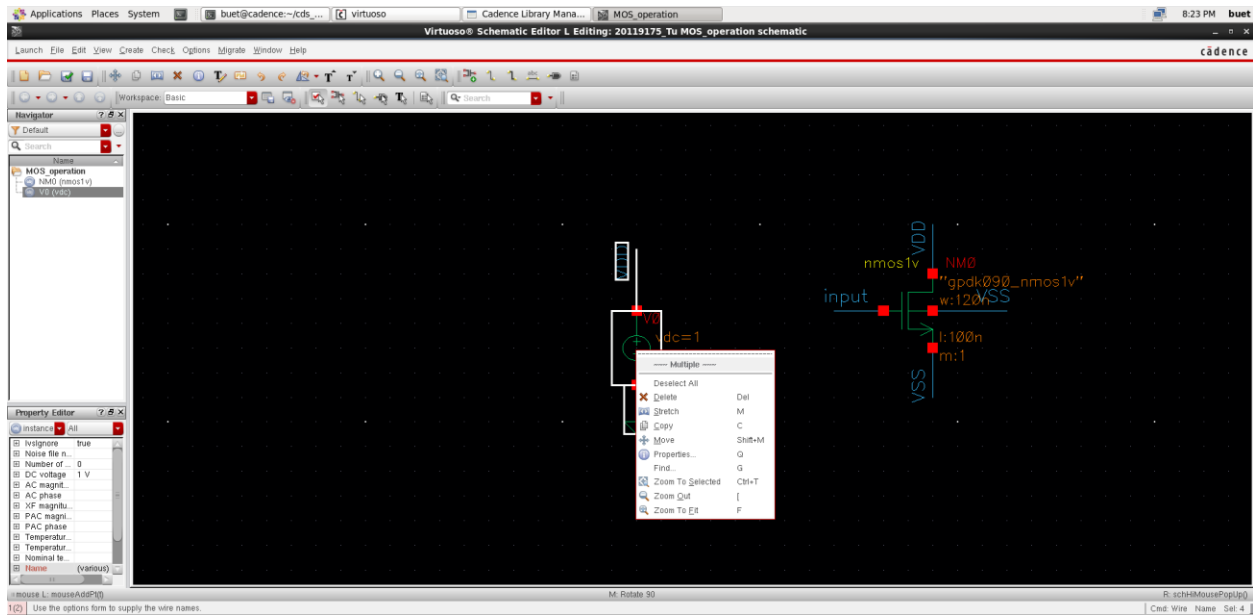
Wire between terminals of nmos and source.



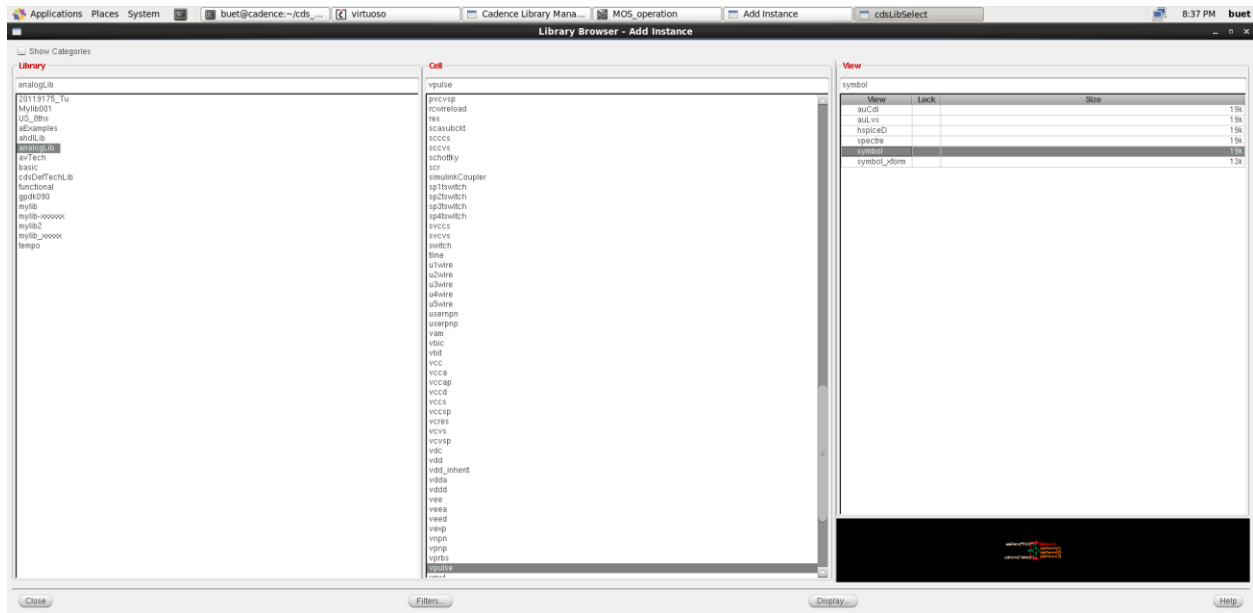
Name the labels for the common junctions.



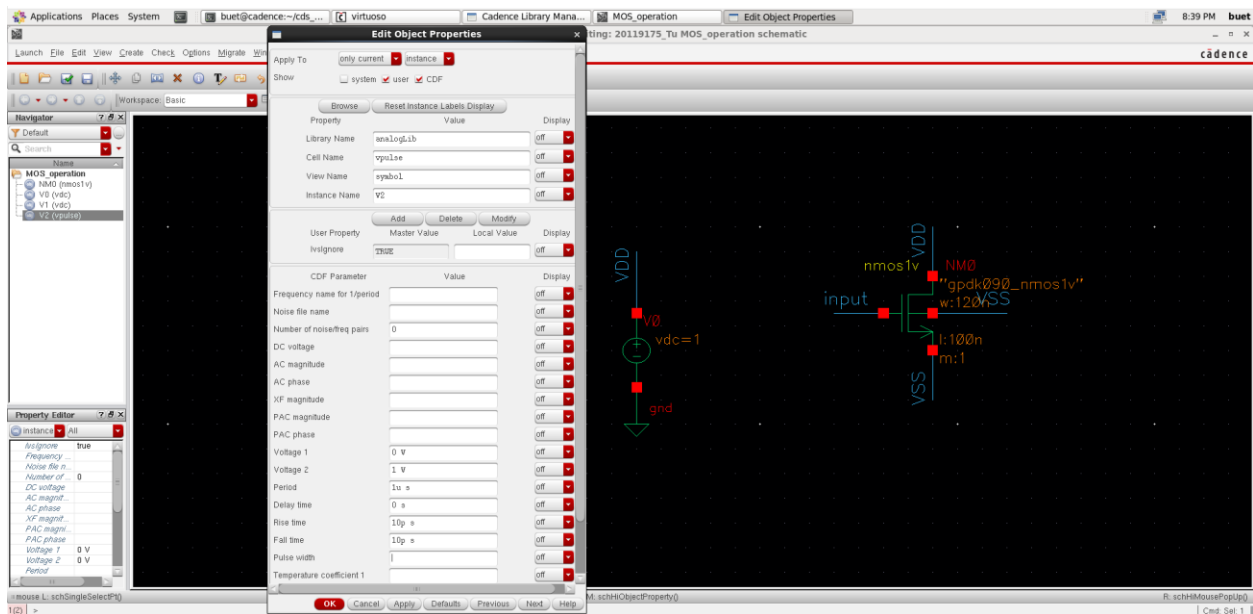
Copy the source vdc, change the value to 0V to create VSS.



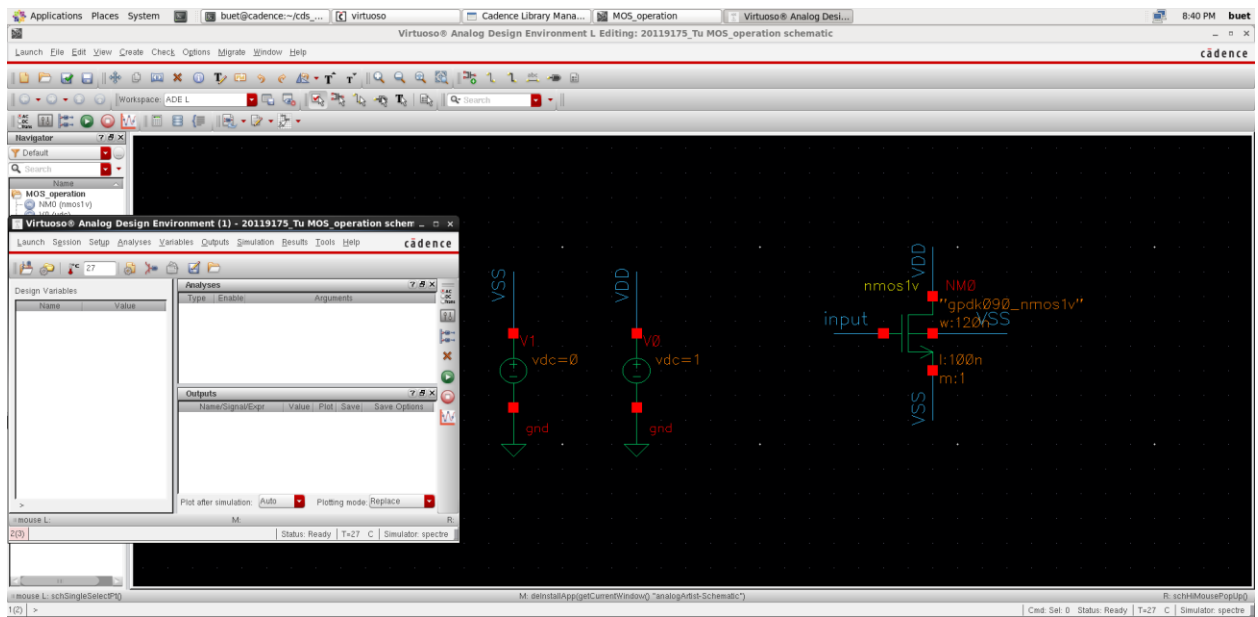
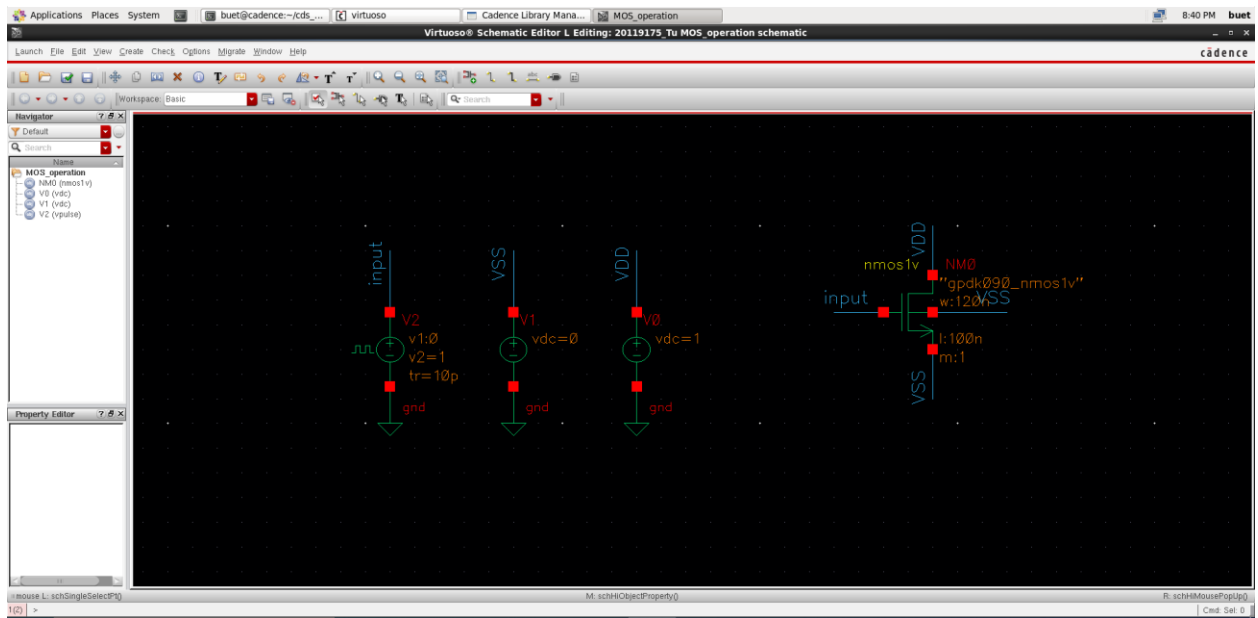
Get pulse source to create input.



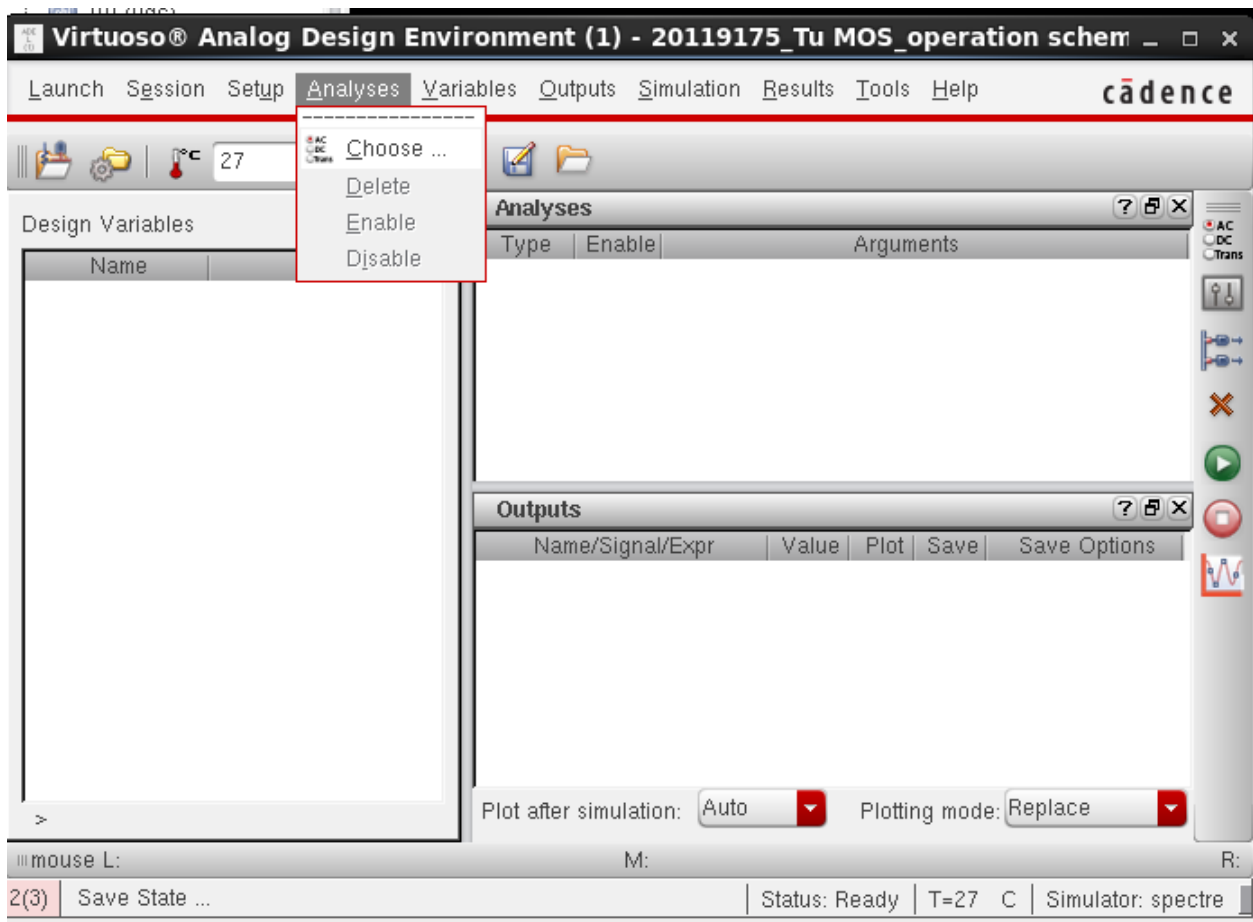
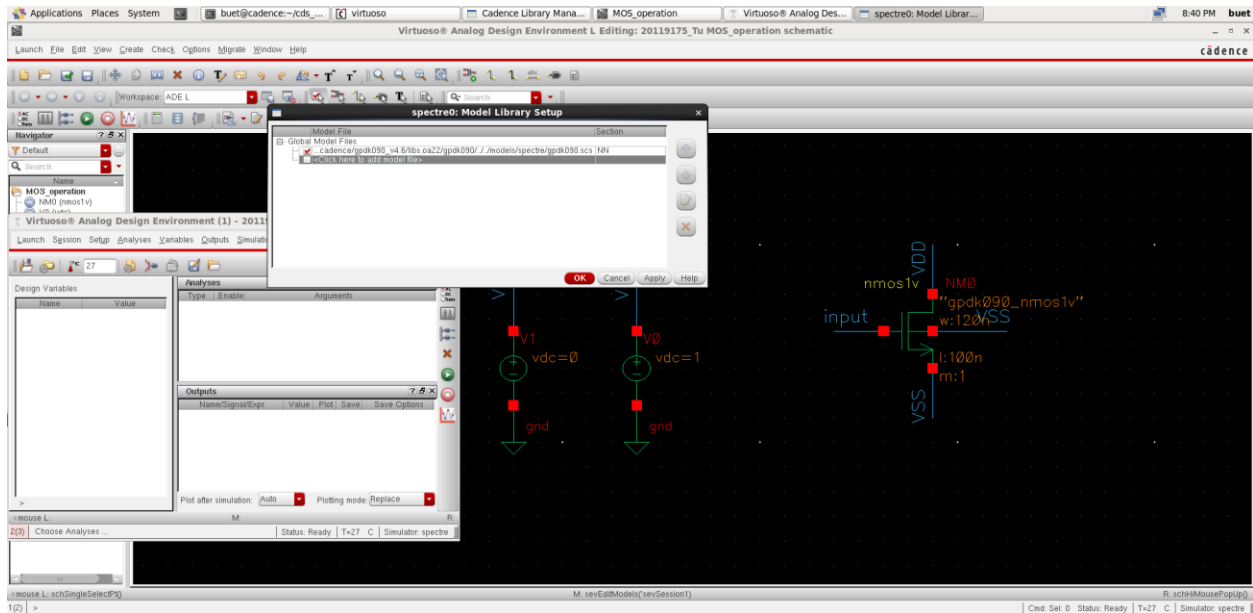
Initialize the values for the pulse source as shown below.







Check setup library for circuit simulation.



Choose simulation time to be 1 microsecond.

**Choosing Analyses -- Virtuoso® Analog Design**

Analysis

<input checked="" type="radio"/> tran	<input type="radio"/> dc	<input type="radio"/> ac	<input type="radio"/> noise
<input type="radio"/> xf	<input type="radio"/> sens	<input type="radio"/> dcmatch	<input type="radio"/> stb
<input type="radio"/> pz	<input type="radio"/> sp	<input type="radio"/> envlp	<input type="radio"/> pss
<input type="radio"/> pac	<input type="radio"/> pstb	<input type="radio"/> pnoise	<input type="radio"/> pxf
<input type="radio"/> psp	<input type="radio"/> qpss	<input type="radio"/> qpac	<input type="radio"/> qpnoise
<input type="radio"/> qpxf	<input type="radio"/> qpasp	<input type="radio"/> hb	<input type="radio"/> hbac
<input type="radio"/> hbnoise	<input type="radio"/> hbasp		

Transient Analysis

Stop Time

Accuracy Defaults (errpreset)

☐ conservative ☐ moderate ☐ liberal

☐ Transient Noise

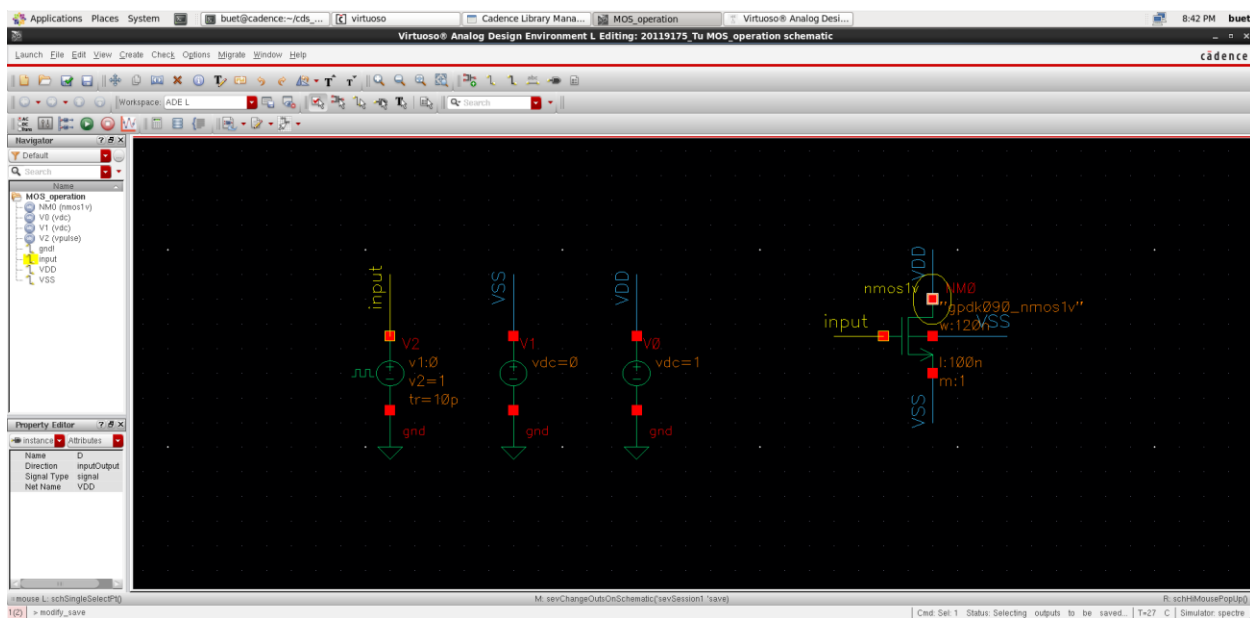
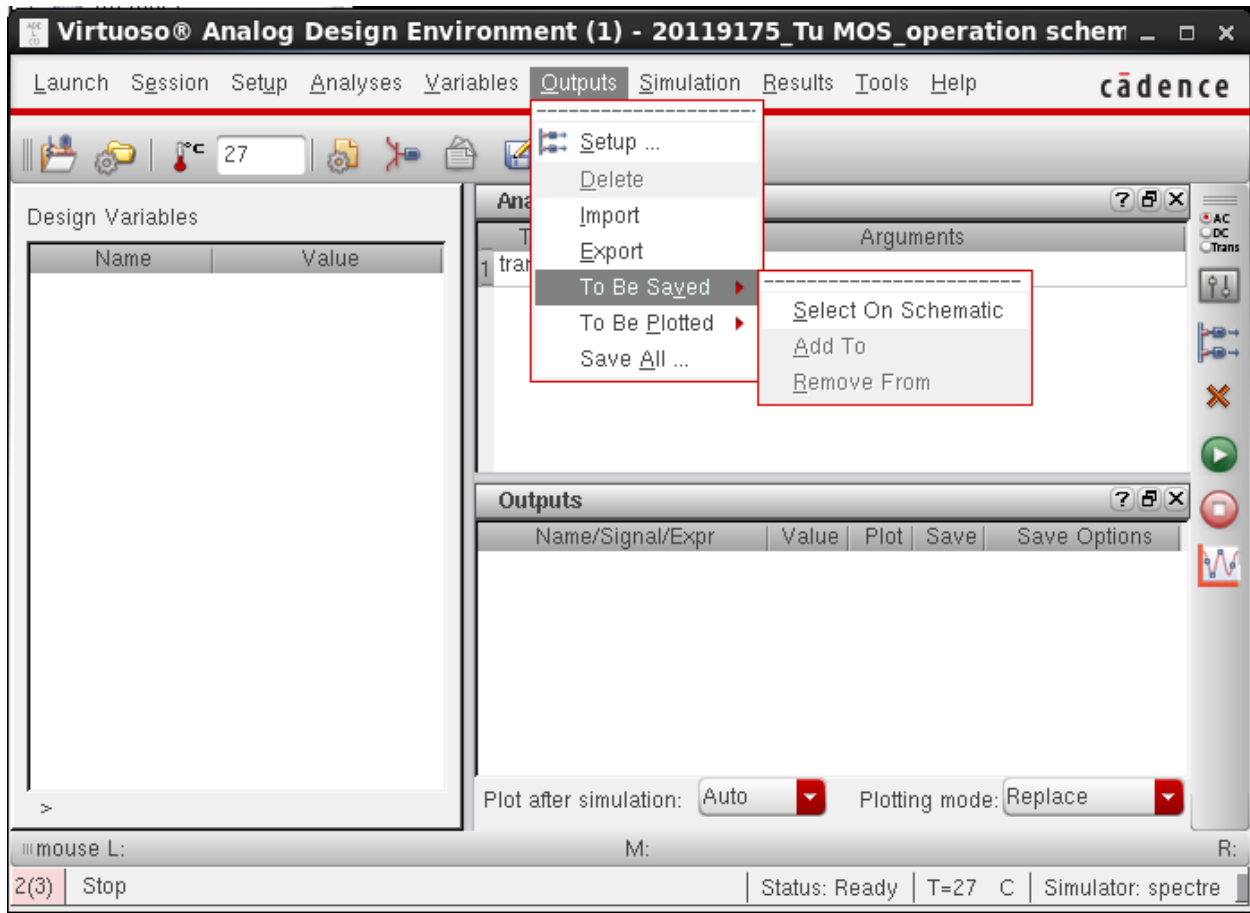
☐ Dynamic Parameter

Enabled ☐

Options...

**OK** Cancel Defaults Apply Help

Select wires to draw voltage and points to draw current in simulation.



Press run. And the following figures are the result of simulation.

