bus that connects major computer components (processor, memory, V/O) is called a \_\_\_\_

system bus

The System bus is made up of \_\_\_

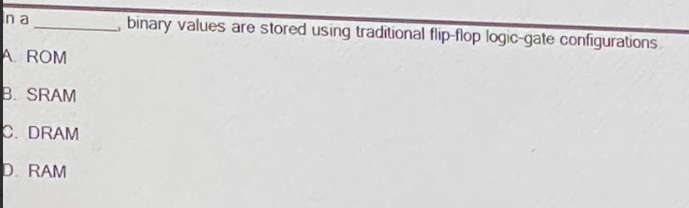
Control bus, Data bus and Address bus.

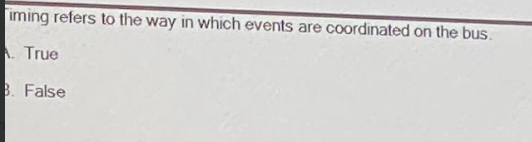
Data are transferred to and from the disk in \_\_\_\_

Sectors

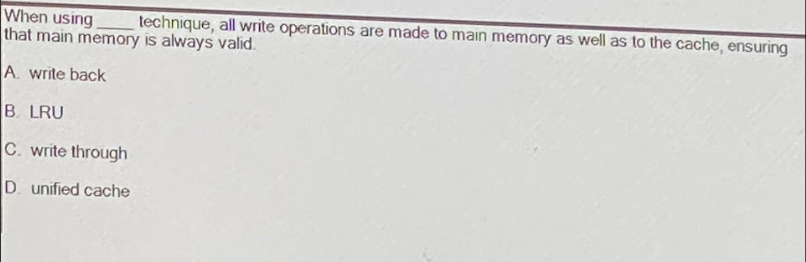
The unary operation \_\_\_ inverts the value of its operand.

NOT

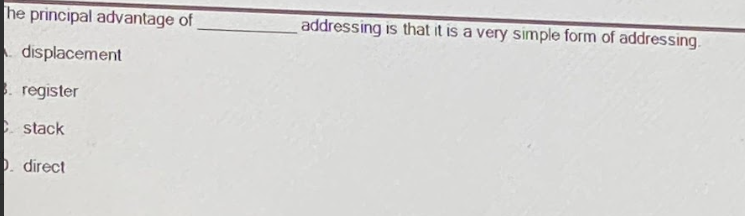
 B. SRAM



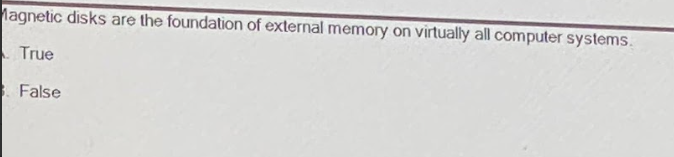
True



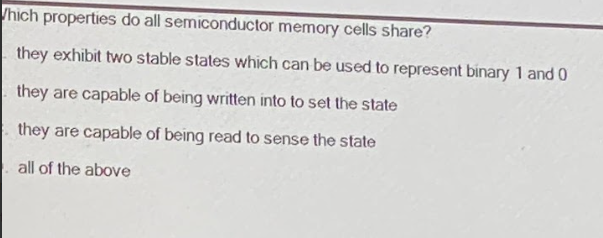
1. write through.



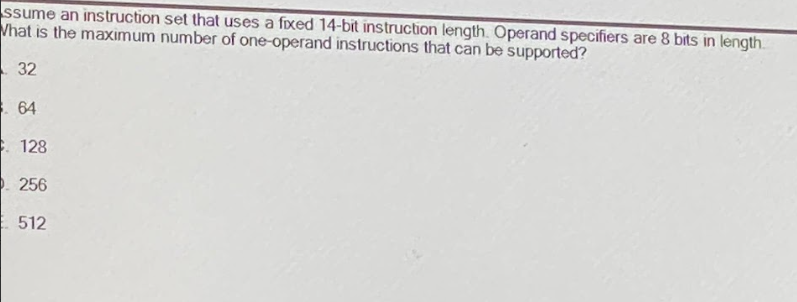
1. direct.



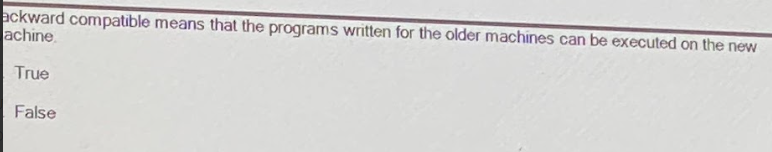
1. True



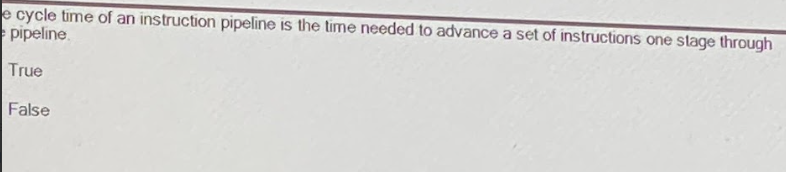
D/ all of the above.



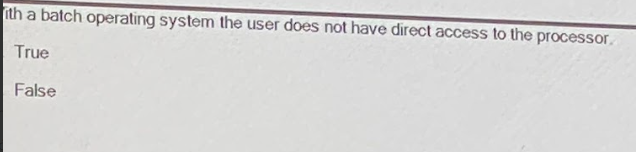
D/ 256.



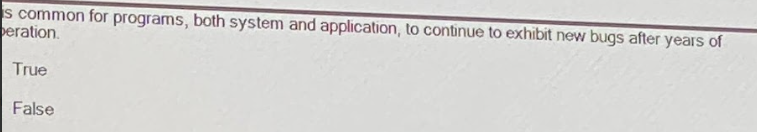
A/ true



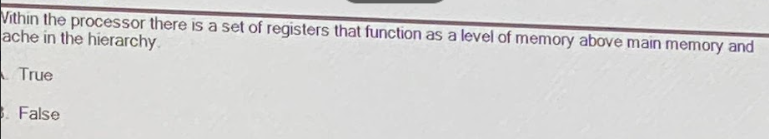
True



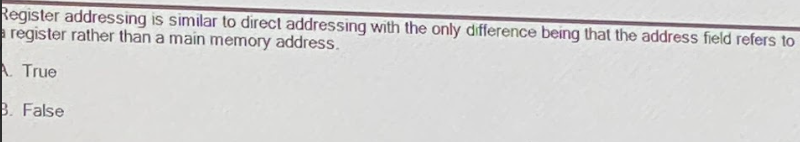
True



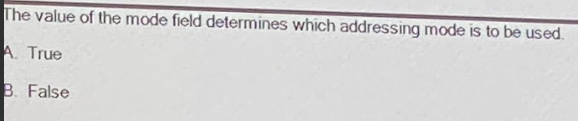
True



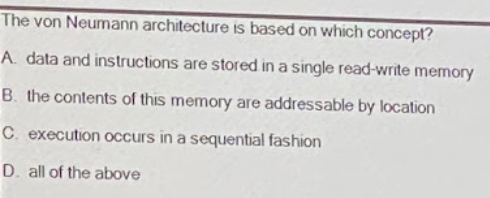
True



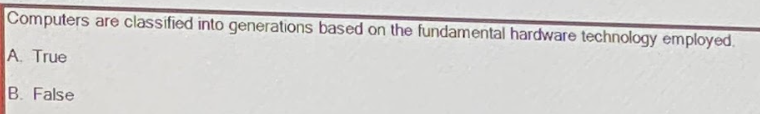
True



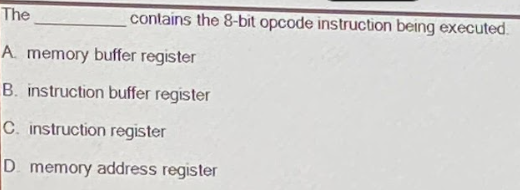
True



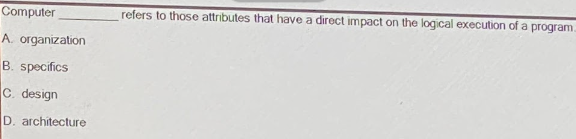
D/ all of the above



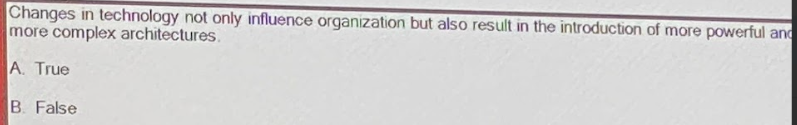
1. True



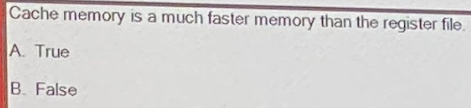
C/ instruction register.



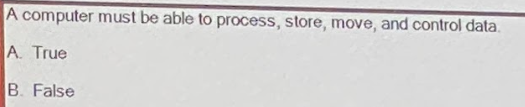
D/ architecture.



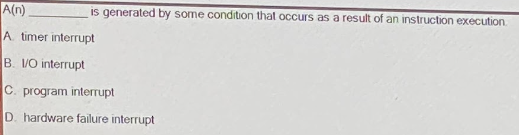
 A. True.



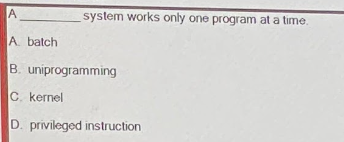
B. False.



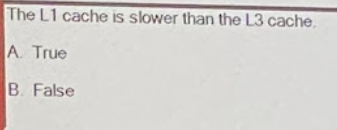
A. True.



program interrupt



uniprogramming



B. False.