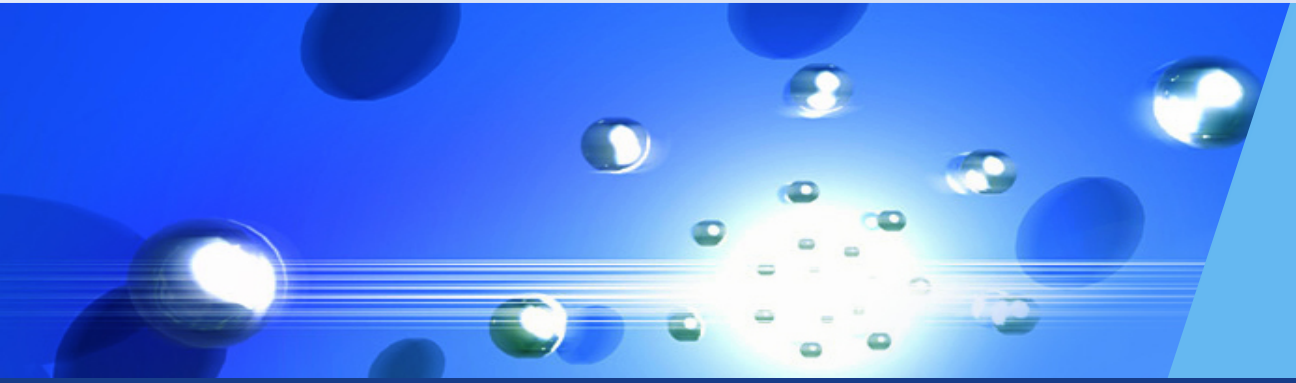
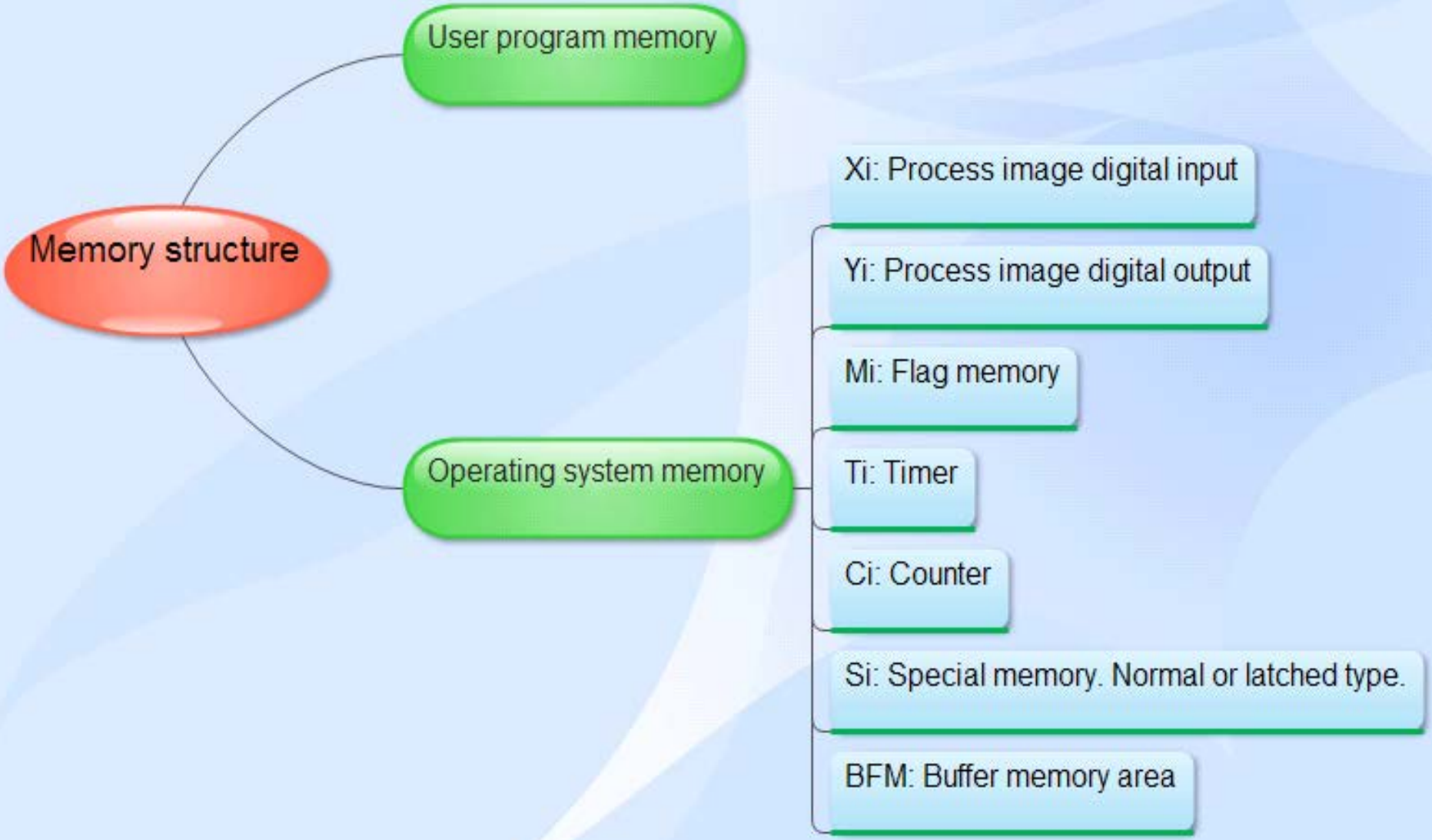
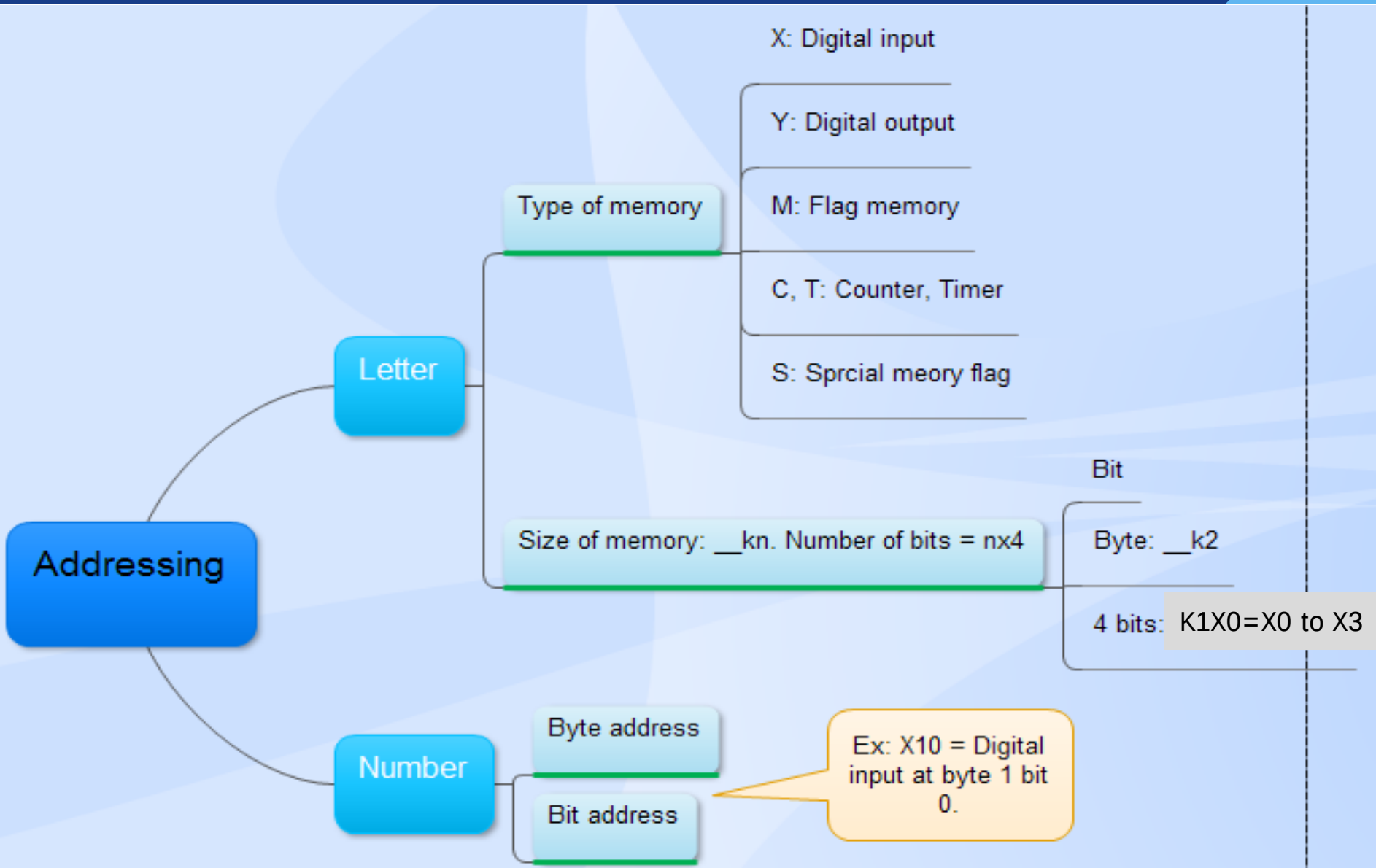


Chapter 4: Memory System and I/O Interaction

PLC Memory Structure



Addressing


Addressing
Letter
Type of memory
X: Digital input
Y: Digital output
M: Flag memory
C, T: Counter, Timer
S: Sprcial meory flag
Size of memory: __kn. Number of bits = nx4
Bit
Byte: __k2
4 bits: K1X0=X0 to X3
Number
Byte address
Bit address
Ex: X10 = Digital input at byte 1 bit 0.

Addressing

Quiz:

- **Define the following address:**
  - **X26, Y15**
  - **M32**
  - **K1X10, K2X0, K4M100**
- **Write the address of following:**
  - **Byte 5, bit 2 digital input**
  - **Point 200 flag memory area**
  - **From M150 to M167**